Surface Mount

High Pass Filter

SCHF-31+

50Ω 31 to 1500 MHz

CASE STYLE: YY161

The Big Deal

- Low insertion loss
- High rejection
- Small size, 0.75 x 0.38 x 0.28”

Product Overview

SCHF-31+ is a 50Ω high pass filter fabricated using SMT technology. This high pass filter covers from 31-1500 MHz. This filter is built with high Q capacitors and wire wound inductors for superior performance. It has repeatable performance across lots and consistent performance across temperature. The unit comes housed in a miniature plastic package measuring just 0.75 x 0.38 x 0.28”, ideal for dense circuit board layouts.

Key Features

Feature	Advantages
Low insertion loss	Can be used in high performance applications.
Good rejection	This enables the filter to attenuate spurious signals and reject harmonics for broad frequency band.
Small size, 0.75" x 0.38" x 0.28"	Accommodates tight space requirements for dense PCB layouts.

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SCHF-31+

CASE STYLE: YY161

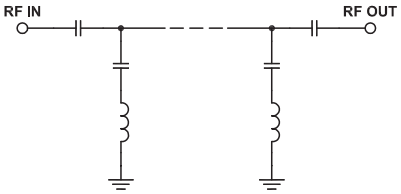
Features

- Low Insertion loss
- High Rejection
- Small size, 0.75 x 0.38 x 0.28"

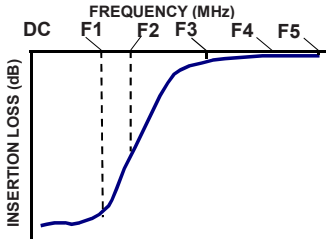
Applications

- SATCOM
- Broadband fiber networks
- CATV
- Radio communications
- Receivers / transformer

Functional Schematic



Typical Frequency Response



+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Electrical Specifications at 25°C

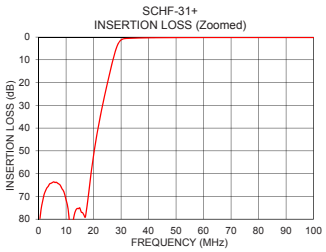
Parameter		F#	Frequency (MHz)	Min.	Typ.	Max.	Unit
Stop Band	Rejection Loss	DC-F1	DC-20.5	40	50	-	dB
		F1-F2	20.5-23.5	20	26	-	dB
	VSWR	DC-F1	DC-23.5	-	20	-	:1
Pass Band	Insertion Loss	F3-F4	31-1000	-	0.5	1.0	dB
		F4-F5	1000-1500	-	0.9	1.5	dB
	VSWR	F3-F5	31-1500	-	1.3	-	:1

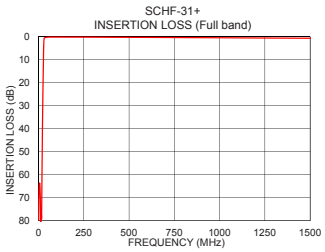
Maximum Ratings	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
RF Power Input	5W Max. @ 25°C

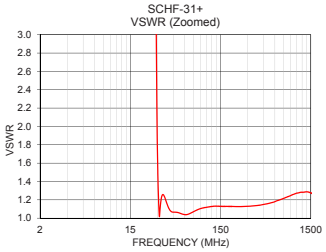
Permanent damage may occur if any of these limits are exceeded.

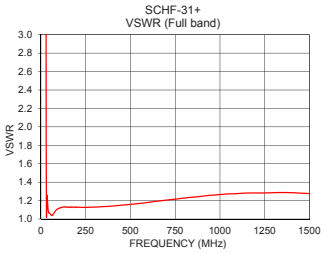
Typical Performance Data at 25°C

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)
1.0	75.00	1527.77
5.0	63.94	2310.54
20.5	48.01	140.05
23.0	31.52	84.16
23.5	28.62	74.50
24.0	25.82	64.53
25.0	20.41	46.43
27.0	10.26	15.44
28.0	5.89	7.03
28.5	4.14	4.66
29.0	2.79	3.16
30.0	1.25	1.69
31.0	0.74	1.13
35.0	0.47	1.25
50.0	0.23	1.06
250.0	0.21	1.13
500.0	0.29	1.16
1000.0	0.48	1.27
1250.0	0.60	1.28
1500.0	0.69	1.28









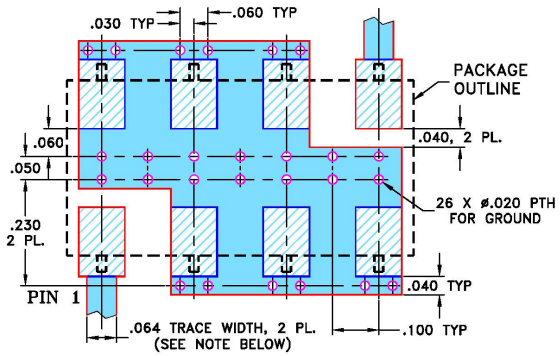
Notes

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

Pad Connections

INPUT	1
OUTPUT	8
GROUND	2, 3, 4, 5, 6, 7

Demo Board MCL P/N: TB-187+
Suggested PCB Layout (PL-049)

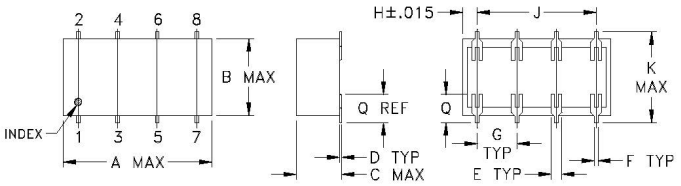


NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .030" ± .002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

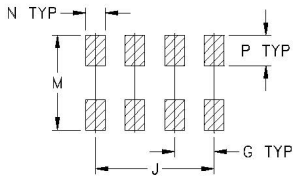
DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

Outline Drawing



PCB Land Pattern



Suggested Layout
Tolerance to be within ±.002

Outline Dimensions (inch / mm)

A	B	C	D	E	F	G	H
.750	**.380**	**.280**	**.010**	**.050**	**.020**	**.200**	**.075**
19.05	9.65	7.11	0.25	1.27	0.51	5.08	1.91

J	K	M	N	P	Q	Wt.
.600	**.450**	**.470**	**.100**	**.150**	**.148**	grams
15.24	11.43	11.94	2.54	3.81	3.76	1.6

Surface Mount High Pass Filter SCHF-31+

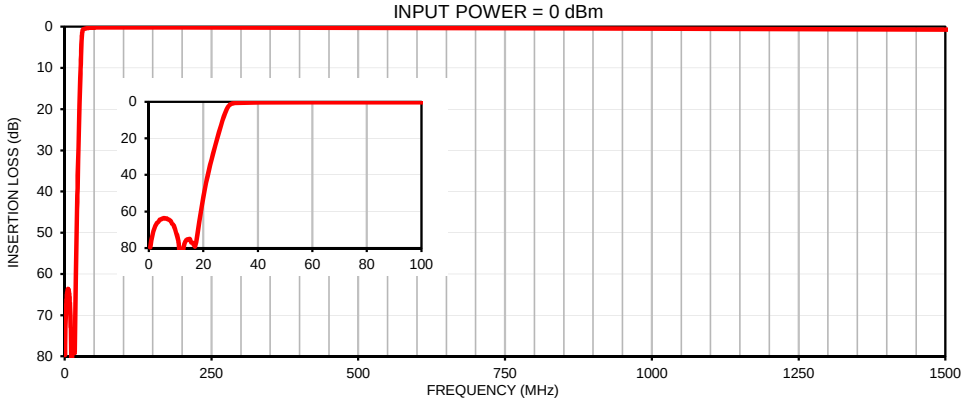
Typical Performance Data

FREQ. (MHz)	Insertion Loss (dB)	Input Return Loss (dB)	Output Return Loss (dB)
1.0	75.00	0.00	0.00
5.0	63.94	0.01	0.01
10.0	71.56	0.02	0.01
15.0	75.09	0.04	0.05
20.0	51.97	0.11	0.12
20.5	48.01	0.12	0.13
21.5	40.88	0.15	0.15
22.0	37.61	0.17	0.17
22.5	34.51	0.18	0.19
23.0	31.52	0.21	0.21
23.5	28.62	0.23	0.24
24.0	25.82	0.27	0.28
24.5	23.08	0.31	0.32
25.0	20.41	0.37	0.39
25.5	17.78	0.46	0.48
26.0	15.20	0.60	0.61
26.5	12.69	0.80	0.82
27.0	10.26	1.13	1.14
27.5	7.97	1.65	1.67
28.0	5.89	2.49	2.51
28.5	4.14	3.79	3.80
29.0	2.79	5.69	5.69
29.5	1.84	8.33	8.30
30.0	1.25	11.86	11.76
30.5	0.92	16.63	16.30
31.0	0.74	24.00	22.55
31.5	0.64	41.47	28.73
32.0	0.60	26.82	25.25
32.5	0.57	22.41	22.01
33.0	0.54	20.41	20.29
33.5	0.53	19.40	19.42
35.0	0.47	18.98	19.18
50.0	0.23	30.37	28.64
100.0	0.19	25.25	24.52
150.0	0.19	24.40	24.33
200.0	0.20	24.44	24.12
250.0	0.21	24.53	24.31
300.0	0.22	24.31	24.05
350.0	0.24	23.99	23.80
400.0	0.25	23.68	23.46
450.0	0.27	23.13	22.93
500.0	0.29	22.70	22.50
550.0	0.31	22.16	21.96
600.0	0.33	21.66	21.60
650.0	0.35	21.09	20.86
700.0	0.36	20.66	20.45
750.0	0.38	20.24	19.96
800.0	0.40	19.83	19.55
850.0	0.42	19.50	19.19
900.0	0.44	19.19	18.95
950.0	0.46	18.85	18.59
1000.0	0.48	18.61	18.35
1050.0	0.50	18.41	18.18
1100.0	0.51	18.32	18.00
1150.0	0.54	18.17	17.90
1200.0	0.56	18.15	17.77
1250.0	0.60	18.13	17.71
1300.0	0.62	18.06	17.60
1400.0	0.64	18.05	17.83
1500.0	0.69	18.33	18.15

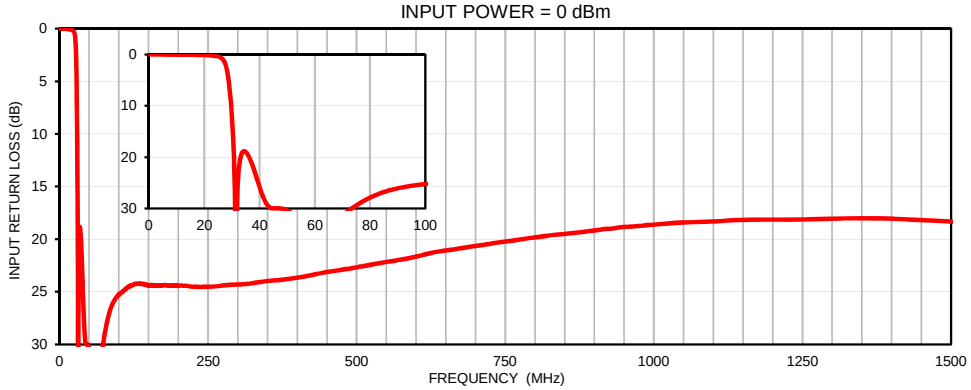
FREQ. (MHz)	Group Delay (ns)
31	66.56
32	57.14
33	47.37
34	39.71
35	34.27
36	30.29
37	27.23
38	24.73
39	22.63
40	20.83
45	14.64
50	11.05
55	8.76
60	7.17
65	6.01
70	5.13
75	4.46
80	3.91
85	3.49
90	3.13
95	2.84
100	2.59
105	2.38
110	2.20
115	2.04
120	1.91
125	1.79
130	1.70
135	1.59
140	1.51
145	1.45
150	1.37
155	1.31
160	1.26
170	1.17
175	1.13
200	0.95
250	0.79
300	0.69
350	0.63
400	0.59
450	0.56
500	0.54
550	0.53
600	0.51
650	0.51
700	0.50
750	0.50
800	0.49
850	0.49
900	0.49
950	0.48
1000	0.48
1100	0.48
1200	0.48
1250	0.48
1300	0.48
1350	0.48
1400	0.48
1500	0.49

Typical Performance Curves

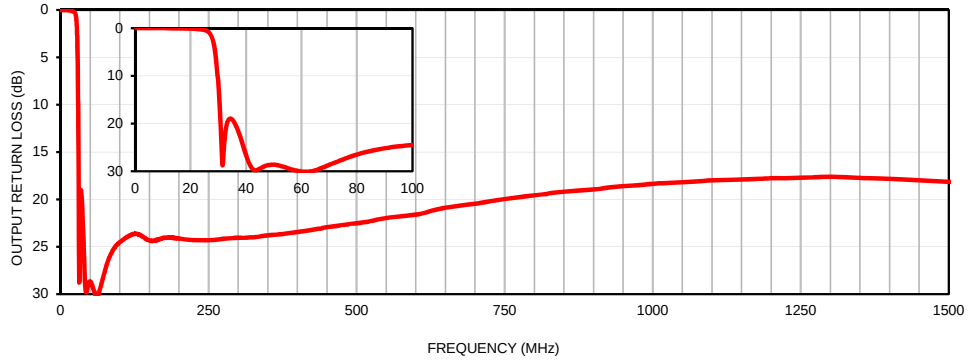
INSERTION LOSS
INPUT POWER = 0 dBm



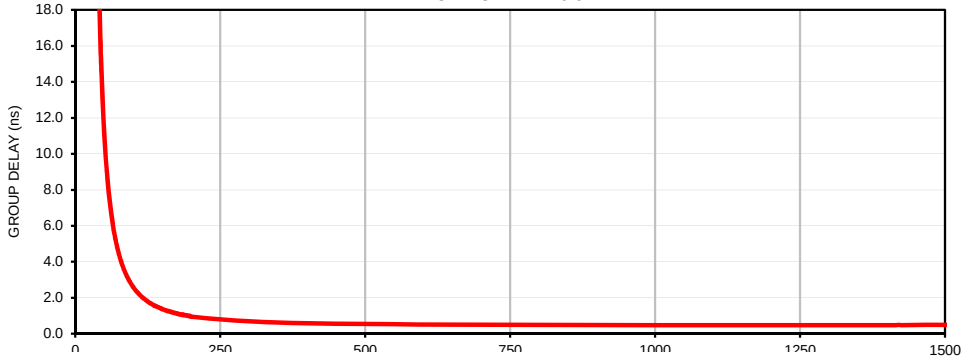
INPUT RETURN LOSS
INPUT POWER = 0 dBm



OUTPUT RETURN LOSS
INPUT POWER = 0 dBm



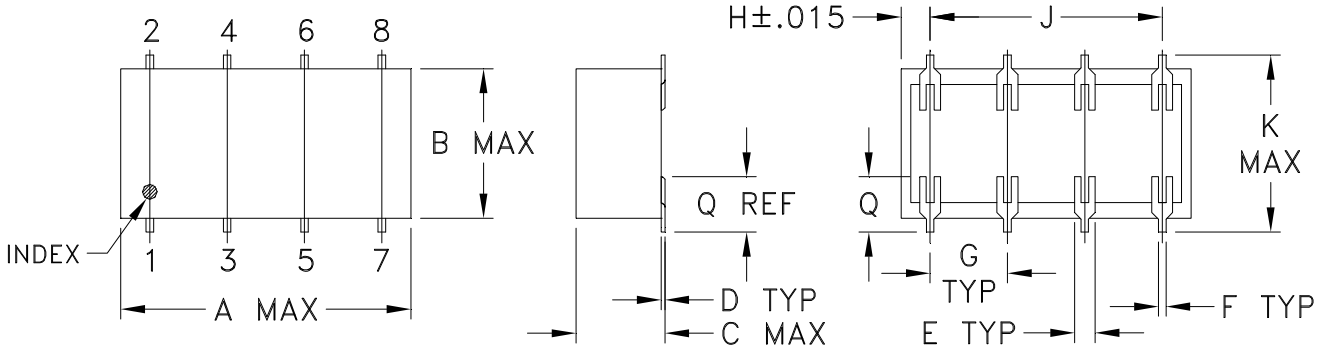
GROUP DELAY
INPUT POWER = 0 dBm



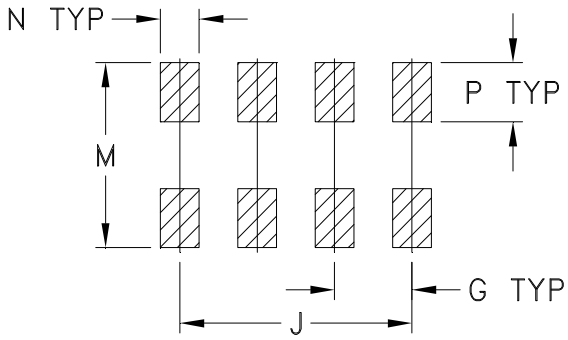
Case Style YY

Outline Dimensions

YY101
YY109
YY161



PCB Land Pattern



Suggested Layout
Tolerance to be within ±.002

CASE#	A	B	C	D	E	F	G	H	J	K	L	M	N	P	Q	WT. GRAMS
YY101*	.75 (19.05)	.38 (9.65)	.20 (5.08)	.010 (0.25)	.050 (1.27)	.020 (0.51)	.200 (5.08)	.075 (1.91)	.600 (15.24)	.450 (11.43)	-- --	.470 (11.94)	.100 (2.54)	.150 (3.81)	.148 (3.76)	1.6
YY109*			.20 (5.08)							.720 (18.29)	-- --	.740 (18.80)				1.6
YY161			.28 (7.11)							.450 (11.43)	-- --	.470 (11.94)				1.6

Dimensions are in inches (mm). Tolerances: 2 Pl. ± .01; 3 Pl. ± .005

Notes:

1. Case material: Plastic.
2. Termination finish:
   For RoHS Case Styles: Tin plate over Nickel plate.
   For RoHS-5 Case Styles: Tin-Lead plate.
3. Special Tolerances: Termination thickness ± .003 inch.
4. * Denotes: For SCM mixers, long termination version (case YY109) is available upon request, consult factory. To order short termination version (case YY101) add -NL suffix.

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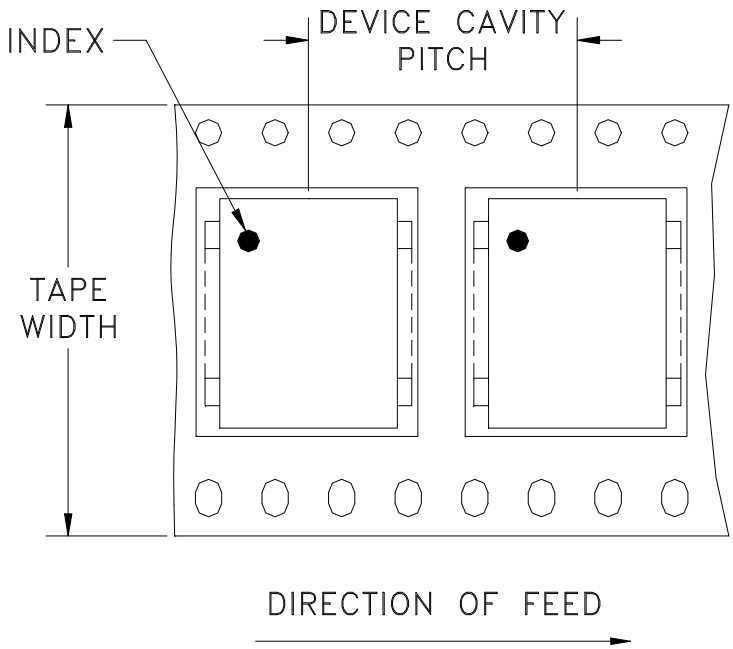
Tape & Reel Packaging TR-F5

DEVICE ORIENTATION IN T&R



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel
32	16	13	500

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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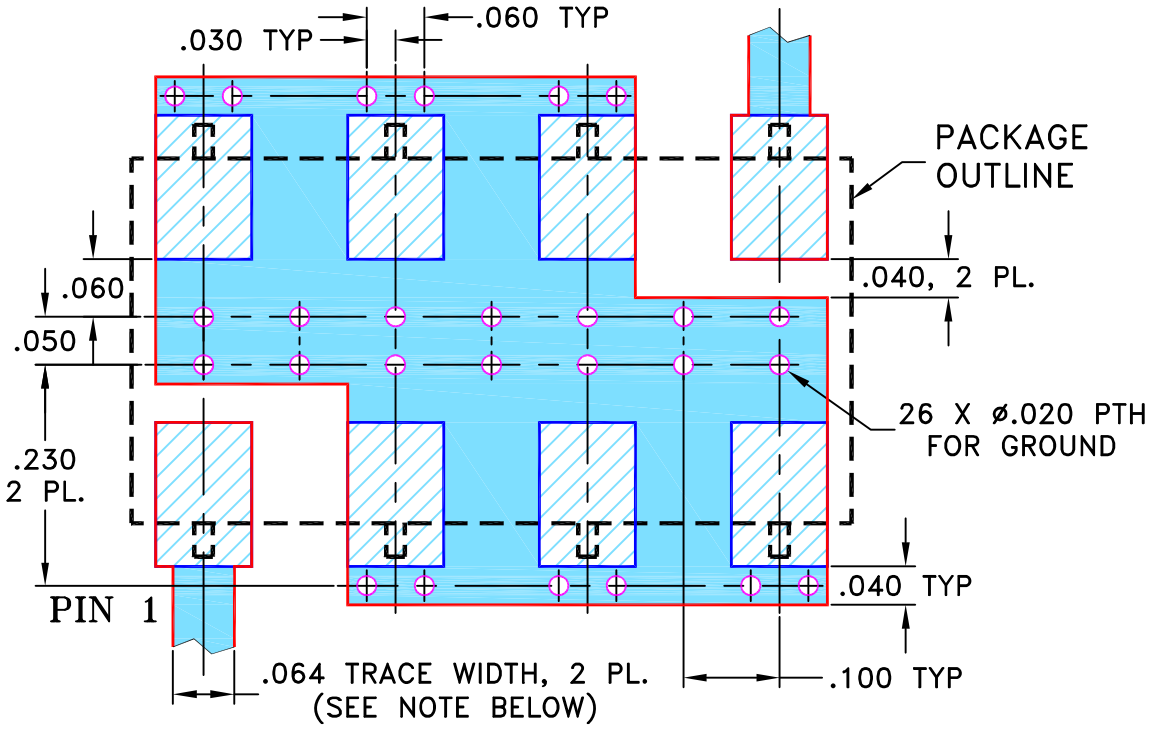
THIRD ANGLE PROJECTION

REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M81561	NEW RELEASE	09/11/02	GF	ABD
A	M101145	ADDED YY101 CASE STYLE & NOTE 2	10/07/05	MMG	ABD
B	M102713	ADDED "...WITH SMOBC"	01/16/06	GT	IL
C	M165986	TB-187+ (WAS TB-187)	02/01/18	ITG	IL

SUGGESTED MOUNTING CONFIGURATION FOR YY101/YY161 CASE STYLE, "cr" PIN CONNECTION



NOTES:
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2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	GF	08/12/02
TOLERANCES ON:	CHECKED	AV	09/11/02
2 PL DECIMALS ±	APPROVED	ABD	09/11/02
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

Mini-Circuits®

ASHEETA1.DWG REV:A DATE:01/12/95

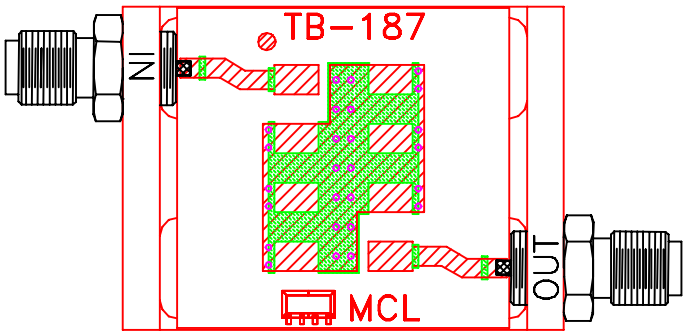
Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235

PL, cr, YY101/YY161, SCLF, TB-187+

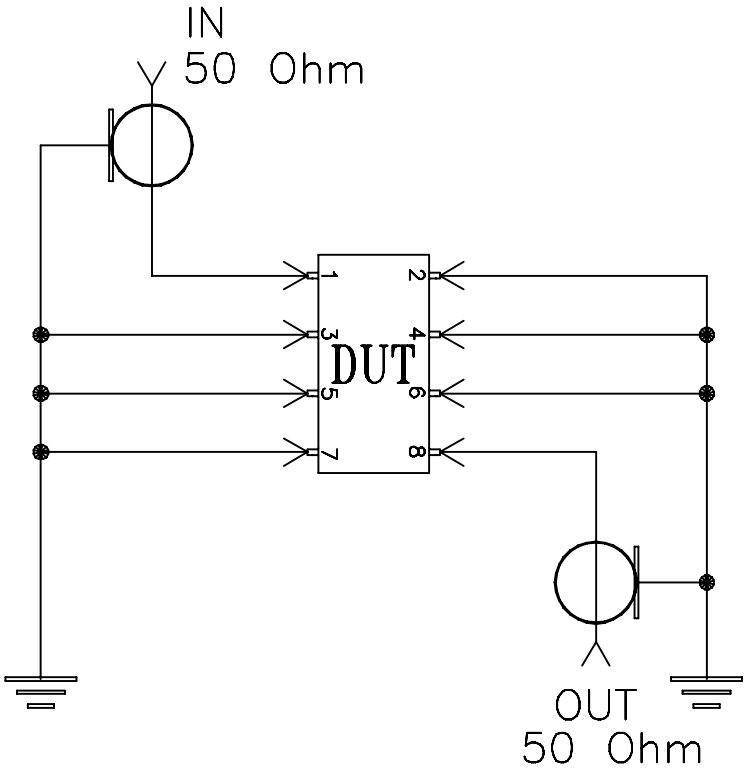
SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-049	C

FILE:	SCALE:	SHEET:
98PL049	5:1	1 OF 1

Evaluation Board and Circuit



TB-187



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.030 inch.

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Environmental Specifications ENV02T1

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215