

Frequency Mixer

SYM-ED6689/1

Level 9 (LO Power +9 dBm)

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : TTT167

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		Min.	Typ.	Max.	Units
Frequency	LO (f _L to f _U)	1130		2500	MHz
	RF (f _L to f _U)	1130		2500	MHz
	IF	0		400	MHz
Conversion Loss			7.3		dB
LO-RF Isolation			35		dB
LO-IF Isolation			29		dB

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
LO	2
RF	1
IF	3
GROUND	4, 5, 6

Frequency Mixer

SYM-ED6689/1

Typical Performance Data

RF (MHz)	LO (MHz)	CONVERSION LOSS (dB)			LO (MHz)	LO-RF ISOLATION (dB)			LO-IF ISOLATION (dB)		
		@LO (dBm)				@LO (dBm)			@LO (dBm)		
		+6	+9	+12		+6	+9	+12	+6	+9	+12
1130.0	1000.0	7.75	6.85	6.55	1000.0	34.2	37.8	40.4	9.8	10.9	12.1
1235.3	1105.3	7.39	6.59	6.29	1105.3	34.7	36.4	37.5	12.8	14.0	14.9
1340.5	1210.5	7.31	6.71	6.51	1210.5	33.1	34.0	34.5	17.0	17.8	18.8
1445.8	1315.8	7.50	7.10	7.00	1315.8	33.8	33.4	34.5	21.0	21.8	22.8
1551.1	1421.1	7.46	7.06	6.96	1421.1	36.1	33.4	33.1	23.8	24.9	25.9
1656.3	1526.3	7.51	7.21	7.11	1526.3	37.8	36.4	35.7	26.0	27.2	28.3
1761.6	1631.6	7.26	7.06	7.06	1631.6	35.7	34.6	34.6	28.2	29.3	30.3
1850.0	1720.0	7.47	6.97	6.87	1720.0	33.0	32.3	33.0	29.3	30.3	31.2
1860.0	1730.0	7.43	7.03	6.93	1730.0	32.8	32.3	32.9	29.8	30.7	31.8
1870.0	1740.0	7.50	7.10	7.00	1740.0	32.4	32.1	32.8	30.3	31.3	32.2
1880.0	1750.0	7.63	7.23	7.13	1750.0	32.5	32.2	32.9	30.6	31.6	32.5
1890.0	1760.0	7.66	7.36	7.16	1760.0	32.8	32.7	33.3	30.7	31.6	32.6
1900.0	1770.0	7.73	7.43	7.23	1770.0	33.3	33.4	33.8	30.5	31.4	32.4
1910.0	1780.0	7.68	7.38	7.18	1780.0	33.9	33.9	34.4	30.0	31.2	32.1
1972.1	1842.1	7.97	7.57	7.47	1842.1	35.9	35.9	35.8	32.0	33.3	34.2
2077.4	1947.4	8.12	7.82	7.72	1947.4	35.2	35.7	36.3	33.5	34.8	35.7
2182.6	2052.6	8.25	7.95	7.65	2052.6	35.9	36.7	37.1	34.8	36.2	37.0
2287.9	2157.9	7.79	7.69	7.59	2157.9	40.3	40.8	40.5	37.6	38.7	39.0
2393.2	2263.2	7.67	7.67	7.67	2263.2	33.3	34.6	35.6	36.5	37.5	38.1
2498.4	2368.4	8.04	7.94	8.04	2368.4	31.4	32.0	32.5	34.3	35.2	35.9

Frequency Mixer

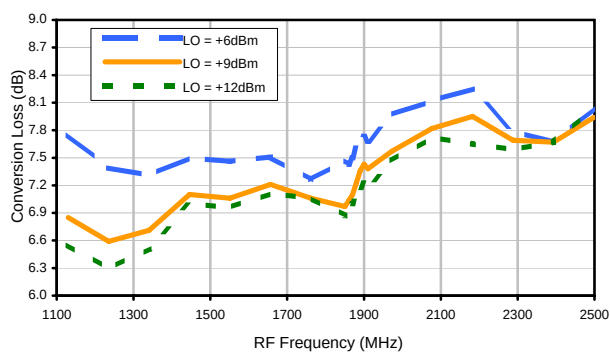
SYM-ED6689/1

Typical Performance Data

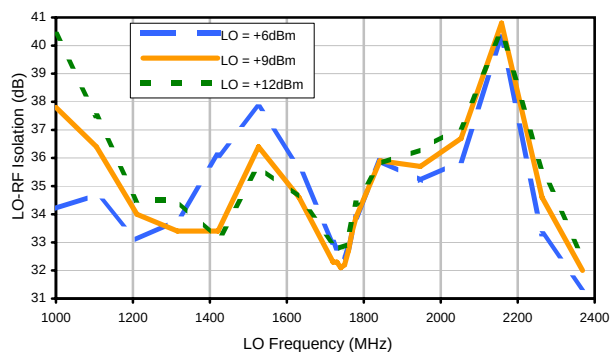
RF/LO (MHz)	RF VSWR (:1)			LO VSWR (:1)			IF (MHz)	IF VSWR (:1)		
	@LO (dBm)			@LO (dBm)				@LO (dBm)		
	+6	+9	+12	+6	+9	+12		+6	+9	+12
1000.0	2.20	1.54	1.42	1.66	1.64	1.63	10.0	1.38	1.17	1.06
1105.3	2.20	1.59	1.46	1.41	1.38	1.33	25.0	1.36	1.17	1.09
1210.5	2.25	1.61	1.50	1.23	1.22	1.18	45.0	1.37	1.22	1.16
1315.8	2.27	1.64	1.54	1.14	1.14	1.16	50.0	1.39	1.24	1.18
1421.1	2.27	1.65	1.54	1.16	1.11	1.13	80.0	1.49	1.35	1.29
1526.3	2.35	1.67	1.54	1.17	1.12	1.15	100.0	1.50	1.36	1.31
1631.6	2.35	1.61	1.52	1.12	1.03	1.11	115.0	1.49	1.37	1.32
1720.0	2.10	1.48	1.51	1.27	1.11	1.04	130.0	1.48	1.37	1.32
1730.0	2.14	1.49	1.51	1.25	1.10	1.05	150.0	1.53	1.41	1.37
1740.0	2.16	1.49	1.53	1.25	1.11	1.06	185.0	1.62	1.51	1.49
1750.0	2.16	1.50	1.56	1.25	1.11	1.05	200.0	1.60	1.51	1.51
1760.0	2.20	1.52	1.57	1.26	1.10	1.04	220.0	1.57	1.50	1.50
1770.0	2.20	1.51	1.57	1.26	1.11	1.04	250.0	1.56	1.49	1.51
1780.0	2.20	1.51	1.57	1.27	1.13	1.06	255.0	1.59	1.53	1.54
1842.1	2.10	1.44	1.55	1.38	1.25	1.17	290.0	1.64	1.61	1.65
1947.4	1.96	1.37	1.58	1.46	1.37	1.30	325.0	1.59	1.59	1.65
2052.6	1.88	1.31	1.51	1.55	1.50	1.45	360.0	1.60	1.64	1.71
2157.9	1.80	1.31	1.50	1.51	1.49	1.48	395.0	1.63	1.69	1.77
2263.2	1.74	1.21	1.46	1.37	1.37	1.37	430.0	1.59	1.67	1.77
2368.4	1.70	1.13	1.44	1.31	1.31	1.32	465.0	1.63	1.76	1.88

Typical Performance Curves

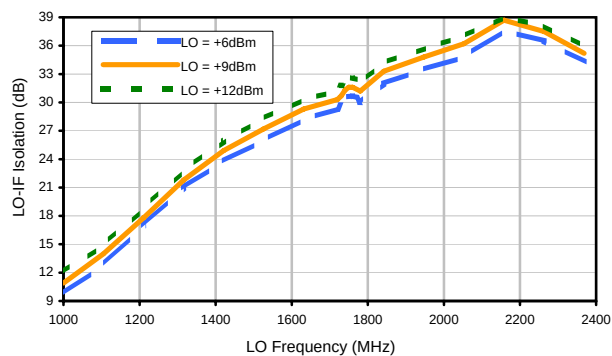
Conversion Loss



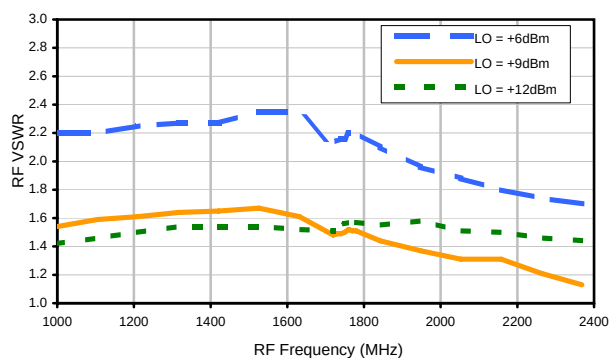
LO-RF Isolation



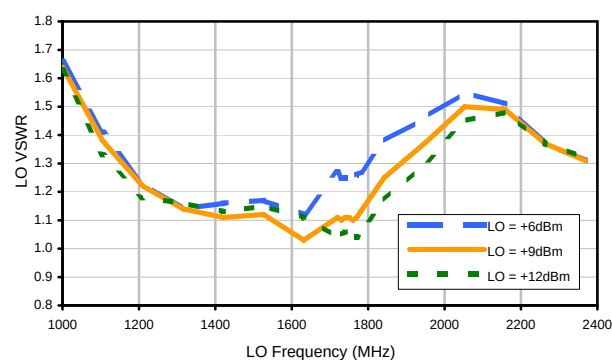
LO-IF Isolation



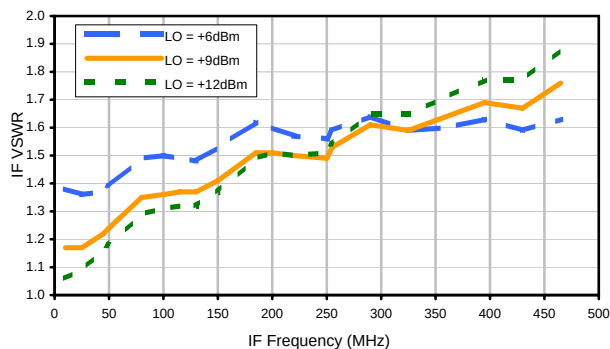
RF VSWR



LO VSWR

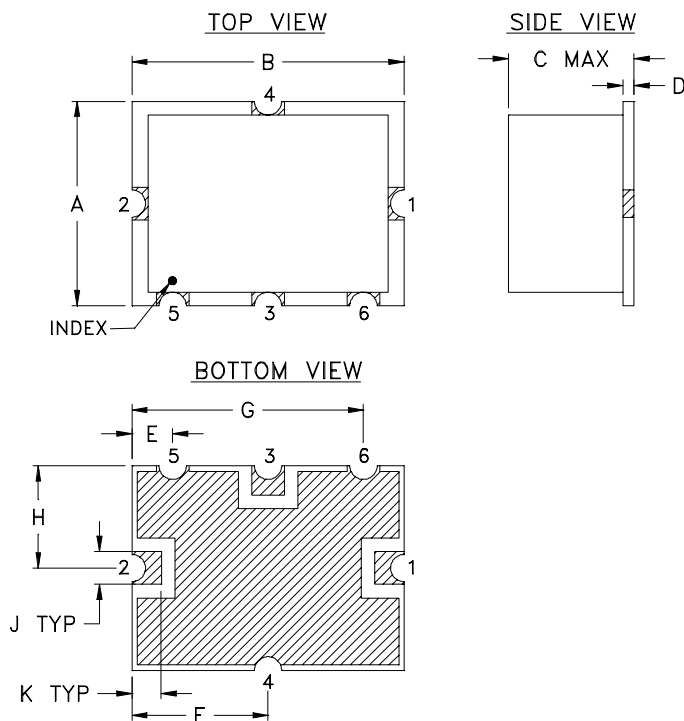


IF VSWR

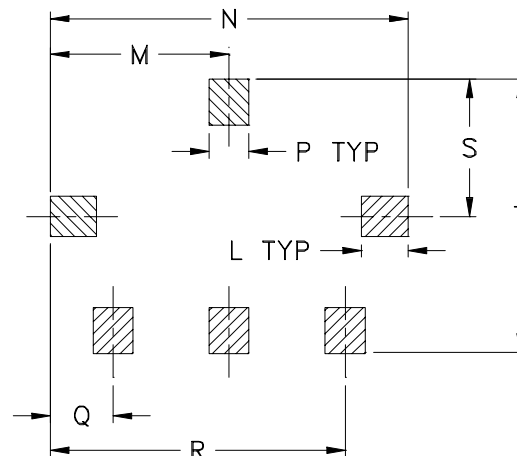


Outline Dimensions

TTT166
TTT167



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm.002$

CASE #	A	B	C	D	E	F	G	H	J	K	L	M	N
TTT166	.38 (9.65)	.50 (12.70)	.15 (3.81)	.020 (0.51)	.075 (1.91)	.250 (6.35)	.425 (10.80)	.187 (4.75)	.050 (1.27)	.050 (1.27)	.070 (1.78)	.270 (6.86)	.540 (13.72)
TTT167			.23 (5.84)										

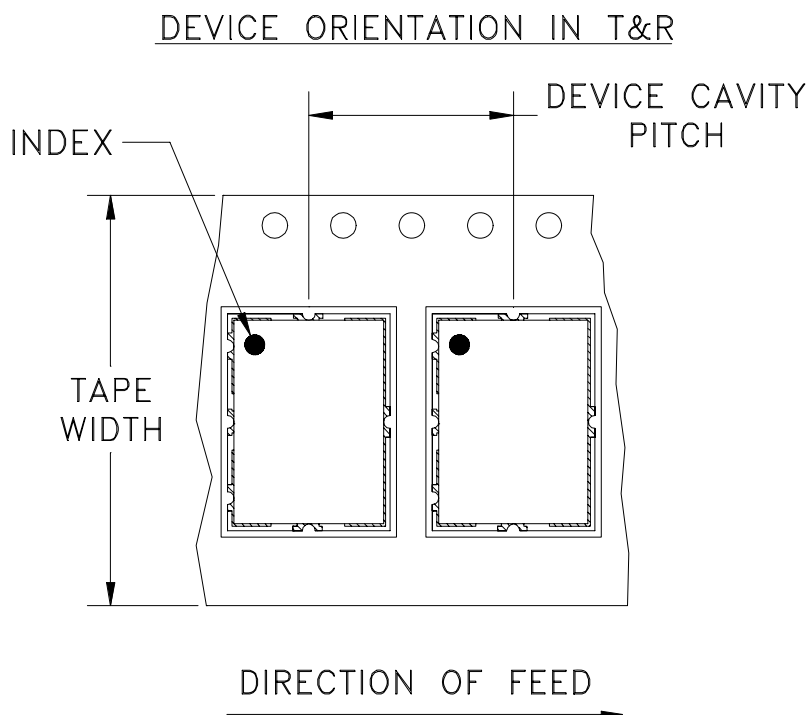
CASE #	P	Q	R	S	T	WT. GRAM
TTT166	.060 (1.52)	.095 (2.41)	.445 (11.30)	.208 (5.28)	.415 (10.54)	.8
TTT167						.8

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .01$; 3 Pl. $\pm .005$

Note:

- Case material: Plastic.
- Base material: Printed wiring laminate.
- Termination finish:
 - For RoHS Case Styles: 3-5 μ inch (.08-.13 microns) Gold over 120-240 μ inch (3.05-6.10 microns) Nickel plate. All models, (+) suffix.
 - For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.

Tape & Reel Packaging TR-F12



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
24	12	7	Small quantity standards (see note)	10
				20
				50
				100
				200
		13	Standard	500

Note: Please Consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



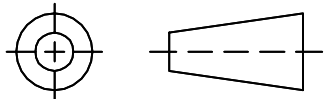
Distribution Centers NORTH AMERICA 800-654-7949 • 417-335-5935 • Fax 417-335-5945 • EUROPE 44-1252-832600 • Fax 44-1252-837010

Mini-Circuits ISO 9001 & ISO 14001 Certified

INTERNET <http://www.minicircuits.com>

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661

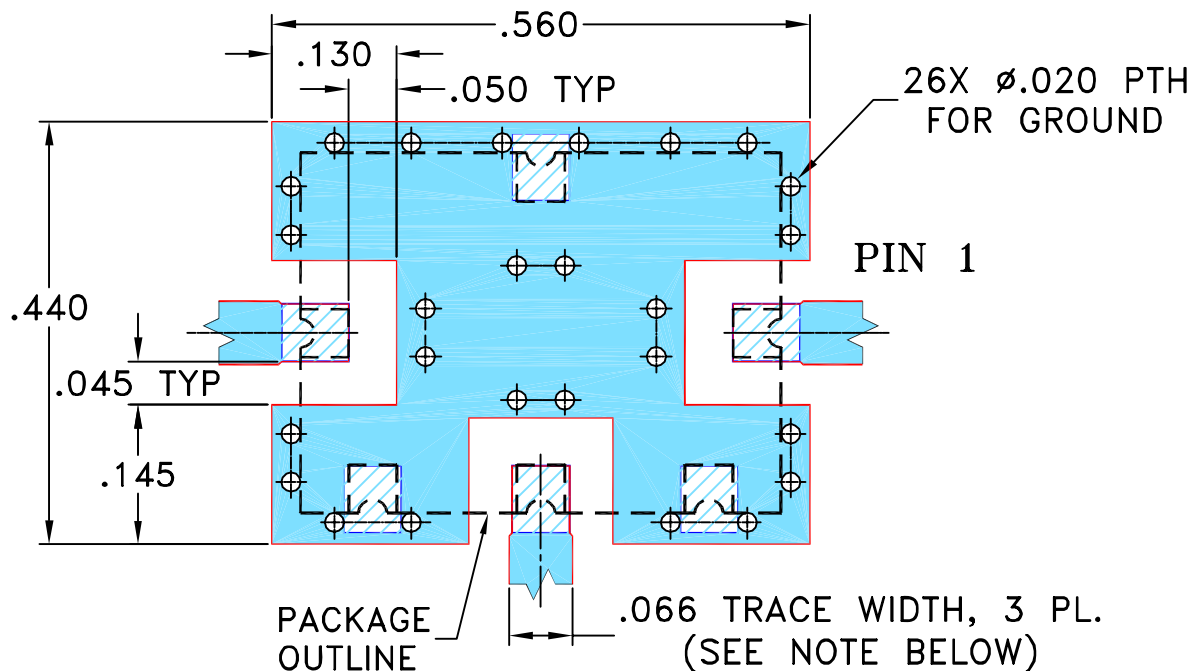
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
A	M86762	ADDED CONNECTIONS "lp & lq"	05/23/03	MMG	WL
B	M94598	ADDED CONNECTION "hk"	10/08/04	MMG	HY
C	M102713	UPDATED NOTES & DESCRIPTION	01/14/06	GF	IL
D	M132989	UPDATED NOTE 2	08/24/11	GF	DJ

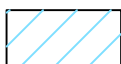
**SUGGESTED MOUNTING CONFIGURATION FOR
TTT166/167 CASE STYLE, "hk"/"lp"/"lq"
"x"/"ck"/"ec" PIN CONNECTIONS**

**NOTE:**

1. TRACE WIDTH IS SHOWN FOR ROGERS R04350B WITH DIELECTRIC THICKNESS .030" $\pm$.002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. THE USE OF SOLDER MASK OVER THE GROUND AREA UNDER THE UNIT AS SHOWN IS RECOMMENDED TO PREVENT POTENTIAL SHORTING. IF USER CHOOSES TO EXPOSE METAL UNDER THE ENTIRE UNIT GROUND PAD FOR IMPROVED GROUNDING, IT IS RECOMMENDED A SOLDER MASK DAM BE APPLIED AROUND EACH GROUND PAD TO ENSURE FILLET AND CONNECTION AT GROUND PADS.
3. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER), SEE NOTE 2.



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

TOLERANCES ON:

2 PL DECIMALS $\pm$ 3 PL DECIMALS $\pm$.005ANGLES $\pm$ FRACTIONS $\pm$

DRAWN

GF

03/18/03

CHECKED

IL

04/15/03

APPROVED

DJ

04/15/03



Mini-Circuits®

13 Neptune Avenue
Brooklyn NY 11235

PL, hk/lp/lq/x/ck/ec, TTT166/167,
SYM/HJK/SYAS/SYPD, TB-12

SIZE
A

CODE IDENT
15542

DRAWING NO:
98-PL-079

REV:
D

FILE: 98PL079

SCALE: 5:1

SHEET: 1 OF 1

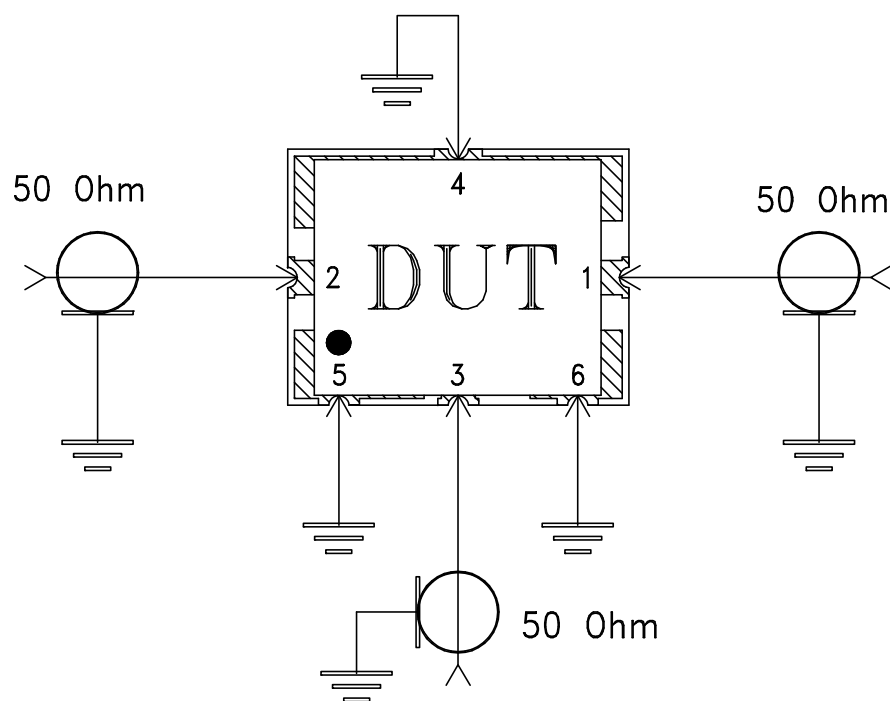
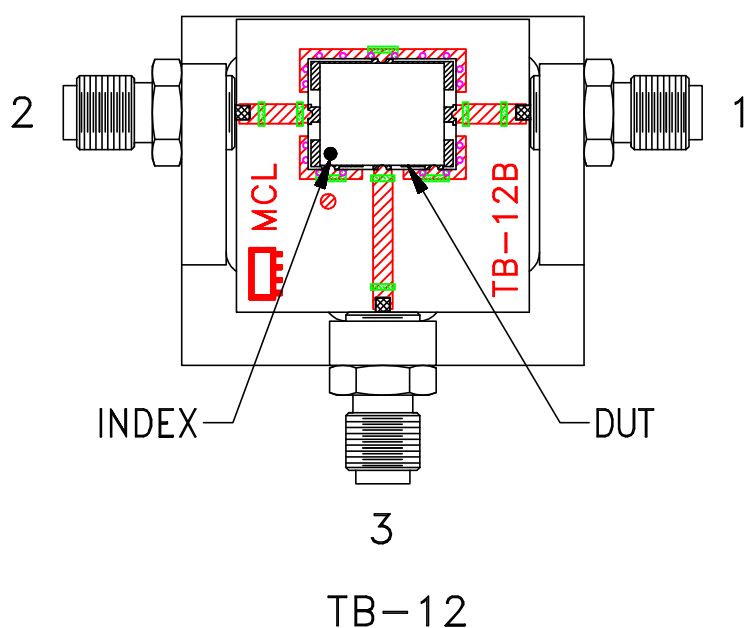
Mini-Circuits®

THIS DOCUMENT AND ITS CONTENTS ARE THE PROPERTY OF MINI-CIRCUITS. EXCEPT FOR USE EXPRESSLY GRANTED, IN WRITING, TO ITS VENDORS, VENDEE AND THE UNITED STATES GOVERNMENT, MINI-CIRCUITS RESERVES ALL PROPRIETARY DESIGN, USE, MANUFACTURING AND REPRODUCTION RIGHTS THERETO. THESE CONTENTS SHALL NOT BE USED, DUPLICATED OR DISCLOSED TO ANY OUTSIDE PARTY, IN WHOLE OR IN PART, WITHOUT WRITTEN PERMISSION OF MINI-CIRCUITS.

ASHEETA1.DWG REV:A DATE:01/12/95

Evaluation Board and Circuit

For Pin Connections Refer to Data Sheet of the DUT



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent,
Dielectric Constant=3.5, Thickness=.030 inch.

 **Mini-Circuits®**



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215