

Engineering Development Model

Power Splitter/Combiner

TCP-ED8261/1

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE:DB714

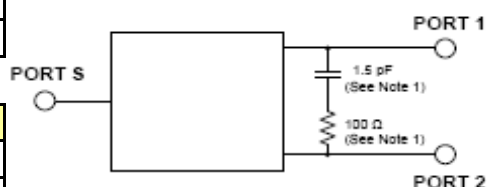
ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		Min.	Typ.	Max.	Units
Frequency		0.5		1400	MHz
Isolation	0.5 - 5 MHz		25		dB
	5 - 700 MHz		28		dB
	700 - 1400 MHz		28		dB
Insertion Loss (Above 3.0 dB)	0.5 - 5 MHz		0.35		dB
	5 - 700 MHz		0.27		dB
	700 - 1400 MHz		0.97		dB
Phase Unbalance	0.5 - 5 MHz		2.20		deg.
	5 - 700 MHz		0.82		deg.
	700 - 1400 MHz		0.10		deg.
Amplitude Unbalance	0.5 - 5 MHz		0.42		dB
	5 - 700 MHz		0.16		dB
	700 - 1400 MHz		0.04		dB
VSWR	SUM Port		1.17		(:1)
	OUT Ports		1.28		(:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS (NOTE 1)	
SUM PORT	1
PORT 1	3
PORT 2	4
GND EXT	6
NOT USED	2, 3

NOTE 1: EXTERNAL RESISTOR 100Ω AND CAPACITOR 1.5pF SHOULD BE CONNECTED BETWEEN PINS 3,4.

Functional Diagram



2 Way-0° Power Splitter/Combiner

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Typical Performance Data

FREQ. (MHz)	TOTAL LOSS ¹ (dB)		AMP. UNBAL. (dB)	ISOLATION (dB)	PHASE UNBAL. (deg.)	FREQ. (MHz)	VSWR (:1)		
	S-1	S-2					S	1	2
0.5	3.18	3.83	0.65	20.10	3.14	0.5	1.26	2.05	1.50
0.7	3.17	3.76	0.59	20.92	2.98	0.7	1.22	1.93	1.45
0.8	3.17	3.70	0.54	21.61	2.82	0.8	1.20	1.85	1.41
0.9	3.16	3.66	0.50	22.21	2.64	0.9	1.18	1.78	1.38
1.0	3.15	3.61	0.47	22.75	2.53	1.0	1.16	1.72	1.36
2.0	3.12	3.45	0.33	26.02	1.82	2.0	1.10	1.47	1.25
3.0	3.11	3.38	0.27	27.99	1.43	3.0	1.07	1.37	1.21
4.0	3.11	3.35	0.24	29.29	1.19	4.0	1.06	1.32	1.18
5.0	3.10	3.33	0.23	30.15	0.98	5.0	1.05	1.28	1.17
6.0	3.11	3.31	0.21	30.88	0.89	6.0	1.05	1.25	1.16
7.0	3.10	3.30	0.20	31.40	0.82	7.0	1.04	1.23	1.15
8.0	3.11	3.30	0.19	31.75	0.76	8.0	1.04	1.22	1.15
9.0	3.11	3.29	0.19	32.04	0.70	9.0	1.04	1.21	1.15
10.0	3.10	3.29	0.19	32.26	0.60	10.0	1.04	1.20	1.14
100.0	3.16	3.30	0.14	30.00	0.15	100.0	1.06	1.10	1.12
190.0	3.20	3.34	0.14	27.03	0.45	190.0	1.09	1.09	1.12
280.0	3.22	3.35	0.13	24.85	0.69	280.0	1.12	1.09	1.11
370.0	3.26	3.40	0.13	23.32	0.88	370.0	1.14	1.08	1.10
460.0	3.32	3.43	0.12	22.25	1.06	460.0	1.15	1.07	1.08
550.0	3.36	3.46	0.10	21.58	1.23	550.0	1.15	1.05	1.06
640.0	3.40	3.48	0.08	21.28	1.38	640.0	1.13	1.03	1.03
730.0	3.43	3.49	0.06	21.37	1.41	730.0	1.11	1.04	1.02
820.0	3.46	3.49	0.03	21.88	1.54	820.0	1.08	1.07	1.04
910.0	3.54	3.55	0.01	23.06	1.63	910.0	1.05	1.11	1.08
1000.0	3.66	3.65	0.01	25.28	1.74	1000.0	1.07	1.17	1.14
1050.0	3.80	3.77	0.03	27.34	1.99	1050.0	1.11	1.21	1.18
1100.0	3.78	3.75	0.03	30.39	1.91	1100.0	1.17	1.25	1.23
1150.0	4.02	3.95	0.07	36.20	2.06	1150.0	1.23	1.30	1.28
1200.0	3.99	3.95	0.03	42.72	2.20	1200.0	1.29	1.35	1.33
1250.0	4.28	4.20	0.08	32.88	2.38	1250.0	1.37	1.40	1.38
1300.0	4.37	4.34	0.03	27.19	2.47	1300.0	1.46	1.47	1.44
1350.0	4.60	4.54	0.07	23.75	2.77	1350.0	1.55	1.54	1.50
1400.0	4.82	4.83	0.01	21.03	2.98	1400.0	1.65	1.62	1.57

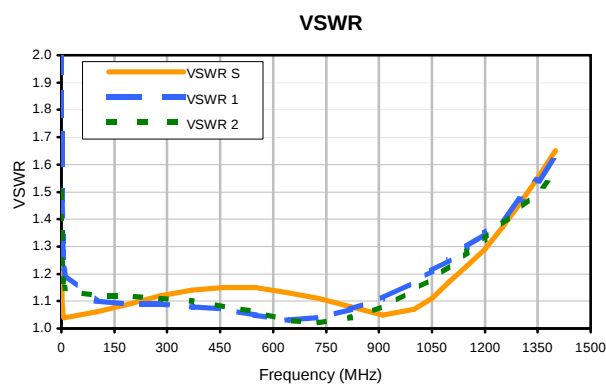
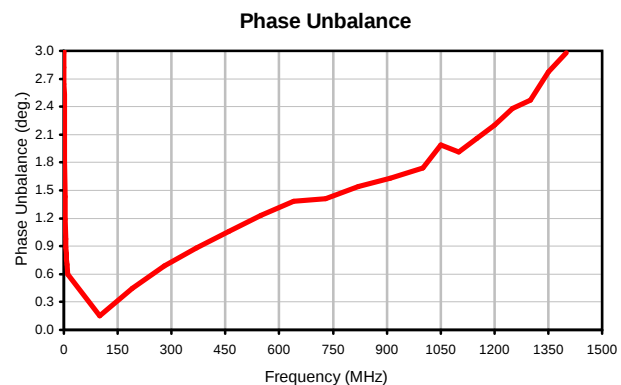
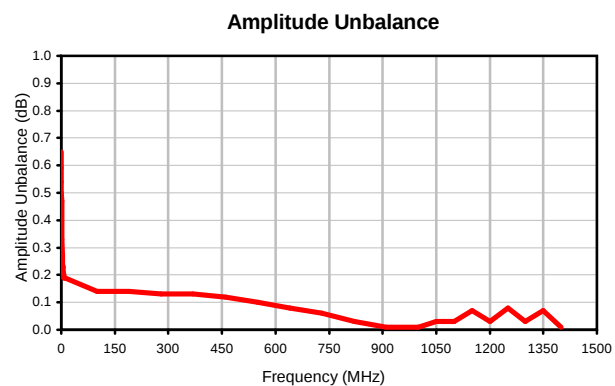
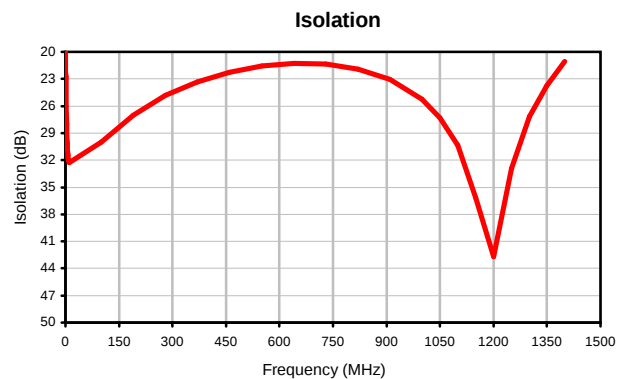
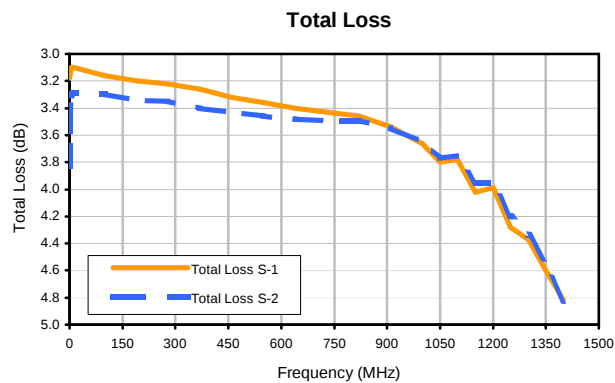
¹ Total Loss = Insertion Loss + 3dB Splitter Loss

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100707
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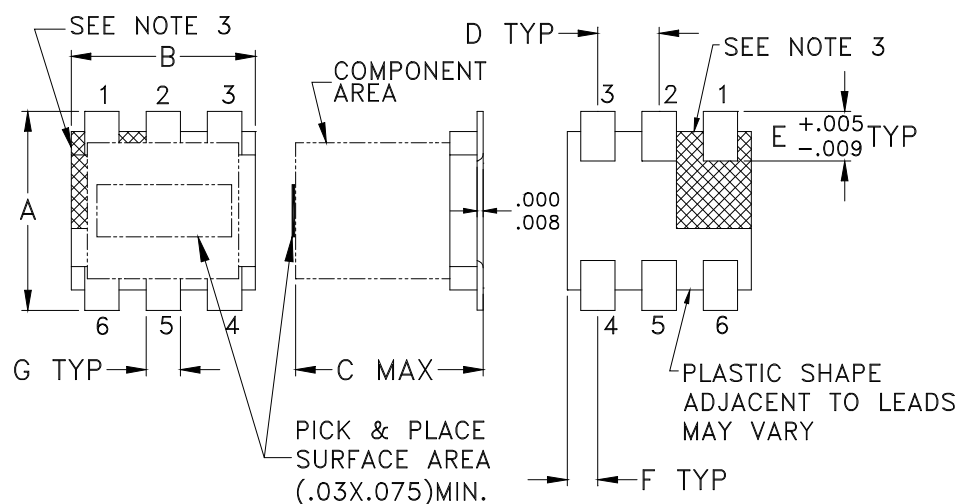
Typical Performance Curves



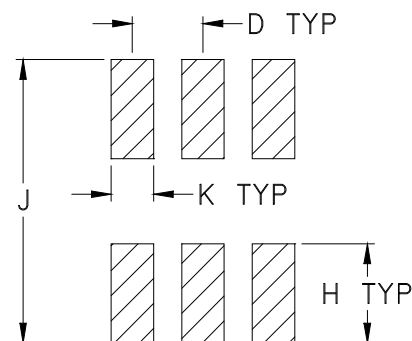
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Outline Dimensions

DB714



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K	WT. GRAM
DB714	.160 (4.06)	.150 (3.81)	.160 (4.06)	.050 (1.27)	.040 (1.02)	.025 (0.64)	.028 (0.71)	.065 (1.65)	.190 (4.83)	.030 (0.76)	.15

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .01$; 3Pl. $\pm .005$

Notes:

- Case material: Plastic.
- Termination finish:
For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.
- Lead #1 identifier shall be located in the cross-hatched area shown.
Identifier may be either a molded or marked feature.



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site

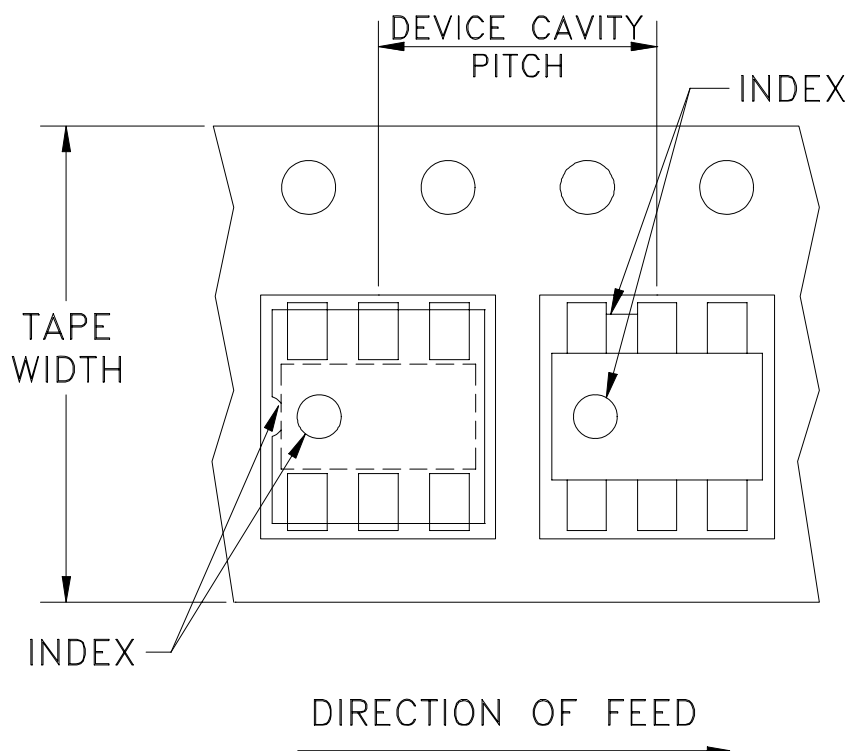


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RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F47

DEVICE ORIENTATION IN T&R



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel see note
12	8	13	1000, 2000
		7	20, 50, 100, 200, 500

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



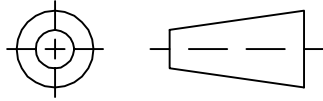
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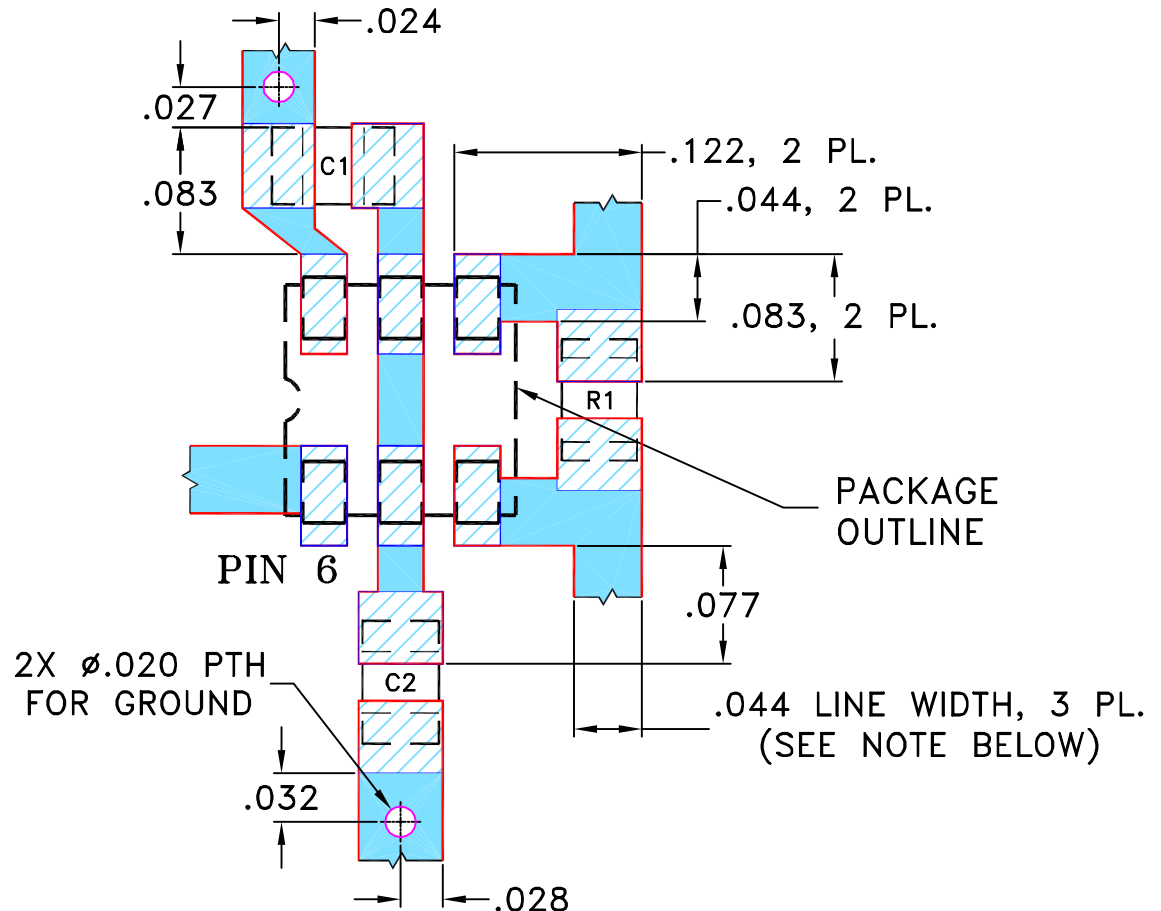
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
A	M71500	ADDED SOLDER MASK & LEGEND	04/00	IL	DB
B	M82377	UPDATED DRAWING	08/06/02	GF	HY
C	M83081	UPDATED DRAWING	06/24/03	MMG	DY
D	M102713	ADDED "...WITH SMOBC", CRCTD MATL	01/12/06	GF	IL

SUGGESTED MOUNTING CONFIGURATION FOR
DB714 CASE STYLE, "mt" PIN CONNECTION

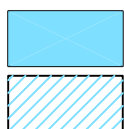


RESISTOR R1: 100 Ohm, 0805 SIZE

CAPACITORS C1 & C2: 1.5 pF, 0805 SIZE

NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS R04350B WITH DIELECTRIC THICKNESS $0.020'' \pm 0.0015''$; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.

2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED	INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN IL	02/24/00
TOLERANCES ON:	CHECKED DY	02/29/00
2 PL DECIMALS $\pm$	APPROVED DB	02/29/00
3 PL DECIMALS $\pm$.005		
ANGLES $\pm$		
FRACTIONS $\pm$		

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ASHEETA1.DWG REV:A DATE:01/12/95



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Brooklyn NY 11235

PL, mt, DB714, TCP, TB-232

SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-001	D
FILE:	98PL001	SCALE:	8:1
		SHEET:	1 OF 1



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215