Engineering Development Model

Voltage Controlled Oscillator

UOS-EDR5454

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : CZ682

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	**Min.**	**Typ.**	**Max.**	**Units**
Frequency	94		99	MHz
Tuning Voltage	0.5		5	V
Power Output		+3		dBm
Phase Noise				
at 1 kHz offset		-85		dBc/Hz
at 10 KHz offset		-114		dBc/Hz
at 100 KHz offset		-132		dBc/Hz
at 1000 kHz offset		-143		dBc/Hz
Pulling at 12 dBr PK-PK all phases		0.4		MHz
Pushing at Vcc=5V±0.25V		0.3		MHz/V
Tuning Sensitivity		1.5 - 6		MHz/V
Harmonic Suppression		-29	-22	dBc
3 dB Modulation Bandwidth		500		kHz
Supply Voltage		5		V
Supply Current			15	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+7V
Absolute Tuning Voltage (Vtune)	+7V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	1
GROUND	2,4,6,7,8

Voltage Controlled Oscillator UOS-EDR5454

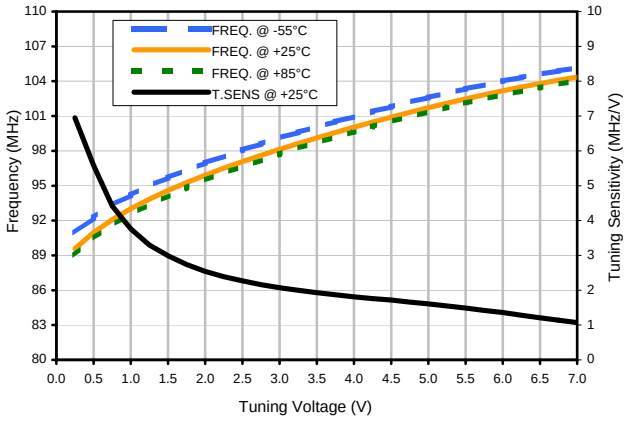
Typical Performance Data

V TUNE	TUNE SENS (MHz/V)	FREQUENCY (MHz)			POWER OUTPUT (dBm)			HARMONICS (dBc)			FREQ. PUSH (MHz/V)
		-55ºC	+25ºC	+85ºC	-55ºC	+25ºC	+85ºC	F2	F3	F4	
0.3	6.95	90.9	89.6	89.0	3.41	3.12	2.86	-27.7	-45.4	-54.2	-0.13
0.5	5.58	92.3	91.0	90.5	3.54	3.22	2.95	-28.2	-46.2	-54.2	-0.10
0.8	4.42	93.3	92.1	91.6	3.63	3.28	3.00	-28.6	-46.6	-53.8	-0.09
1.0	3.76	94.2	93.0	92.5	3.69	3.33	3.04	-29.0	-47.0	-53.7	-0.08
1.3	3.30	95.0	93.8	93.4	3.74	3.36	3.07	-29.2	-47.3	-53.8	-0.08
1.5	2.99	95.7	94.6	94.1	3.77	3.37	3.08	-29.5	-47.7	-53.9	-0.07
1.8	2.74	96.4	95.3	94.8	3.79	3.38	3.09	-29.6	-48.1	-54.1	-0.07
2.0	2.54	97.0	95.9	95.5	3.81	3.39	3.09	-29.8	-48.5	-54.4	-0.07
2.3	2.39	97.5	96.5	96.1	3.81	3.38	3.08	-29.9	-48.9	-54.4	-0.07
2.5	2.27	98.1	97.1	96.7	3.81	3.37	3.07	-30.0	-49.2	-54.7	-0.07
2.8	2.16	98.6	97.6	97.2	3.80	3.36	3.05	-30.1	-49.7	-54.7	-0.06
3.0	2.07	99.1	98.1	97.7	3.78	3.34	3.03	-30.2	-50.2	-54.9	-0.06
3.3	2.00	99.6	98.6	98.2	3.76	3.31	3.00	-30.3	-50.5	-55.0	-0.06
3.5	1.93	100.1	99.1	98.7	3.73	3.28	2.97	-30.4	-51.0	-55.3	-0.06
3.8	1.87	100.5	99.6	99.2	3.70	3.25	2.94	-30.5	-51.3	-55.2	-0.06
4.0	1.81	101.0	100.0	99.6	3.67	3.22	2.90	-30.5	-51.7	-55.3	-0.06
4.3	1.76	101.4	100.5	100.1	3.63	3.18	2.86	-30.6	-52.0	-55.2	-0.06
4.5	1.72	101.8	100.9	100.5	3.59	3.13	2.82	-30.7	-52.3	-55.1	-0.06
4.8	1.66	102.2	101.3	100.9	3.55	3.09	2.77	-30.7	-52.6	-55.3	-0.06
5.0	1.61	102.6	101.7	101.4	3.50	3.04	2.72	-30.8	-52.9	-55.0	-0.06
5.3	1.55	103.0	102.1	101.7	3.45	2.99	2.67	-30.8	-53.0	-54.9	-0.06
5.5	1.49	103.3	102.5	102.1	3.39	2.94	2.62	-30.9	-53.3	-55.0	-0.06
5.8	1.42	103.7	102.8	102.5	3.34	2.90	2.57	-31.0	-53.4	-54.6	-0.06
6.0	1.36	104.0	103.2	102.8	3.29	2.85	2.52	-31.0	-53.6	-54.5	-0.06
6.3	1.28	104.3	103.5	103.2	3.24	2.80	2.47	-31.1	-53.6	-54.6	-0.06
6.5	1.21	104.6	103.8	103.5	3.19	2.75	2.42	-31.1	-53.8	-54.5	-0.06
6.8	1.14	104.9	104.1	103.7	3.14	2.70	2.37	-31.2	-53.7	-54.4	-0.06
7.0	1.07	105.2	104.3	104.0	3.09	2.65	2.32	-31.2	-53.8	-54.3	-0.06

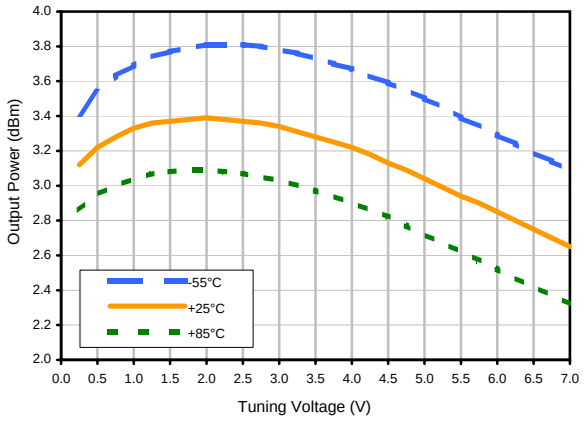
FREQ OFFSET (KHz)	PHASE NOISE (dBc/Hz)
1	-85
10	-114
100	-132
1000	-143

Typical Performance Data

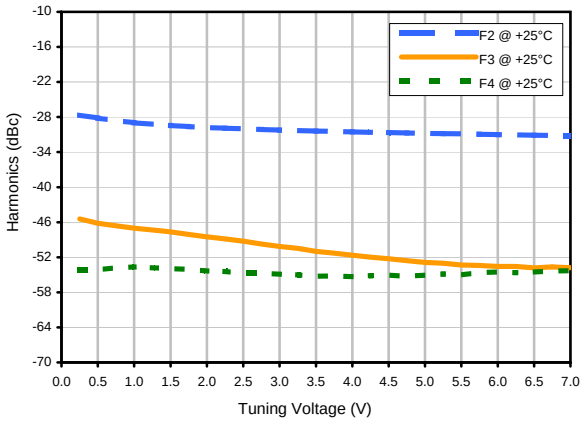
Frequency and Tuning Sensitivity



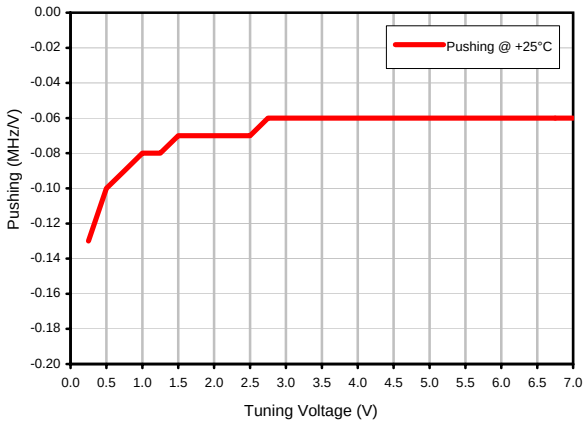
Power Output



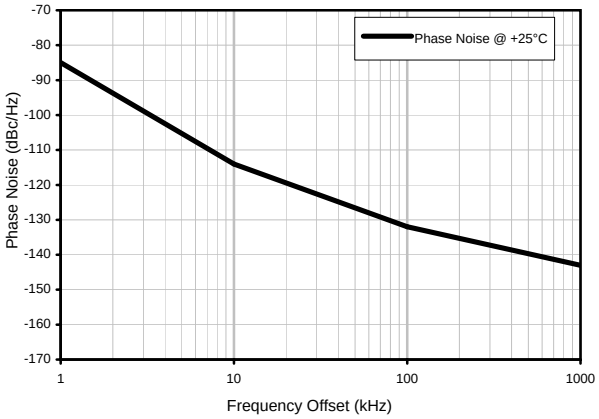
Harmonics Level



Frequency Pushing



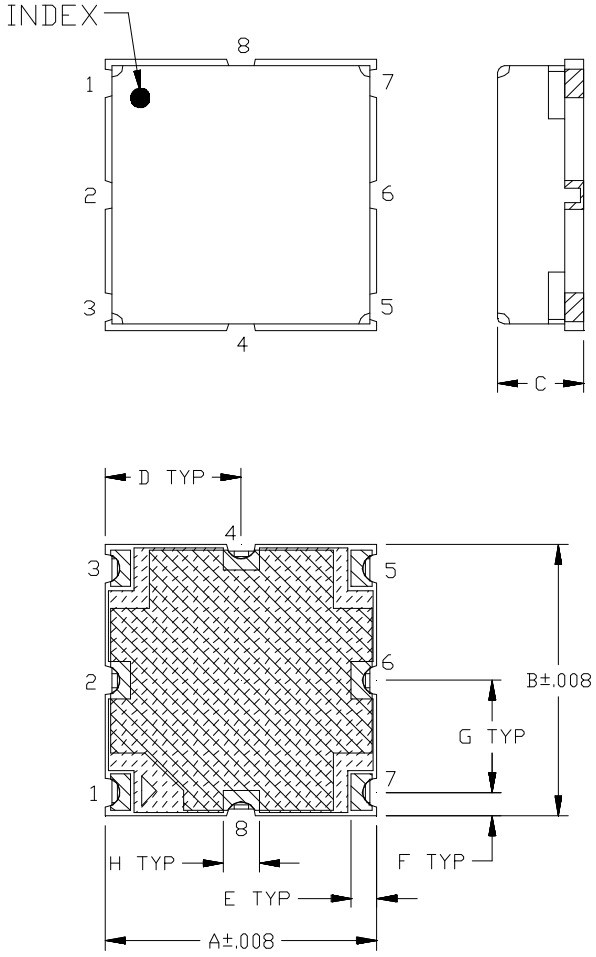
Phase Noise



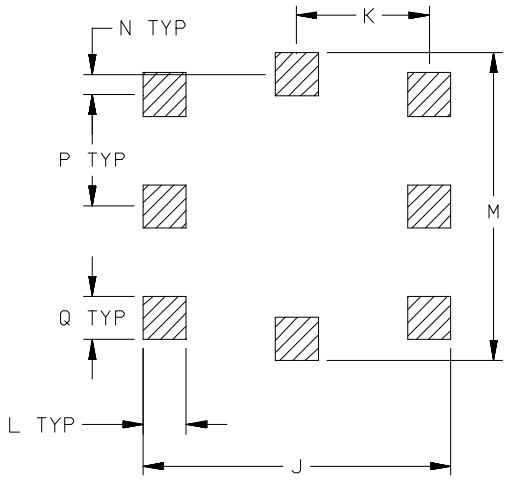
Case Style CZ

Outline Dimensions

CZ682



PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

METALLIZATION

SOLDER RESIST

CASE #	A	B	C	D	E	F	G	H	J	K
CZ682	.375 (9.52)	.375 (9.52)	.131 (3.33)	.188 (4.77)	.035 (0.89)	.033 (0.84)	.154 (3.91)	.050 (1.27)	.425 (10.80)	.183 (4.65)

CASE #	L	M	N	P	Q	WT. GRAM
CZ682	.060 (1.52)	.425 (10.80)	.028 (0.71)	.154 (3.91)	.060 (1.52)	.60

Dimensions are in inches (mm). Tolerances: 2Pl. ± .03; 3Pl. ± .010

Notes:

1. Case material: Nickel-Silver alloy.
2. Base: Printed wiring laminate.
3. Termination finish:
   For RoHS Case Styles: 3-5 µ inch (.08-.13 microns) Gold over 120-240 µ inch (3.05-6.10 microns) Nickel plate
   For RoHS-5 Case Styles: Tin-Lead plate.

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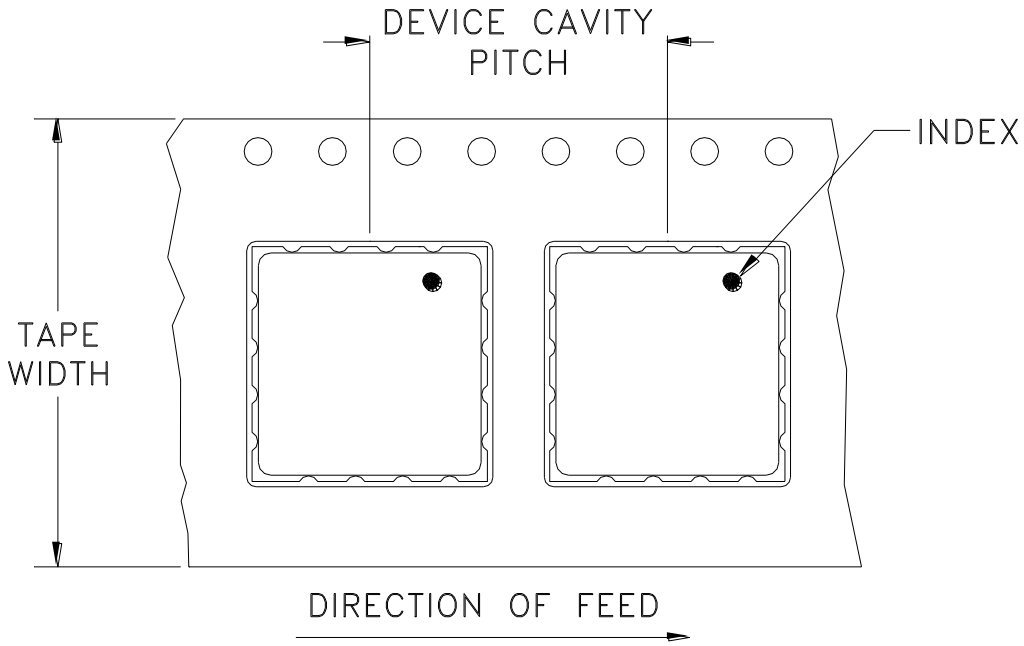
Tape & Reel Packaging TR-F60

DEVICE ORIENTATION IN T&R



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
24	16	7	Small quantity standards (see note)	10
				20
				50
				100
		13	Standard	200
				500
				1000

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf

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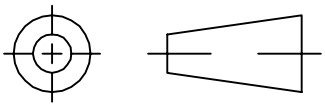
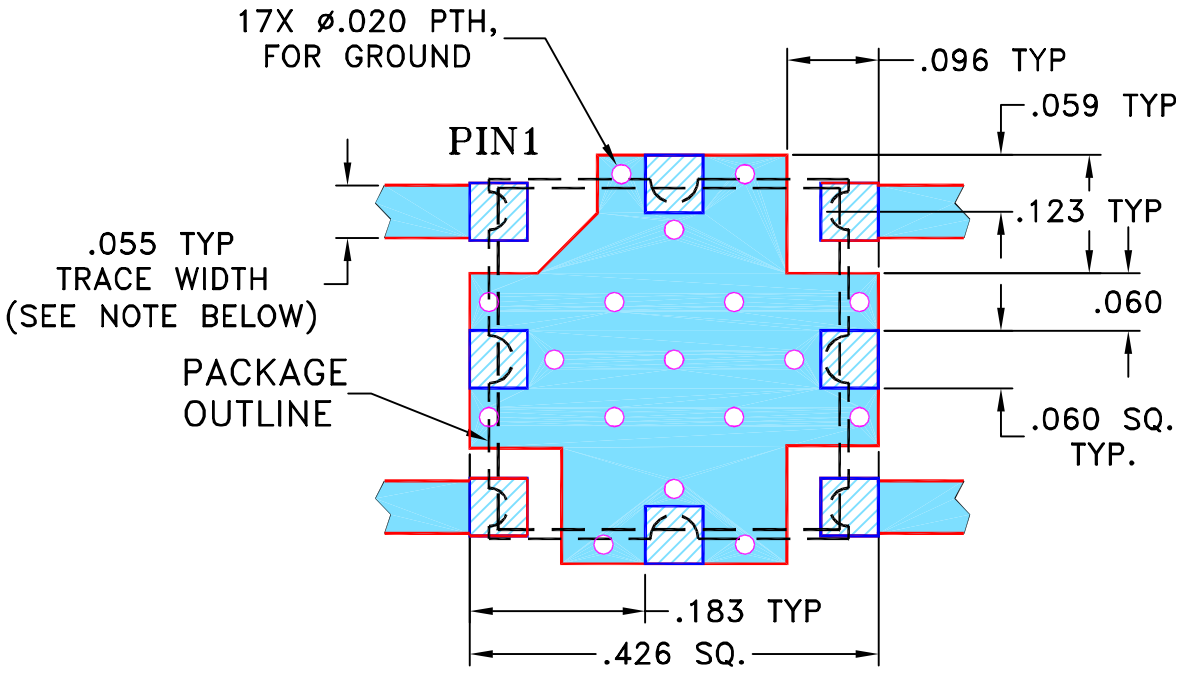
THIRD ANGLE PROJECTION



REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M76040	NEW RELEASE (FROM RAVON)	03/01	DK	HH
A	M82575	UPDATED DRAWING	08/08/02	GF	MM
B	M101142	ADDED NOTE 2 & TB-128 TO DESCRIPTION	10/10/05	MMG	MM
C	M102713	ADDED "...WITH SMOBC"	01/12/06	GF	IL

SUGGESTED MOUNTING CONFIGURATION FOR CZ682 CASE STYLE, "my" PIN CONNECTION



NOTE: 1. TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS 0.030" ± 0.002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	DK (RAV)	03/22/01
TOLERANCES ON:	CHECKED	DE (RAV)	03/27/01
2 PL DECIMALS ±	APPROVED	HH (RAV)	03/27/01
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

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ASHEETA1.DWG REV:A DATE:01/12/95

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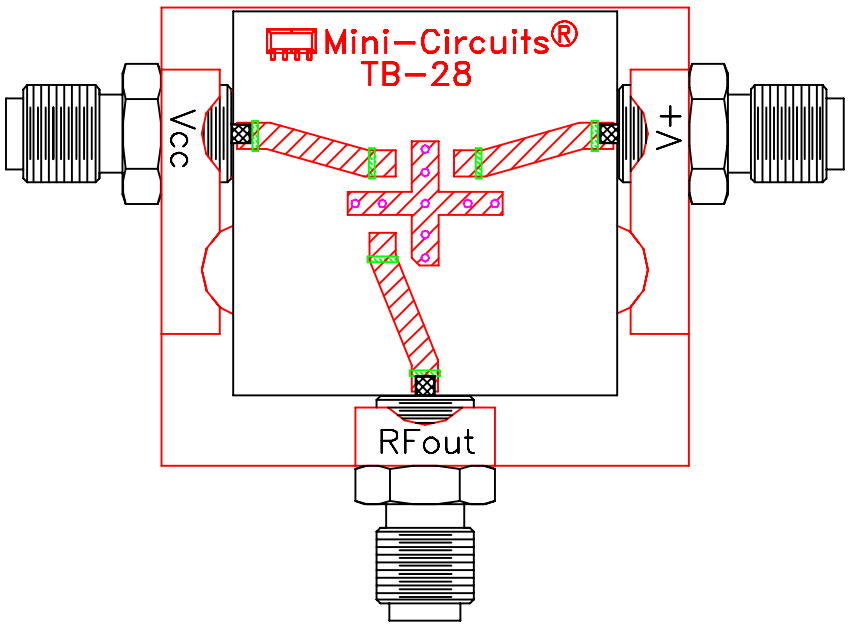
PL, my, CZ682, MOS, TB-128

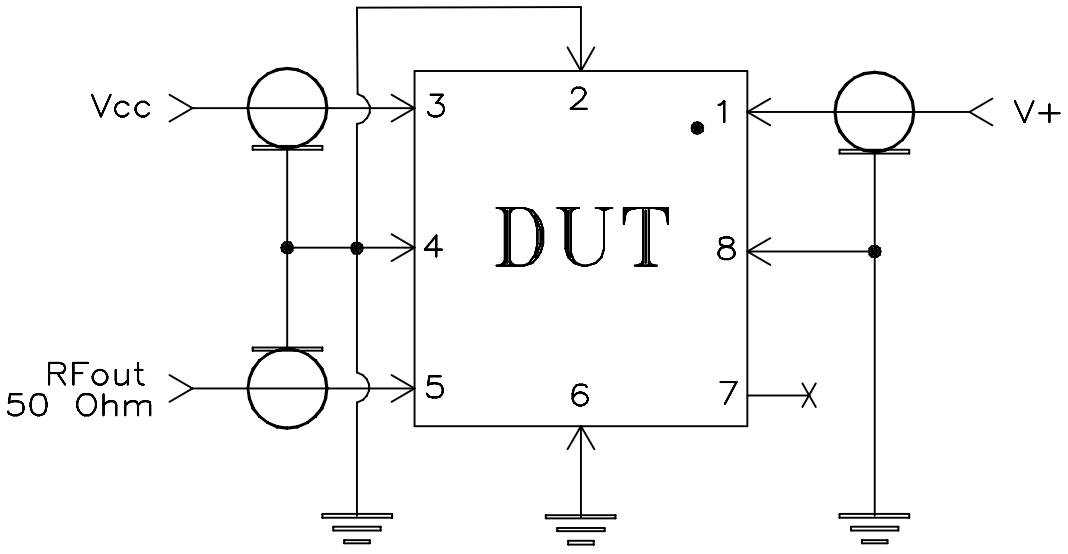
SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-023	C

FILE:	SCALE:	SHEET:
98PL023	5:1	1 OF 1

Evaluation Board and Circuit



TB-28



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.030 inch.

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