

The Big Deal:

- Ultra Flat Gain Response:
± 0.2 dB over 1200-1600 MHz
- Excellent Combination of gain,
P1dB, IP3 and NF
- 50Ω Input and Output:
no External Components Required



CASE STYLE: DL1636

Product Overview:

YSF-162+ is an advanced amplifier module in a Mini-Circuits System In Package **MSIP®**. This module is fully matched to 50Ω in/out impedance and has built-in Input & Output DC block capacitors. It is enclosed in a 5 x 6 mm MCLP plastic package. The YSF-162+ uses E-PHEMT technology enabling it to work with a single positive supply voltage.

Key Features

| Feature | Advantages |
|--|--|
| Superior Gain Flatness ± 0.2dB | The YSF-162+ provides industry leading gain flatness over both GPS satellite bands (1227 and 1575 MHz) making this ideal for use in applications where gain-flatness and repeatability are critical performance requirements. |
| High Gain | The YSF-162+ is a two-stage design with internal feedback and bias to provide flat 20 dB nominal gain, supporting applications where a single gain block must overcome large system losses such as long cable runs and lossy components. |
| Strong Combination of Performance | The YSF-162+ provides a strong combination of performance parameters including high gain (20 dB), high IP3 (+35 dBm) and P1dB (+20 dBm) and low noise figures (2.8 dB) that are difficult to achieve in a single stage design and available only in the YSF amplifier series. |
| Integrated Matching, DC Blocking and Bias in Small Package | The YSF-162+ includes all support circuits including: Matching, Bias and DC Blocking, all integrated into a single 5x6mm package making the total footprint equal to or smaller than most solutions. |
| Excellent Return Loss | The YSF-162+ includes integrated input and output matching circuits to make this amplifier a simple, complete drop-in solution. The matching circuits provide excellent output return loss (20dB), and are designed to give optimal P1dB and IP3 performance in a 50Ω environment. |
| High Reverse Isolation | With 30 dB of reverse isolation – the YSF-162+ is an ideal gain block for use in integrated systems to minimize VSWR interactions resulting from cascading highly reflective components such as sharp filters. |

Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
 B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
 C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp



MSIP® Mini-Circuits System In Package

Flat Gain Amplifier

1.2-1.6 GHz

Product Features

- Matched 50-ohm surface mount amplifier
- High gain, 20 dB typ.
- Up to +20 dBm typ. output power
- High IP3, +35 dBm
- Low Noise Figure, 3.2 dB typ.
- High directivity, 31 dB isolation
- Internal Input & Output DC Block
- Separate terminal for DC
- Protected by us patent 8,994,157



YSF-162+

CASE STYLE: DL1636

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

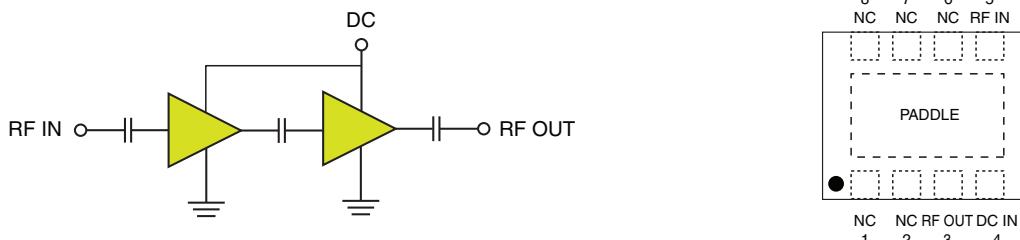
Typical Applications

- GPS
- Receivers & transmitters
- Radar

General Description

YSF-162+ is an advanced amplifier module in a Mini-Circuits System In Package **MSIP®**. This module is fully matched to 50Ω in/out impedance and has built-in Input & Output DC block capacitors. It is enclosed in a 5 x 6 mm MCLP plastic package. The YSF-162+ uses E-PHEMT* technology enabling it to work with a single positive supply voltage.

simplified schematic and pad description



| Function | Pad Number | Description |
|----------|------------|------------------------|
| RF-IN | 5 | RF Input |
| RF-OUT | 3 | RF Output |
| DC | 4 | DC Supply |
| GND | Paddle | Connected to ground |
| NOT USED | 1,2,6,7,8 | No internal connection |

*Enhancement mode Pseudomorphic High Electron Mobility Transistor

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Mini-Circuits®

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Characterization Test Circuit

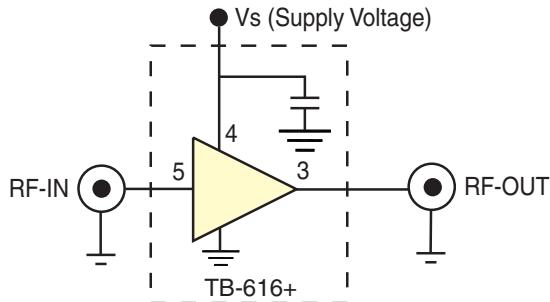


Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization Test Fixture TB-616+) Gain, Return loss, Output power at 1dB compression (P₁ dB), Output IP3 (OIP3) and Noise Figure measured using Agilent's N5242A PNA-X microwave network analyzer.

Conditions:

1. Gain: $P_{in} = -25\text{dBm}$
2. Output IP3 (OIP3): Two tones, spaced 10 MHz apart, 2.5 dBm/tone at output.

Recommended Application Circuit

(refer to evaluation board for PCB Layout and component values)

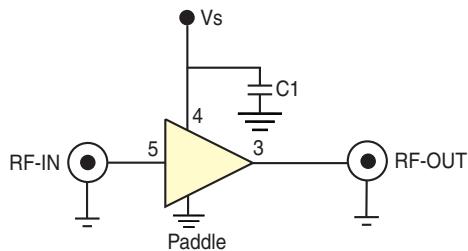
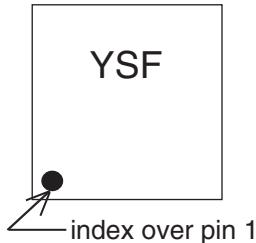


Fig 2. Recommended Application Circuit

Product Marking



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Additional Detailed Technical Information

additional information is available on our dash board. To access this information [click here](#)

| | |
|---|--|
| Performance Data | Data Table |
| | Swept Graphs |
| | S-Parameter (S2P Files) Data Set (.zip file) |
| Case Style | DL1636 Plastic package, exposed paddle, lead finish: tin/silver/nickel |
| Tape & Reel Standard quantities available on reel | F68 7" reels with 20, 50, 100, 200, 500 or 1K devices. 13" reels with 2K, or 4K devices. |
| Suggested Layout for PCB Design | PL-352 |
| Evaluation Board | TB-616-3+ |
| Environmental Ratings | ENV08T1 |

ESD Rating

Human Body Model (HBM): Class 1A in accordance with ANSI/ESD STM 5.1 - 2001

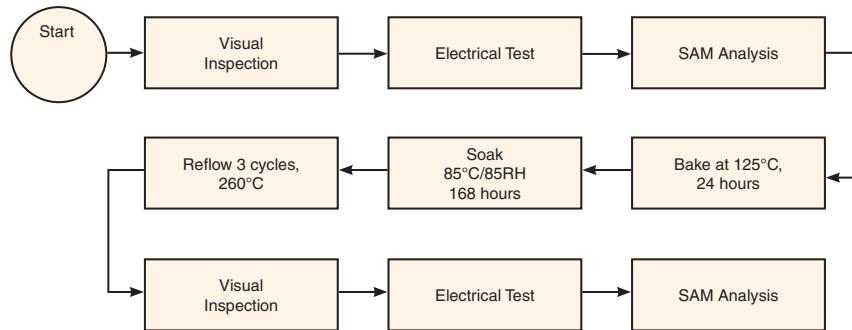
Machine Model (MM): Class M1 (25V) in accordance with ANSI/ESD STM5.2-1999

**Attention**

Observe precautions
for handling electrostatic
sensitive devices

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

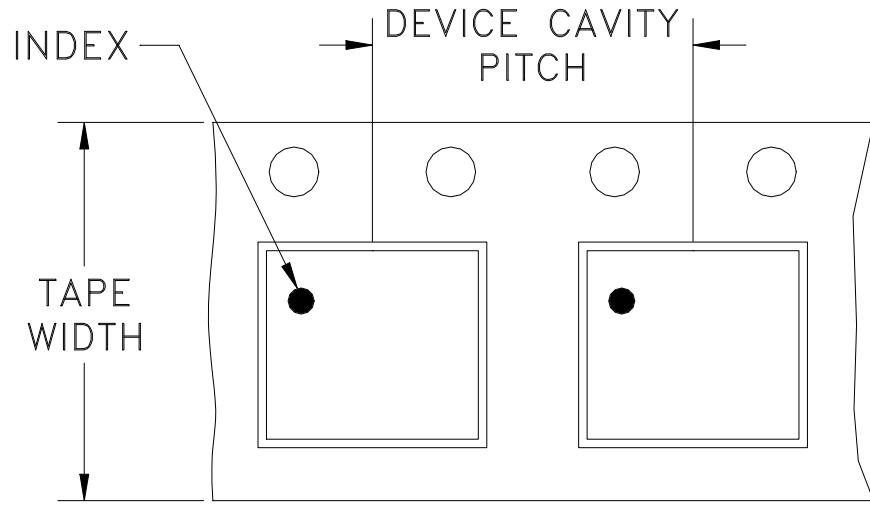
MSL Test Flow Chart**Notes**

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Tape & Reel Packaging TR-F68

DEVICE ORIENTATION IN T&R



DIRECTION OF FEED



| Tape Width, mm | Device Cavity Pitch, mm | Reel Size, inches | Devices per Reel see note | |
|----------------|-------------------------|-------------------|---------------------------|------------------------|
| 12 | 8 | 7 | Small quantity standard | 20 50 100 200 |
| | | | 500 | |
| | | | Standard | 1000 |
| | | | Standard | 2000 |
| | | 13 | Standard | 3000 |
| | | | Standard | 4000 |
| | | | | |
| | | | | |
| | | | | |

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



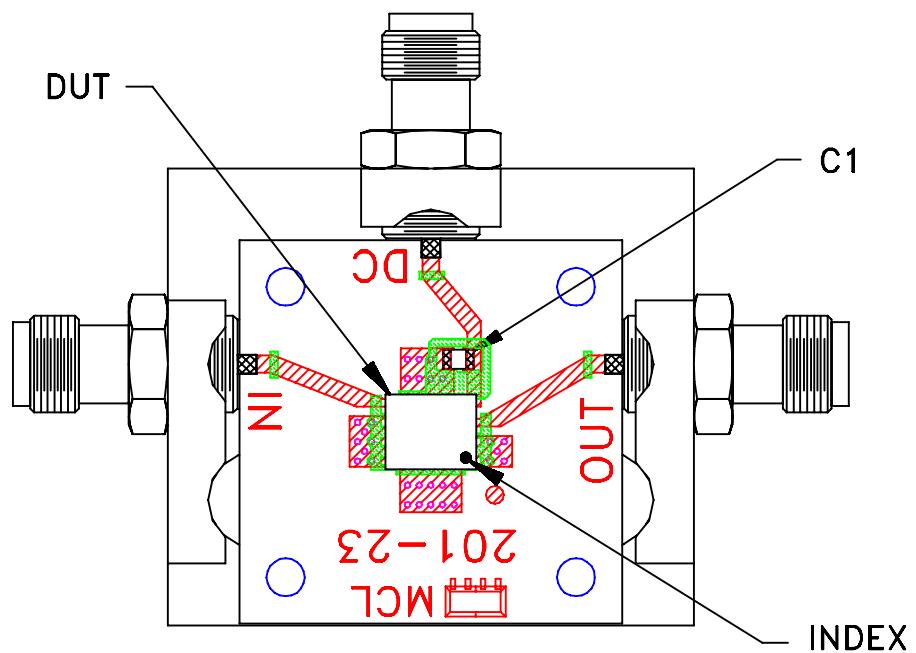
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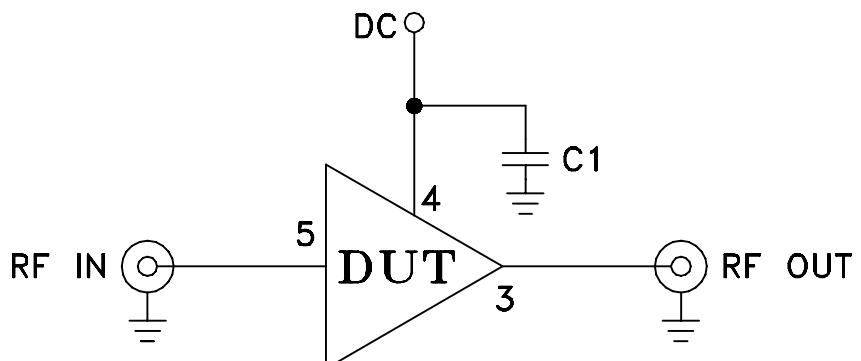
P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661

Mini-Circuits ISO 9001 & ISO 14001 Certified

Evaluation Board and Circuit



TB-616-3+



| COMPONENT | VALUE |
|-----------|----------|
| DUT | YSF-162+ |
| C1 | 1000 pF |

Schematic Diagram

Notes:

1. 50 Ohm SMA Female connectors.
2. PCB Material: RO4350 or equivalent,
Dielectric Constant=3.5, Thickness=.020 inch.

 Mini-Circuits®



Environmental Specifications

ENV08T1

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

| Specification | Test/Inspection Condition | Reference/Spec |
|--------------------------------|--|---|
| Operating Temperature | -40° to 85° C or -45° to 85° C or -55° to 105° C or -40° to 105° C or -40° to 95° C Ambient Environment | Individual Model Data Sheet |
| Storage Temperature | -55° to 100° C or -65° to 150° Ambient Environment | Individual Model Data Sheet |
| HTOL | 1000 hours at 125°C | MIL-STD-883, Method 1005, Condition B |
| Thermal Shock | -55° to 100°C, 100 cycles | MIL-STD-202, Method 107, Condition A-3, except +100°C |
| Mechanical Shock | 1.5Kg, 0.5 ms, 5 shock pulses, Y1 direction only | MIL-STD-883, Method 2002, Condition B, except Y1 direction only |
| Vibration (Variable Frequency) | 50g peak | MIL-STD-883, Method 2007, Condition B |
| Autoclave | 15 psig, 100% RH, 121°C, 96 hours | JESD22-A102, Condition C |
| HAST | 130°C, 85% RH, 96 hours | JESD22-A110 |
| Solderability | 10X Magnification | J-STD-002, Para 4.2.5, Test S, 95% Coverage |
| Solder Reflow Heat | Sn-Pb Eutetic Process: 240°C peak Pb-Free Process: 260°C peak | J-STD-020, Table 4-1, 4-2 and 5-2; Figure 5-1 |
| Moisture Sensitivity: Level 1 | Bake at 125°C for 24 hours Soak at 85°C/85% RH for 168 hours, Reflow 3 cycles at 260°C peak | J-STD-020 |



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| Specification | Test/Inspection Condition | Reference/Spec |
|--------------------------------|---|-------------------------|
| Marking Resistance to Solvents | Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + propylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C | MIL-STD-202, Method 215 |