

Engineering Development Model

Power Splitter/Combiner

ZFSC-ED6436

32 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : 99-01-567

ELECTRICAL SPECIFICATIONS 75Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		950		1750 MHz
Isolation	950 - 1750 MHz		22	
Total Loss Above 15.1 dB	950 - 1750 MHz		0.40	
Amplitude Unbalance	950 - 1750 MHz		0.343	
VSWR	SUM Port		1.30	(:1)
	OUT Ports		1.20	(:1)

MAXIMUM RATINGS	
Operating Temperature	-55°C to 100°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	S
PORT 1	1
PORT 2	2
...	...
PORT 32	32

Functional Diagram



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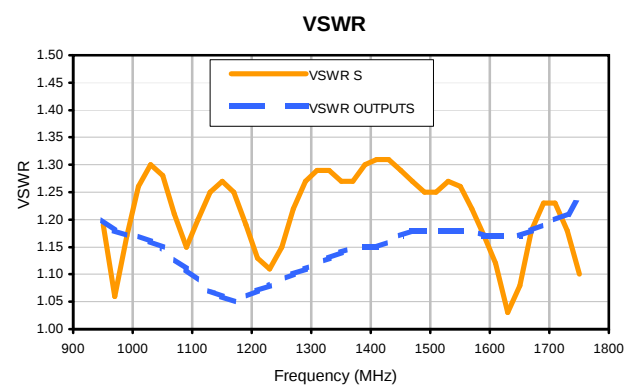
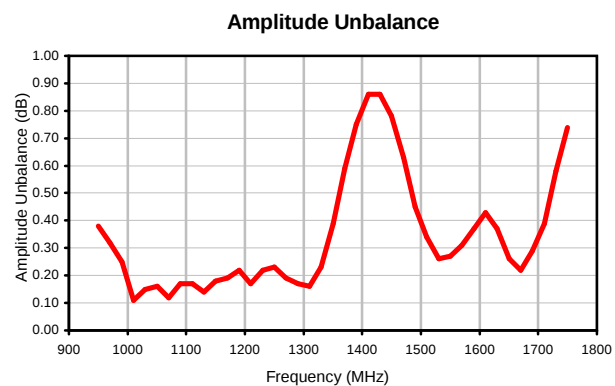
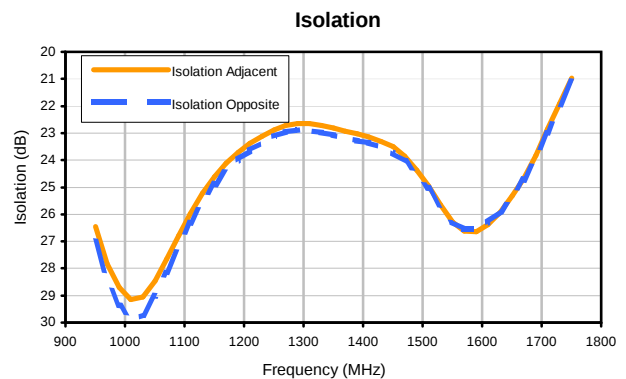
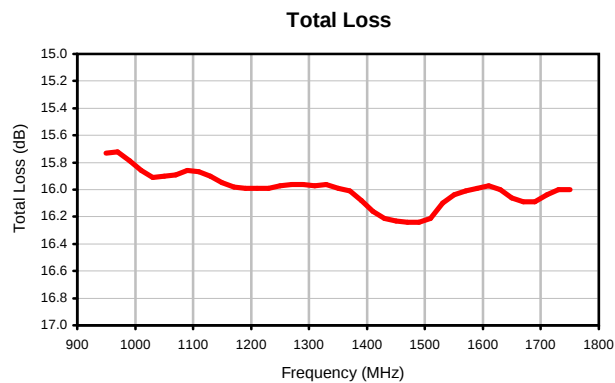
Typical Performance Data

FREQ. (MHz)	TOTAL LOSS ¹ (dB)	AMP. UNBAL. (dB)	ISOLATION (dB)		FREQ. (MHz)	VSWR (:1)	
			Adjacent	Opposite		S	OUTPUTS
950.0	15.73	0.38	26.46	26.99	950.0	1.19	1.20
970.0	15.72	0.32	27.82	28.41	970.0	1.06	1.18
990.0	15.79	0.25	28.71	29.38	990.0	1.17	1.17
1010.0	15.86	0.11	29.15	29.91	1010.0	1.26	1.17
1030.0	15.91	0.15	29.05	29.71	1030.0	1.30	1.16
1050.0	15.90	0.16	28.47	29.04	1050.0	1.28	1.15
1070.0	15.89	0.12	27.67	28.16	1070.0	1.21	1.13
1090.0	15.86	0.17	26.79	27.23	1090.0	1.15	1.11
1110.0	15.87	0.17	25.98	26.31	1110.0	1.20	1.09
1130.0	15.90	0.14	25.23	25.52	1130.0	1.25	1.07
1150.0	15.95	0.18	24.61	24.89	1150.0	1.27	1.06
1170.0	15.98	0.19	24.12	24.39	1170.0	1.25	1.05
1190.0	15.99	0.22	23.72	23.98	1190.0	1.19	1.06
1210.0	15.99	0.17	23.38	23.63	1210.0	1.13	1.07
1230.0	15.99	0.22	23.11	23.36	1230.0	1.11	1.08
1250.0	15.97	0.23	22.89	23.13	1250.0	1.15	1.09
1270.0	15.96	0.19	22.73	22.96	1270.0	1.22	1.10
1290.0	15.96	0.17	22.65	22.89	1290.0	1.27	1.11
1310.0	15.97	0.16	22.65	22.91	1310.0	1.29	1.12
1330.0	15.96	0.23	22.72	22.98	1330.0	1.29	1.13
1350.0	15.99	0.39	22.81	23.07	1350.0	1.27	1.14
1370.0	16.01	0.59	22.93	23.18	1370.0	1.27	1.15
1390.0	16.08	0.75	23.03	23.28	1390.0	1.30	1.15
1410.0	16.16	0.86	23.15	23.36	1410.0	1.31	1.15
1430.0	16.21	0.86	23.31	23.52	1430.0	1.31	1.16
1450.0	16.23	0.78	23.51	23.73	1450.0	1.29	1.17
1470.0	16.24	0.63	23.86	24.05	1470.0	1.27	1.18
1490.0	16.24	0.45	24.38	24.51	1490.0	1.25	1.18
1510.0	16.21	0.34	24.94	25.04	1510.0	1.25	1.18
1530.0	16.10	0.26	25.65	25.71	1530.0	1.27	1.18
1550.0	16.04	0.27	26.28	26.28	1550.0	1.26	1.18
1570.0	16.01	0.31	26.62	26.56	1570.0	1.22	1.18
1590.0	15.99	0.37	26.64	26.54	1590.0	1.17	1.17
1610.0	15.97	0.43	26.38	26.28	1610.0	1.12	1.17
1630.0	16.00	0.37	25.93	25.88	1630.0	1.03	1.17
1650.0	16.06	0.26	25.33	25.33	1650.0	1.08	1.17
1670.0	16.09	0.22	24.68	24.73	1670.0	1.18	1.18
1690.0	16.09	0.29	23.84	23.92	1690.0	1.23	1.19
1710.0	16.04	0.39	22.87	23.00	1710.0	1.23	1.20
1730.0	16.00	0.58	21.91	22.02	1730.0	1.18	1.21
1750.0	16.00	0.74	20.98	21.09	1750.0	1.10	1.24

¹ Total Loss = Insertion Loss + 15.1dB Splitter Loss

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Typical Performance Curves





All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 100°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Barometric Pressure	100,000 Feet	MIL-STD-202, Method 105, Condition D
Humidity	90% RH, 65°C Units may require bake-out after humidity to restore full performance.	MIL-STD-202, Method 103
Thermal Shock	-65° to 125°C, 5 cycles	MIL-STD-202, Method 107, Condition B
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	100g, 6ms sawtooth, 3 shocks each direction 3 axes (total 18)	MIL-STD-202, Method 213, Condition I