

Application Note (AN-60-033)

Enhanced Linearity in the HELA-10 Power Amplifier

1.0 Introduction

To better satisfy the high linearity requirements of multiple-carrier wideband communication systems such as CATV when using Mini-Circuits model HELA-10, a combination of techniques has been found advantageous:

- Biasing at a higher DC current for better second- and third-order intermodulation suppression.
- Improving symmetry in the application circuit for better second-harmonic cancellation and further second-order intermodulation suppression.

Application note AN-60-009 included a description of how 2nd order and 3rd order intermodulation intercepts (IP2 and IP3) are improved by operating at higher current in a 50-ohm system, and explained how the balanced amplifier configuration suppresses even-order harmonics and even-order intermodulation products. This new application note extends the work of AN-60-009 by showing how intermodulation performance is improved using specially designed transformers optimized for symmetry, both at normal current and at the higher current. It presents typical performance in a 75-ohm system as used in the CATV industry.

2.0 Application Circuit

A schematic diagram of the application circuit used to obtain the results reported herein is given in Figure 1. It is designed for use in a 75-ohm system. The components are surface-mount. Bias resistors R2 and R3, which increase the DC current typically from 380mA to 520mA with 12V supply, are the same values as used in Section 4.2 of AN-60-009 for that purpose.

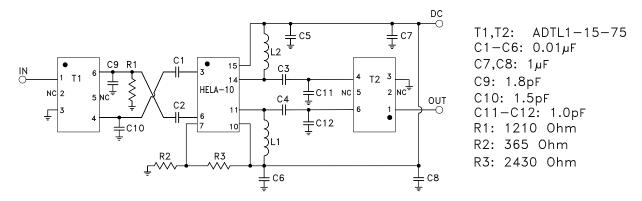


Figure 1 – Schematic Diagram with Bias Set for Higher Current



3.0 Tests Performed

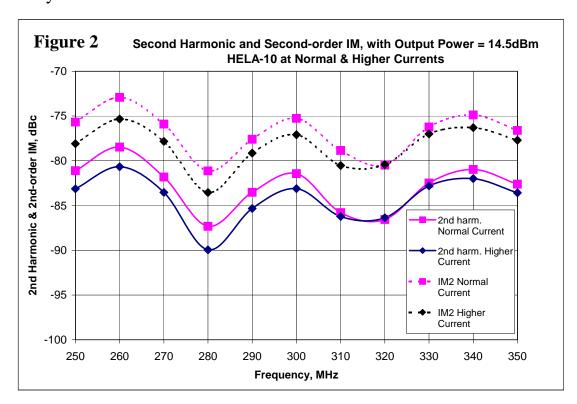
Linearity testing was done with 2 tones 1MHz apart, swept over the frequency range 250 to 350MHz. Second- and third-order intermodulation products as well as second-harmonic power were measured. The significant 2^{nd} order intermodulation product is the sum frequency 500 to 700MHz which, together with the 2^{nd} harmonic, falls within the 50-850MHz CATV band. The output power at each tone was 14.5dBm.

In addition, the following characteristics were measured over the ranges 10 – 50MHz, 50 – 850MHz, and 850 – 1400MHz: Gain, Isolation, Input and Output VSWR, and Output Power at 0.5dB and 1.0dB compression.

All of these tests were done with 12V supply, utilizing the circuit in Figure 1 and 75-ohm instrumentation.

4.0 Results

Figure 2 shows the typical power in each second harmonic as well as the typical power in the sum-frequency 2^{nd} intermodulation product, relative to the power in each output tone. Up to 2.5dB advantage is obtained using the higher current. Note also that there is a 6dB difference between the 2^{nd} harmonic and the 2^{nd} order IM product. This is explained by the following analysis.





Consider a signal V_{IN} consisting of 2 equal-amplitude cosine waves $\cos \omega_1 t$ and $\cos \omega_2 t$ that is inputted to a device having a nonlinear response:

$$V_{OUT} = a_1 V_{IN} + a_2 V_{IN}^2 + a_3 V_{IN}^3 + \cdots$$

Ignoring 4th order and higher terms, the response is:

$$\begin{aligned} V_{OUT} &= a_1 \left(\cos \omega_1 t + \cos \omega_2 t\right) + a_2 \left(\cos \omega_1 t + \cos \omega_2 t\right)^2 + \\ &\quad a_3 \left(\cos \omega_1 t + \cos \omega_2 t\right)^3 \end{aligned}$$

Expanding the squared term:

$$a_{2} (\cos \omega_{1}t + \cos \omega_{2}t)^{2} = a_{2} \cos^{2}\omega_{1}t + a_{2} \cos^{2}\omega_{2}t + 2a_{2} \cos \omega_{1}t \cos \omega_{2}t$$

$$= a_{2} \left[1 + \frac{1}{2} \cos 2\omega_{1}t + \frac{1}{2} \cos 2\omega_{2}t + \cos (\omega_{1} + \omega_{2})t + \cos (\omega_{1} - \omega_{2})t \right]$$

Note that the second-harmonic terms (for frequencies $2\omega_1$ and $2\omega_1$) inside the brackets of this equation have coefficient ½, while the second-order intermodulation terms (for the sum and difference frequencies $\omega_1 + \omega_2$ and $\omega_1 - \omega_2$) have coefficient 1. This shows that each of the second-order intermodulation products (IM2) is 6dB greater than each second-harmonic component in the output spectrum.

When the two test tones have frequencies close together, 300 and 301MHz for example (representing adjacent channels in a multi-carrier signal), the output spectrum in the second-harmonic region will contain the sum frequency 601MHz surrounded by the two second-harmonic components 600 and 602MHz, which are each 6dB below the 601 MHz component.

Figure 3 shows second-order intermodulation intercept, IP2, calculated from the intermodulation data.



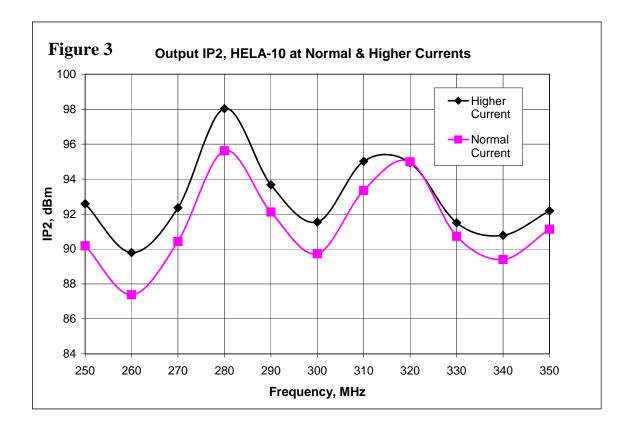


Table 2 compares the above IP2 results with AN-60-009 (the normal current in Figure 18 and the higher current in Figure 43, of AN-60-009). The present work shows typically 4 to 13dB better performance.

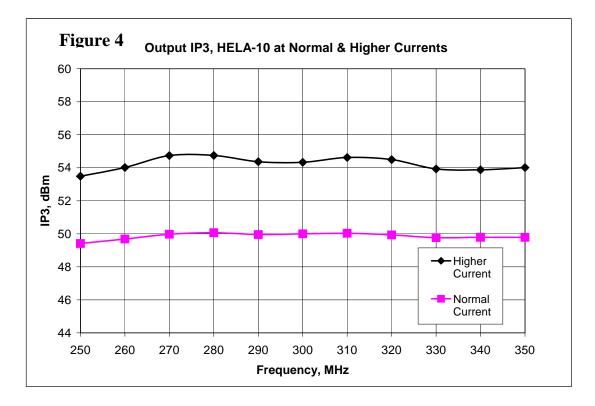
	At normal current	At higher current
The present work	87.5 to 95.5dBm	90 to 98dBm
AN-60-009	82dBm	86dBm

Table 2 - IP2 Comparison at 250 - 350MHz

The advantage of up to 2.5 dB afforded by the higher current is on top of the advantage gained by the more symmetrical application circuit.



Typical third-order intercept is shown in Figure 4. Operating at the higher current yields 4dB better performance.



Gain and reverse isolation are shown Figure 5. Gain is essentially flat from 50 to 1000MHz. Directivity, which is the dB-difference between isolation and gain, is typically 7dB.

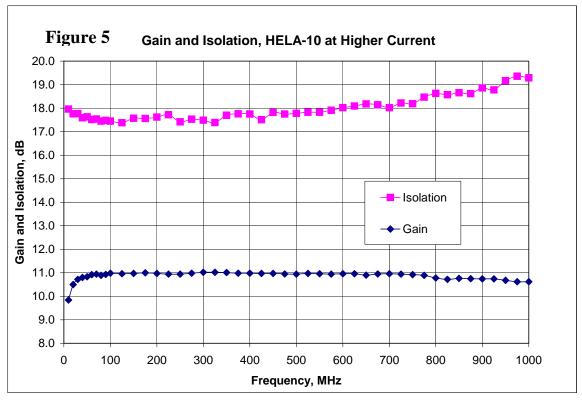
HELA-10 in its high-symmetry application circuit ensures excellent match to 75 ohms, as shown in Figure 6. Mid-band VSWR is typically 1.05:1. Over the range 50 – 850MHz it rises typically to 1.3:1 at the input and 1.2:1 at the output.

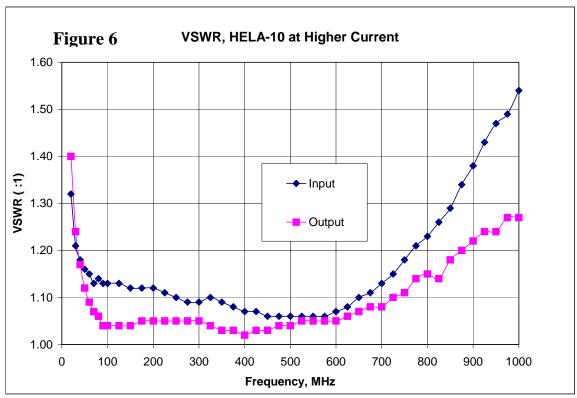
To complete the picture, power output at 1-dB compression is shown in Figure 7. In mid-band typical P1dB is 31dBm, and over 50 – 850MHz it remains above 29.5dBm.

5.0 Conclusion

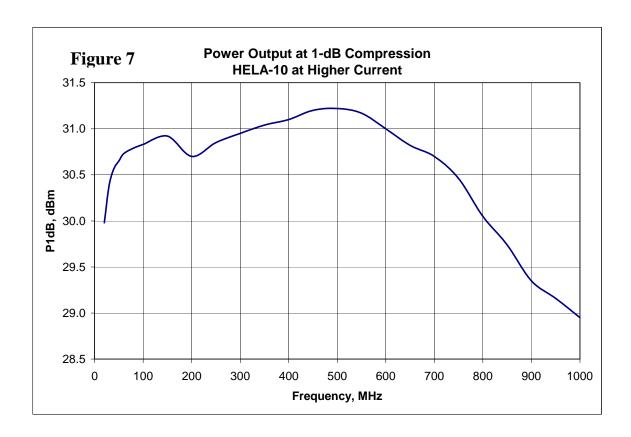
Substantially improved performance has been demonstrated using an application circuit having enhanced symmetry that provides superior cancellation of second-order distortion. This is in addition to improvement in both second-order and third-order distortion performance achieved by biasing at a higher operating current.













IMPORTANT NOTICE

© 2015 Mini-Circuits

This document is provided as an accommodation to Mini-Circuits customers in connection with Mini-Circuits parts only. In that regard, this document is for informational and guideline purposes only. Mini-Circuits assumes no responsibility for errors or omissions in this document or for any information contained herein.

Mini-Circuits may change this document or the Mini-Circuits parts referenced herein (collectively, the "Materials") from time to time, without notice. Mini-Circuits makes no commitment to update or correct any of the Materials, and Mini-Circuits shall have no responsibility whatsoever on account of any updates or corrections to the Materials or Mini-Circuits' failure to do so.

Mini-Circuits customers are solely responsible for the products, systems, and applications in which Mini-Circuits parts are incorporated or used. In that regard, customers are responsible for consulting with their own engineers and other appropriate professionals who are familiar with the specific products and systems into which Mini-Circuits' parts are to be incorporated or used so that the proper selection, installation/integration, use and safeguards are made. Accordingly, Mini-Circuits assumes no liability therefor.

In addition, your use of this document and the information contained herein is subject to Mini-Circuits' standard terms of use, which are available at Mini-Circuits' website at www.minicircuits.com/homepage/terms_of_use.html.

Mini-Circuits and the Mini-Circuits logo are registered trademarks of Scientific Components Corporation d/b/a Mini-Circuits. All other third-party trademarks are the property of their respective owners. A reference to any third-party trademark does not constitute or imply any endorsement, affiliation, sponsorship, or recommendation: (i) by Mini-Circuits of such third-party's products, services, processes, or other information; or (ii) by any such third-party of Mini-Circuits or its products, services, processes, or other information.