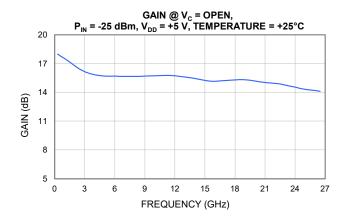
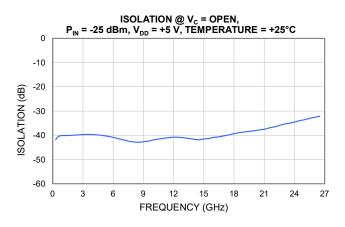
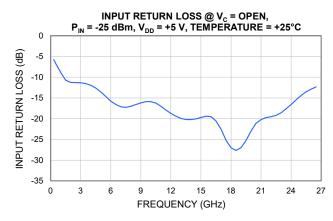
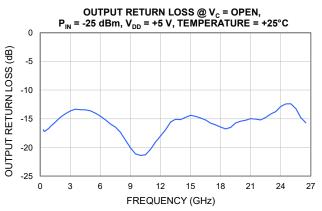
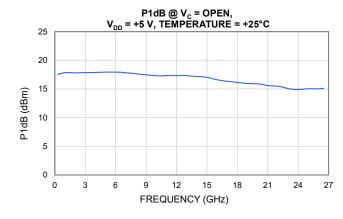
### TYPICAL PERFORMANCE GRAPHS WITH $V_{DD}$ = +5 V AND $V_{D}$ = OPEN

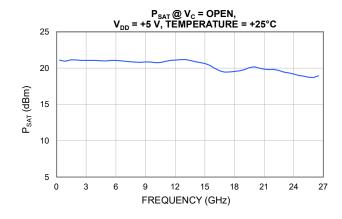




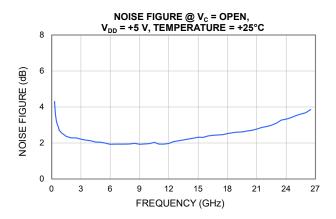






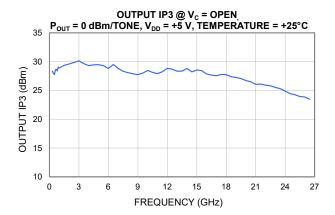


### TYPICAL PERFORMANCE GRAPHS WITH $V_{DD}$ = +5 V AND $V_{D}$ = OPEN



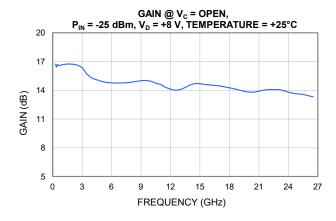
#### TYPICAL PERFORMANCE GRAPHS WITH $V_{DD} = +5 \text{ V}$ AND $V_{D} = \text{OPEN}$

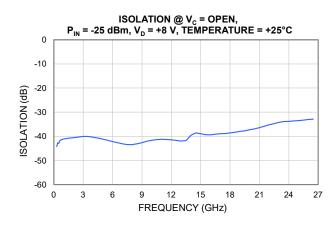
Note: All data in this section represents the Die attached in a 5x5 mm 32-Lead QFN style package and measured on Mini-Circuits Characterization Test Board TB-PVGA-273C+

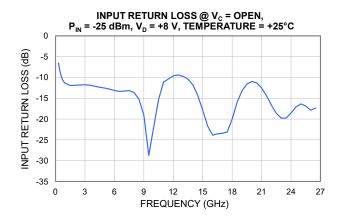


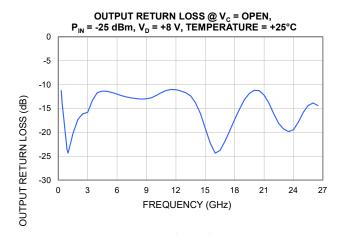
#### TYPICAL PERFORMANCE GRAPHS WITH $V_D = +8 \text{ V}$ AND $V_{DD} = OPEN$

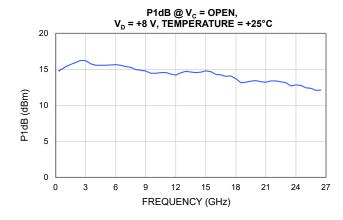
Note: All data on this page represents the Die attached in a 5x5 mm 32-Lead QFN style package and measured on Mini-Circuits Characterization Test Board TB-PVGA-273C+

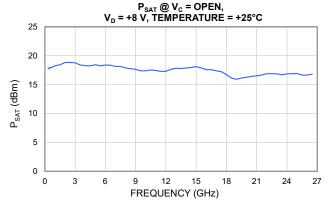












### TYPICAL PERFORMANCE GRAPHS WITH $V_D = +8 \text{ V}$ AND $V_{DD} = OPEN$

Note: All data on this page represents the Die attached in a 5x5 mm 32-Lead QFN style package and measured on Mini-Circuits Characterization Test Board TB-PVGA-273C+

