



REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	ECO-004682	NEW RELEASE	10/24/20	ITG	SL

The diagram shows a top-down view of a microstrip package. It features four pins arranged in a square pattern, each with a circular pad. The pins are labeled "PIN 1" and "PIN 2". The pads are labeled ".028 [0.71] LINE WIDTH, 4 PL. (SEE NOTE BELOW)". The package outline is shown as a dashed rectangle. A central area is labeled "PACKAGE OUTLINE". A small circle at the bottom center is labeled " ϕ .014 [ϕ 0.36] PTH FOR GROUND".

PIN 1

PIN 2

.028 [0.71]
LINE WIDTH, 4 PL.
(SEE NOTE BELOW)

PACKAGE OUTLINE

ϕ .014 [ϕ 0.36] PTH FOR GROUND

1. LINE WIDTH IS SHOWN FOR FR4 IT-180A, DIELECTRIC THICKNESS: .016±.001; COPPER: 1/2 OZ. ON EACH SIDE. FOR OTHER MATERIALS LINE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

UNLESS OTHERWISE SPECIFIED	INITIALS		DATE
DIMENSIONS ARE IN INCHES	DRAWN	ITG	10/23/20
TOLERANCES ON:	CHECKED	GF	10/23/20
2 PL DECIMALS ±	APPROVED	SL	10/24/20
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

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