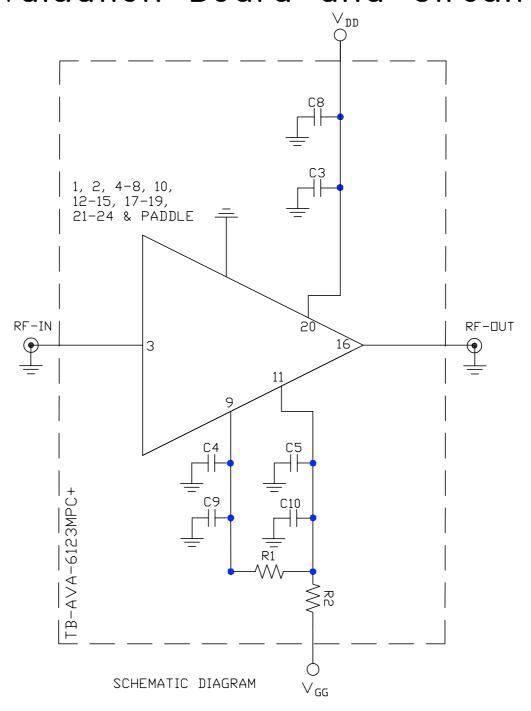
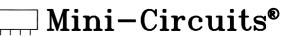
Evaluation Board and Circuit



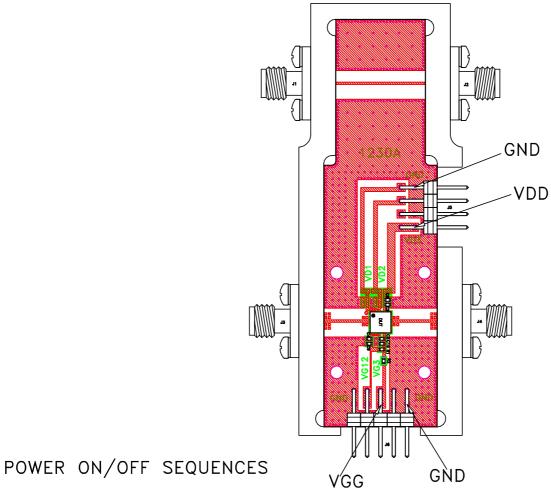
Component	Size	Value	PartNumber	Manufacturer
R1, R2	0402	100Ω	RK73H1ETTP1000F	KOA SPEER ELECTRONICS
C3-C5	0402	0.001µF	GRM1555C1H102JA01D	MURATA
C8-C10	0402	0.1µF	GRM155R71E104KE14D	MURATA

Notes:

- 1. 2.92mm Female Connectors.
- 2. PCB Material: Roger R04003C LOPRO or equivalent, Thickness=0.0087±.001 inch



TEST BIASING SEQUENCES



<u>Caution:</u> Permanent damage to the device will occur if the Power ON and Power OFF sequences are not followed.

POWER ON

- 1. Set VGG = -2V. Apply VGG
- 2. Set VDD= +5V. Apply VDD
- 3. Increase VGG to obtain desired IDD=140mA
- 4. Apply RF Signal

POWER OFF

- 1. Turn OFF RF Signal
- 2. Adjust VGG down to -2V
- 3. Turn OFF VDD
- 4. Turn OFF VGG

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	ASHEETA2.DWG REV:A DATE: 01/12/94	FILE:	WTB-AVA-6123M	PC+	SCALE:	4:1	SHEET:	5	OF	5