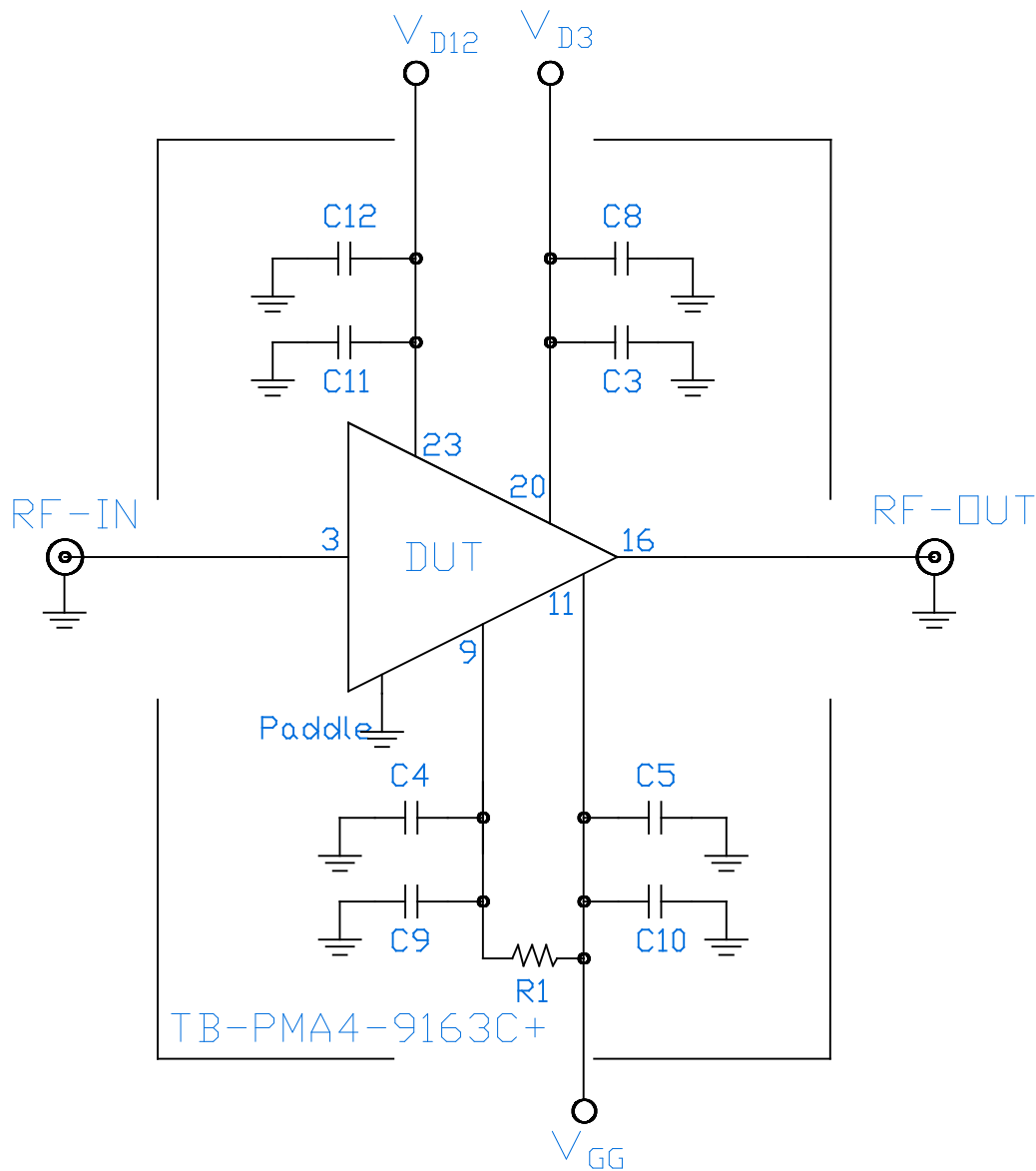


Evaluation Board and Circuit



SCHEMATIC DIAGRAM

Component	Size	Value	PartNumber	Manufacturer
C3,C4,C5,C11	0402	1nf	GRM1555C1H102JA01D	MURATA
C8,C9,C10,C12	0402	0.1 μ F	GRM155R71E104KE14D	MURATA
R1	0402	100 Ω	RK73H1ETTP1000F	KOA SPEER ELECTRONICS

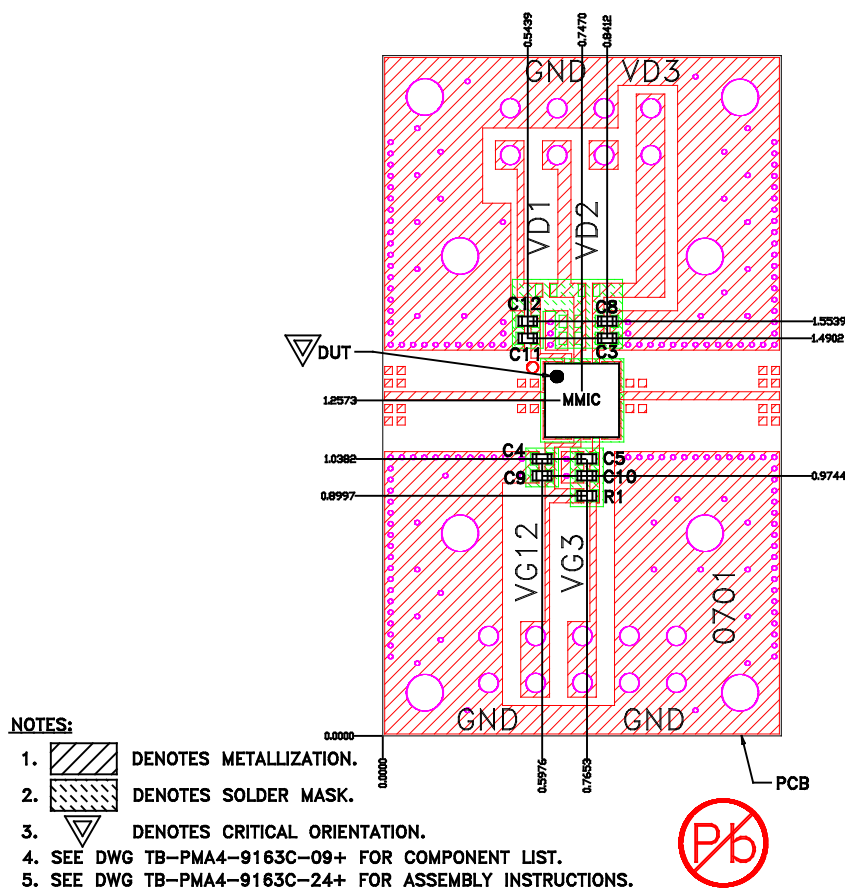
Notes:

- 2.92mm Female Connectors.
- PCB Material: Roger RO4003C LOPRO or equivalent, Thickness=0.0087 $\pm$.001 inch



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TEST BIASING SEQUENCES



POWER ON/OFF SEQUENCES

Caution: Permanent damage to the device will occur if the Power ON and Power OFF sequences are not followed.

POWER ON

1. Set VGG = -2V and Turn ON.
2. Set VD12 = +5V and Turn ON.
3. Set VD3 = +5V and Turn ON.
4. Increase VGG to desired nominal I_{DD} =145mA.
5. Turn ON RF Signal.

POWER OFF

6. Turn OFF RF Signal
7. Decrease VGG to -2V.
8. Turn OFF VD12, VD3.
9. Turn OFF VGG.

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SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	TB-PMA4-9163C-20+	OR
FILE:	WTB-AVA-6123MPC+	SCALE: 4:1	SHEET: 8 OF 8