

Engineering Development Model

Power Splitter/Combiner* 2RACK-2-ED13184/3

*2 Splitters

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE :99-01-1314

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		500		1300 MHz
Isolation	500-1300 MHz		25	dB
Insertion Loss Above 3.0 dB	500-1300 MHz		0.20	dB
Phase Unbalance	500-1300 MHz		0.15	deg.
Amplitude Unbalance	500-1300 MHz		0.05	dB
VSWR	SUM Port		1.21	(:1)
	OUT Ports		1.06	(:1)

MAXIMUM RATINGS	
Operating Temperature	-5°C to 85°C
Storage Temperature	-40°C to 85°C

COAXIAL CONNECTIONS	
SUM PORT (LOW POWER)	J1
PORT 1 (LOW POWER)	J2
PORT 2 (LOW POWER)	J3
SUM PORT (HIGH POWER)	J4
PORT 1 (HIGH POWER)	J5
PORT 2 (HIGH POWER)	J6

Functional Diagram

