

Engineering Development Model

Power Splitter/Combiner

ADP-ED12390/1

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

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CASE STYLE : 99-01-716

ELECTRICAL SPECIFICATIONS 75Ω @ +25°C					
Parameter		Min.	Typ.	Max.	Units
Frequency		5		1000	MHz
Isolation	5 - 50 MHz		26		dB
	50 - 500 MHz		26		dB
	500 - 1000 MHz		29		dB
Insertion Loss Above 3.0 dB	5 - 50 MHz		0.30		dB
	50 - 500 MHz		0.40		dB
	500 - 1000 MHz		0.70		dB
Phase Unbalance	5 - 50 MHz		0.008		deg.
	50 - 500 MHz		0.128		deg.
	500 - 1000 MHz		0.790		deg.
Amplitude Unbalance	5 - 50 MHz		0.021		dB
	50 - 500 MHz		0.015		dB
	500 - 1000 MHz		0.012		dB
VSWR	SUM Port		1.10		(:1)
	OUT Ports		1.11		(:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	6
PORT 1	4
PORT 2	3
GND EXT	1
NOT USED	2, 5

Functional Diagram

