Engineering Development Model

Power Splitter/Combiner ADP-ED12786/10

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : CD636

ELECTRICAL SPECIFICATIONS 75Ω @ +25°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		5		2750	MHz
Isolation	**5 - 50 MHz**		22		dB
	50 - 1375 MHz		19		dB
	1375 - 2750 MHz		18		dB
Insertion Loss Above 3.0 dB	**5 - 50 MHz**		0.87		dB
	50 - 1375 MHz		0.65		dB
	1375 - 2750 MHz		1.56		dB
Phase Unbalance	**5 - 50 MHz**		0.010		deg.
	50 - 1375 MHz		0.338		deg.
	1375 - 2750 MHz		7.815		deg.
Amplitude Unbalance	**5 - 50 MHz**		0.000		dB
	50 - 1375 MHz		0.156		dB
	1375 - 2750 MHz		0.444		dB
VSWR	**SUM Port**		1.40		(:1)
	OUT Ports		1.40		(:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	1
PORT 1	4
PORT 2	3
GND	6
SHORT	2, 5
Pins#2,5 to GND	C1 = C2 = 0.3 pF
Pin 4	L1: 3.3 nH to R1 one side
Pin 3	L2: 3.3 nH to R1 another side
R1	200Ω

Functional Diagram