

Engineering Development Model

Power Splitter/Combiner

ADP-ED7369/1

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : CD542

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		3		2400 MHz
Isolation	3 - 30 MHz		18	dB
	30 - 1200 MHz		18	dB
	1200 - 2400 MHz		19	dB
Insertion Loss Above 3.0 dB	3 - 30 MHz		0.52	dB
	30 - 1200 MHz		0.62	dB
	1200 - 2400 MHz		0.88	dB
Phase Unbalance	3 - 30 MHz		0.043	deg.
	30 - 1200 MHz		0.579	deg.
	1200 - 2400 MHz		0.568	deg.
Amplitude Unbalance	3 - 30 MHz		0.018	dB
	30 - 1200 MHz		0.027	dB
	1200 - 2400 MHz		0.060	dB
VSWR	SUM Port		1.82	(:1)
	OUT Ports		1.51	(:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	1
PORT 1	3
PORT 2	4
GND EXT	6
NOT USED	2, 5

Functional Diagram

