Power Splitter/Combiner

ADP-ED7705/1

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Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE: CJ725

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C						
Parameter		Min.	Тур.	Max.	Units	
Frequency		0.2		640	MHz	
Isolation	.2-2 MHz		35		dB	
	2-320 MHz		36		dB	
	320-640 MHz		26		dB	
Insertion Loss Above 6.0 dB	.2-2 MHz		0.73		dB	
	2-320 MHz		0.50		dB	
	320-640 MHz		1.10		dB	
Phase Unbalance	.2-2 MHz		0.134		deg.	
	2-320 MHz		0.666		deg.	
	320-640 MHz		3.867		deg.	
Amplitude Unbalance	.2-2 MHz		0.024		dB	
	2-320 MHz		0.052		dB	
	320-640 MHz		0.465		dB	
VSWR	SUM Port		1.24		(:1)	
	OUT Ports		1.23		(:1)	

MAXIMUM RATINGS				
Operating Temperature	-40°C to 85°C			
Storage Temperature	-55°C to 100°C			

PIN CONNECTIONS				
SUM PORT	2			
PORT 1	8			
PORT 2	7			
PORT 3	6			
PORT 4	5			
GROUND	1,3,4			

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		PORT 1
PORT S		PORT 2
0	⊢ [PORT 3
		PORT 4



