

Engineering Development Model

Power Splitter/Combiner

ADPQ-ED7723/1

2 Way-90°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE : CD542

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		544		727 MHz
Isolation	544 - 727 MHz	22	29	
Insertion Loss				
Average of Coupled Outputs	544 - 727 MHz		0.30	1.20 dB
above 3.0 dB				
Phase Unbalance	544 - 727 MHz		0.836	3.000 deg.
Amplitude Unbalance	544 - 727 MHz		0.618	2.000 dB
VSWR	SUM Port		1.10	1.30 (:1)
	OUT Ports		1.10	1.35 (:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	1
PORT 1	3
PORT 2	6
GND EXT	2, 5
50Ω TERMINATION	4

Functional Diagram

