

Engineering Development Model

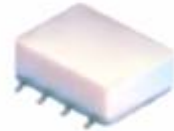
Power Splitter/Combiner

ADQ-ED12533/1

2 Way-90°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE : CJ725

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		100		300 MHz
Isolation	100 - 300 MHz		25	
Insertion Loss				
Average of Coupled Outputs	100 - 300 MHz		0.50	
above 3.0 dB				
Phase Unbalance	100 - 300 MHz		89.878	
				deg.
Amplitude Unbalance	100 - 300 MHz		0.976	
				dB
VSWR	SUM Port		1.15	(:1)
	OUT Ports		1.10	(:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	1
PORT 1	8
PORT 2	4
GND EXT	2,3,6,7
50Ω TERMINATION	5

Functional Diagram

