

## Engineering Development Model

# RF Transformer

# ADT2-ED10724/2

Impedance Ratio : 2

### Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE : CD636

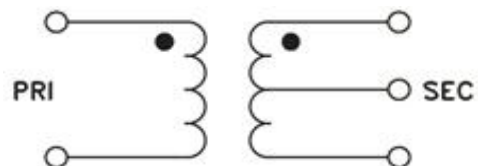
| ELECTRICAL SPECIFICATIONS 50Ω @ +25°C |                    |      |          |      |       |
|---------------------------------------|--------------------|------|----------|------|-------|
| Parameter                             |                    | Min. | Typ.     | Max. | Units |
| Frequency                             |                    | 40   |          | 350  | MHz   |
| Insertion Loss *                      | 3 dB Bandwidth     |      | 40 - 350 |      | MHz   |
|                                       | 2 dB Bandwidth     |      | 40 - 340 |      | MHz   |
|                                       | 1 dB Bandwidth     |      | 40 - 220 |      | MHz   |
| Amplitude Unbalance                   | over 3dB Bandwidth |      | 0.5      |      | dB    |
|                                       | over 1dB Bandwidth |      | 0.1      |      | dB    |
| Phase Unbalance                       | over 3dB Bandwidth |      | 5.0      |      | deg.  |
|                                       | over 1dB Bandwidth |      | 1.00     |      | deg.  |

### Note:

\* Insertion Loss is referenced to mid-band loss, 1.92dB typ.

| MAXIMUM RATINGS       |                |
|-----------------------|----------------|
| Operating Temperature | -20°C to 85°C  |
| Storage Temperature   | -55°C to 100°C |
| RF Power              | 0.25 W         |
| DC Current            | 30 mA          |

### Configuration : A



| PIN CONNECTIONS |   |
|-----------------|---|
| PRIMARY DOT     | 4 |
| PRIMARY         | 6 |
| SECONDARY DOT   | 3 |
| SECONDARY       | 1 |
| SECONDARY CT    | 2 |
| ISOLATE         | 5 |