

# Monolithic Amplifier

# AVA-183P+

Mini-Circuits

 $50\Omega$  0.5 to 18 GHz

## THE BIG DEAL

- Ultra-wideband, 0.5 to 18 GHz
- Positive gain slope
- Single positive supply voltage



Generic photo used for illustration purposes only CASE STYLE: DQ1225

+RoHS Compliant The +Suffix identifies RoHS Compliance. See our website for methodologies and qualifications

## **APPLICATIONS**

- Wi-Fi
- WLAN
- LTE
- WiMAX
- C-band Satcom

#### **PRODUCT OVERVIEW**

The AVA-183P+ is a InGaAs E-PHEMT\* based wideband, positive gain slope MMIC amplifier. This design operates on a single +5V supply, is well matched for  $50\Omega$  and comes in a tiny, low profile package (3 x 3 x 0.89mm), accommodating dense circuit board layouts.

#### **KEY FEATURES**

Feature	Feature Advantages	
Positive Gain Slope vs. Frequency +0.13 dB/GHz (0.5-10 GHz) +0.25 dB/GHz (10-18 GHz)	Useful for compesating negative gain slope of most wideband microwave components and eliminating the need for equalization	
Positive Supply Voltage	Simplifies external circuit by eliminating need for negative voltage and sequencing	
3 x 3mm, 12-lead MCLP package	Tiny footprint saves space in dense layouts while providing low inductance, repeatable transitions, and excellent thermal contact to the PCB.	

\*Enhancement mode Pseudomorphic High Electron Mobility Transistor





# WIDEBAND, POSITIVE GAIN SLOPE Monolithic Amplifier



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#### ELECTRICAL SPECIFICATIONS<sup>1</sup> AT 25°C AND +5V, UNLESS NOTED OTHERWISE

Parameter	Condition (GHz)	Min.	Тур.	Max.	Unit
Frequency Range		0.5		18	GHz
	0.5	6.1	6.8	7.5	
	5	_	7.5	_	
Gain	10	7.3	8.1	8.9	dB
	15	_	9.8	_	
	18	9.2	10.8	11.9	
	0.5 - 10	_	0.13	_	dB/GHz
Gain Slope	10 -18	_	0.25	_	
	0.5		15		
	5		13		
Input Return Loss	10		10		dB
	15		23		
	18		14		
	0.5		11		
	5		8		
Output Return Loss	10		7		dB
	15		9		
	18		19		
	0.5		10.9		
	5		10.6		
Output Power at 1dB Compression	10		11.7		dBm
	15		12.3		
	18		11.6		
	0.5		22.8		
	5		21.4		
OIP3	10		21.5		dBm
	15		20.6		
	18		19.1		
	0.5		7.5		
	5		5.3		
Noise Figure	10		4.8		dB
	15		4.6		
	18		5.0		
Device Operating Voltage(VDD)		+4.75	+5	+5.25	V
Device Operating Current (IDD)		_	46.3	54	mA
Device Current Variation vs. Temperature <sup>2</sup>			-168.13		µA/°C
Device Current Variation vs. Voltage <sup>3</sup>			0.0085		mA/mV
Thermal Resistance, Junction to Ground			292.5		°C/W

2. Current variation vs temperature=(Current at 10°C-Current at -55°C)/155°C 3. Current variation vs Voltage=(Current at 5.25V - Current at 4.75V)/(5.25V-4.75V)

#### **MAXIMUM RATINGS<sup>4</sup>**

Parameter	Ratings
Operating Temperature (ground lead)	-55°C to 100°C
Storage Temperature	-65°C to 150°C
Junction Temperature	161°C
Total Power Dissipation	0.46W <sup>6</sup>
Input Power (CW), Vd=5V <sup>5</sup>	+22 dBm (5 minutes max.) +13 dBm (continuous)
DC Voltage	6V

4.Permanent damage may occur if any of these limits are exceeded. Electrical maximum ratings are not intended for continuous normal operation. 5.Measured on Mini-Circuits test board, TB-AVA-183P+ 6. Derates linearly to 0.24W at 100°C

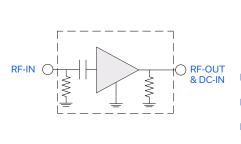
## **Mini-Circuits**

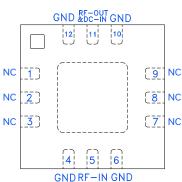


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### **SIMPLIFIED SCHEMATIC & PAD DESCRIPTION**

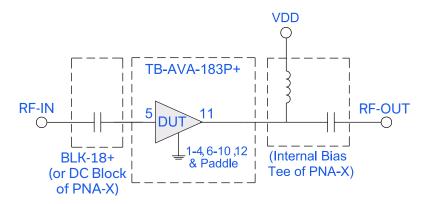




TOP VIEW

Function	Pad Number	Description (See Figure 1)
RF-IN	5	Connects to RF input via external DC blocking capacitor
RF-OUT & DC-IN	11	Connects to RF output & V <sub>DD</sub> via external bias-tee
Ground	4,6,10,12 & Paddle	Connects to ground
No Connection	1-3, 7-9	Not used internally. Connected to ground on test board

#### **RECOMMENDED CHARACTERIZATION TEST CIRCUIT**



#### Fig 1. Characterization Test Circuit

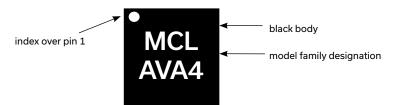
Note: This block diagram is used for characterization. (DUT soldered on Mini-Circuits Characterization test board TB-AVA-183P+) Gain, Return loss, Output power at 1dB compression (P1 dB), output IP3 (OIP3) and noise figure measured using Agilent's N5242A PNA-X microwave network analyzer.

#### Conditions:

1. Gain and Return loss: Pin= -25dBm

2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, -3 dBm/tone at output.

#### **PRODUCT MARKING**



Marking may contain other features or characters for internal lot control



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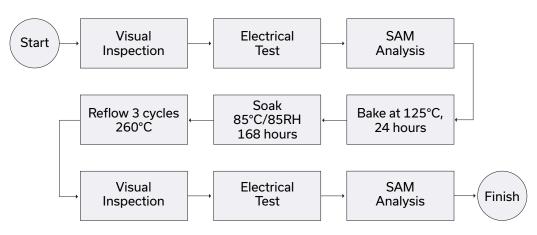
#### ADDITIONAL DETAILED TECHNICAL INFORMATION IS AVAILABLE ON OUR DASH BOARD. TO ACCESS CLICK HERE

Performance Data	Data Table Swept Graphs S-Parameter (S2P Files) Data Set (.zip file)	
Case Style	DQ1225 Plastic package, exposed paddle, lead finish: Matte-Tin	
Tape & Reel Standard quantities available on reel	F66 7″ reels with 20, 50, 100, 200, 500, 1K, or 2K devices.	
Suggested Layout for PCB Design	PL-666	
Evaluation Board	TB-AVA-183P+ (no connectors), TB-AVA-183PC+ (with connectors)	
Environmental Ratings	ENV08T9	

#### **ESD RATING**

Human Body Model (HBM): Class 1A (250 to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

## **MSL TEST FLOW CHART**



#### NOTES

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
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