



Mini-Circuits

LTCC SURFACE MOUNT

Bandpass Filter & Balun

BBFCG2-472+

50Ω 4200 to 5200 MHz 1:2 Ratio

THE BIG DEAL

- Compact Design includes Balun and Filter in One Package
- Low Passband Insertion Loss, Typ. 2 dB avg.
- Excellent CMRR, Typ. 21.5 dB
- Small 0805 Surface Mount Footprint

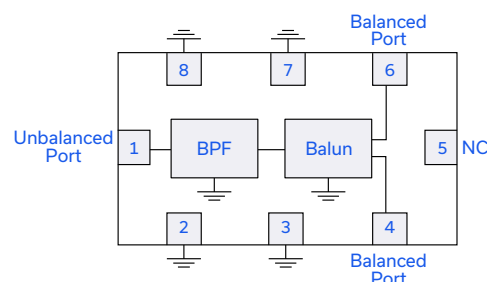


Generic photo used for illustration purposes only

APPLICATIONS

- 5G Sub-6 GHz, MIMO Wireless Infrastructure Systems
- Satellite Communications
- Telecommunications
- RF Front-End Modules

FUNCTIONAL DIAGRAM



PRODUCT OVERVIEW

Mini-Circuits' BBFCG2-472+ is a tiny ceramic RF balun filter with an impedance ratio of 1:2, covering a variety of wireless communications applications from 4200 to 5200 MHz. This model provides low insertion loss, low phase unbalance (relative to 180°), and low amplitude unbalance. Fabricated using LTCC technology, the unit comes housed in a tiny, rugged ceramic package (0.079" x 0.049" x 0.037") suitable for harsh operating environments.

KEY FEATURES

| Features | Advantages |
|-------------------|--|
| Compact Design | Integrates filter and balun in one small package. |
| Tiny Size, 0805 | Accommodates tight space requirements for dense PCB layouts. |
| LTCC Construction | LTCC process enables tiny size and low cost, suitable for high-volume production. Rugged ceramic package provides excellent reliability in harsh operating environments. |





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ELECTRICAL SPECIFICATIONS^{1,2} AT +25°C

| Parameter | F# | Frequency (MHz) | Min. | Typ. | Max. | Units |
|--|--|-----------------|-------------|------|------|-------|
| Impedance Ratio | - | - | - | 1:2 | - | - |
| Passband | Center Frequency ³ | - | - | 4700 | - | MHz |
| | Average Insertion Loss ⁴ | F3-F4 | 4200 - 5200 | 2 | 2.5 | dB |
| | Return Loss - Unbalanced Port | F3-F4 | 4200 - 5200 | 10 | 13.5 | dB |
| | Return Loss - Balanced Port ⁴ | F3-F4 | 4200 - 5200 | 10 | 13.5 | dB |
| Stopband, Lower Rejection ⁴ | DC-F1 | 10 - 3100 | 27 | 32 | - | dB |
| | F1-F2 | 3100 - 3580 | - | 25 | - | dB |
| Stopband, Upper Rejection ⁴ | F5-F6 | 6740 - 7650 | 28 | 32 | - | dB |
| | F6-F7 | 7650 - 11000 | 30 | 39 | - | dB |
| Amplitude Unbalance (±) | F3-F4 | 4200 - 5200 | -1.4 | ±1.2 | +1.4 | dB |
| Phase Unbalance (Relative to 180°) | F3-F4 | 4200 - 5200 | -11 | - | +11 | Deg. |
| CMRR | F3-F4 | 4200 - 5200 | 18 | 21.5 | - | dB |

1. Tested on Evaluation Board P/N TB-BBFCG2-472C+ with connectors and feedline loss compensated.

2. This component should not be used as a DC Block. In applications where DC voltage and/or current is present at either the input or output ports, external DC blocking capacitors are required.

3. Typical variation ±3%.

4. Measured in mixed mode.

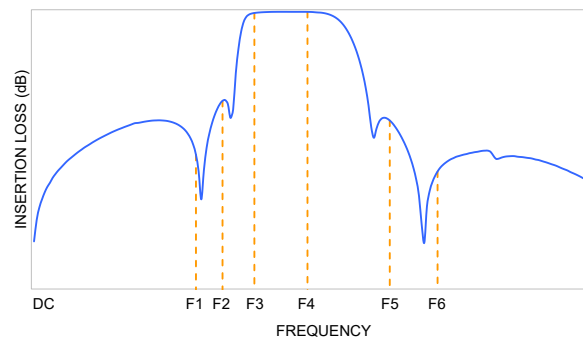
ABSOLUTE MAXIMUM RATINGS⁵

| Parameter | Ratings |
|--------------------------|-----------------|
| Operating Temperature | -55°C to +125°C |
| Storage Temperature | -55°C to +125°C |
| Input Power ⁶ | 2 W |

5. Permanent damage may occur if any of these limits are exceeded.

6. Power rating applies only to signals within the passband at +25°C. Power rating above +25°C operating temperature decreases linearly to 0.5 W at +125°C.

TYPICAL FREQUENCY RESPONSE AT +25°C



DC RESISTANCE PORT-PORT

| Function | Pad Number |
|----------------------------------|------------|
| Unbalanced Port to Ground | DC Short |
| Unbalanced Port to Balanced Port | DC Open |
| Balanced Port to Ground | DC Open |
| Balanced Port to Balanced Port | DC Short |





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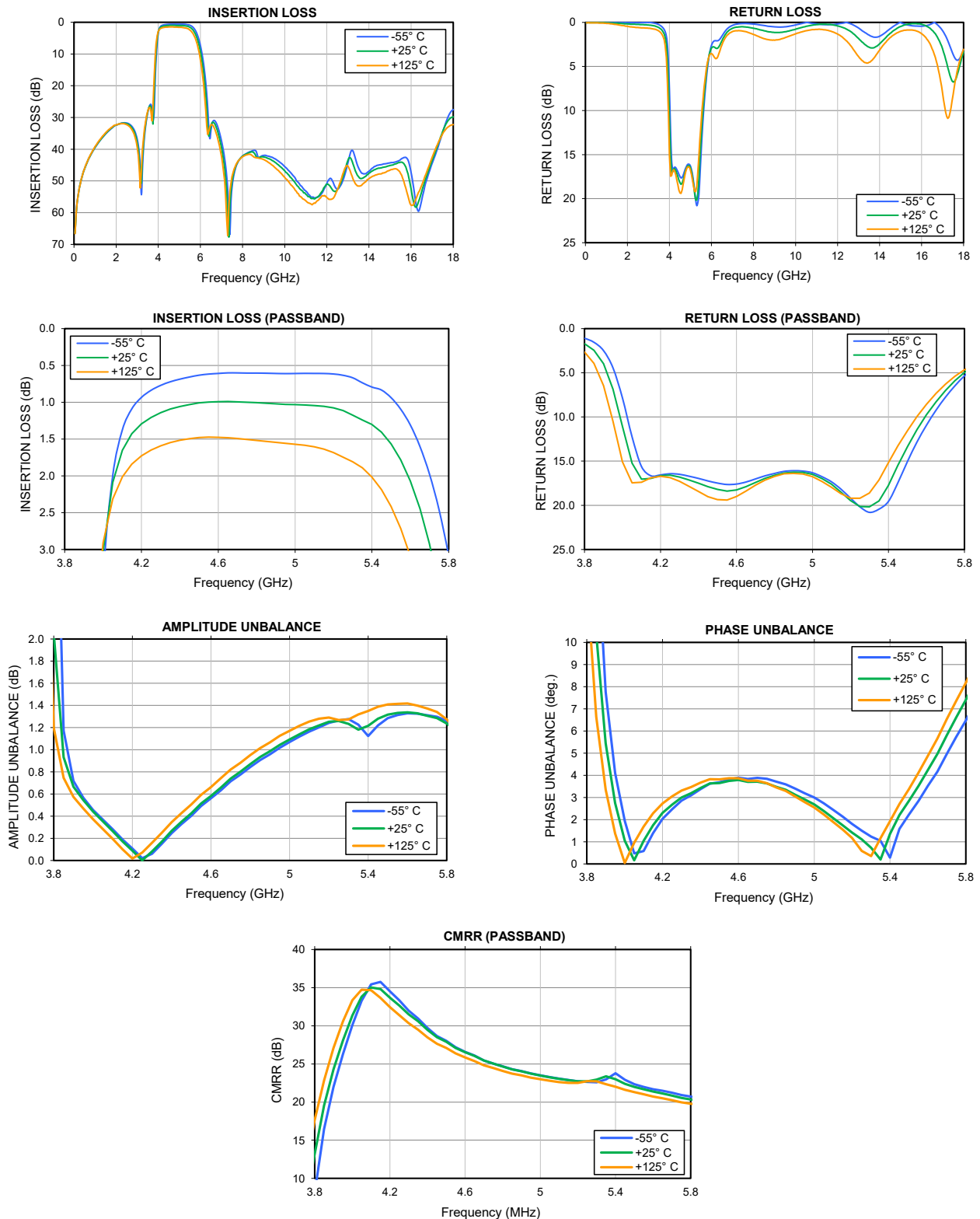
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TYPICAL PERFORMANCE GRAPHS



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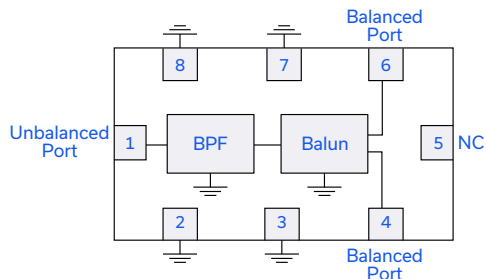
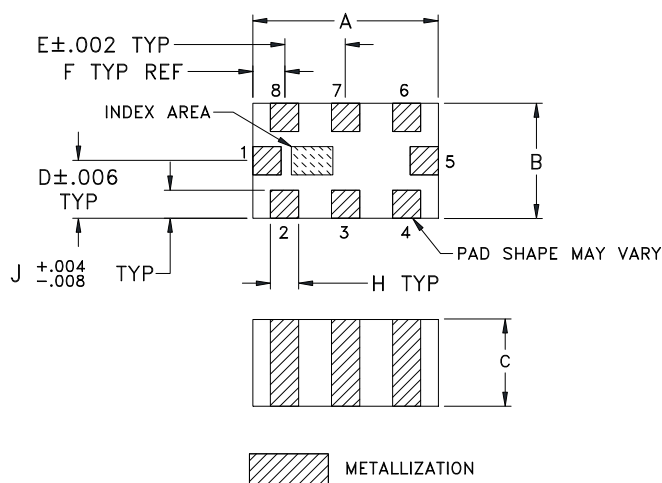


Figure 1. BBFCG2-472+ Functional Diagram

PAD DESCRIPTION

| Function | Pad Number | Description |
|-----------------|------------|---|
| Unbalanced Port | 1 | Unbalanced Input Port |
| Balanced Ports | 4, 6 | Balanced Output Ports |
| Ground | 2, 3, 7, 8 | Connects to Ground on PCB. (See drawing PL-724) |
| NC | 5 | No connection, not used internally. See drawing PL-724 for connection to PCB. |

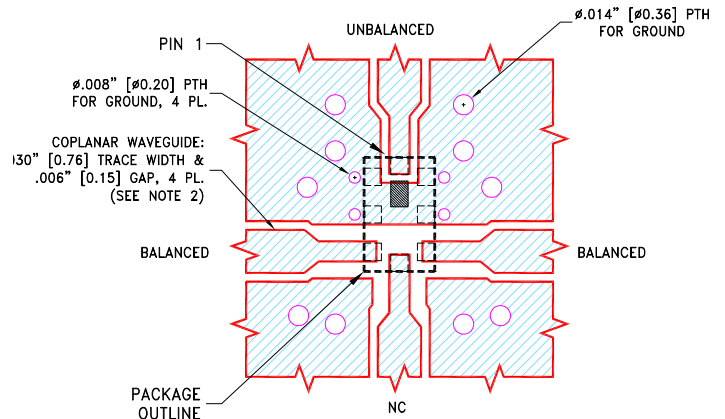
CASE STYLE DRAWING



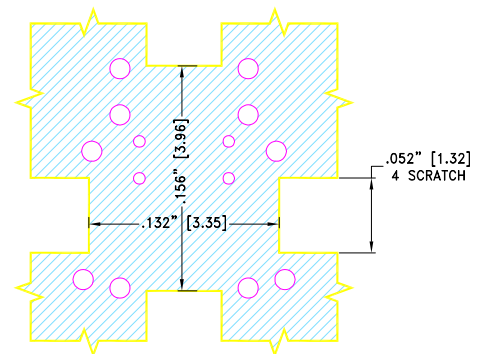
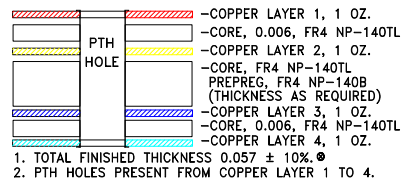
OUTLINE DIMENSIONS (Inch/mm)

| A | B | C | D | E | F | G | H | J | wt |
|------|------|------|------|------|------|------|------|------|-------|
| .079 | .049 | .037 | .025 | .026 | .014 | .110 | .012 | .010 | grams |
| 2.00 | 1.25 | 0.95 | 0.63 | 0.65 | 0.35 | 2.80 | 0.30 | 0.25 | .008 |

SUGGESTED PCB LAYOUT (PL-724)



STACK-UP DIAGRAM



NOTES:

- PCB IS MULTILAYER PCB, SEE STACK-UP DIAGRAM.
- TRACE WIDTH & GAP PARAMETERS ARE SHOWN FOR FR4 NP-140TL WITH DIELECTRIC THICKNESS .006" ± .0005"; COPPER: 1 OZ. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
- COPPER LAYERS 3,4 OF THE PCB IS CONTINUOUS GROUND PLANES.

■ DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

Figure 2. Suggested PCB Layout PL-724

PRODUCT MARKING*: NO MARKING

*Marking may contain other features or characters for internal lot control.



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ADDITIONAL DETAILED INFORMATION IS AVAILABLE ON OUR DASHBOARD.

[CLICK HERE](#)

| | |
|---------------------------------|---|
| Performance Data & Graphs | Data Graphs S-Parameter (S3P Files) Data Set (.zip file) with connectors and feedline loss compensated. |
| Case Style | GE0805C-15 Lead Finish: Tin over Nickel Plating |
| RoHS Status | Compliant |
| Tape and Reel | F114 |
| Suggested Layout for PCB Design | PL-724 |
| Evaluation Board | TB-BBFCG2-472C+ Gerber File |
| Environmental Rating | ENV06T10 |

Notes

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.

B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.

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