



LTCC SURFACE MOUNT

# Bandpass Filter & Balun

**BBFCG2-472+**

50Ω 4200 to 5200 MHz 1:2 Ratio

## THE BIG DEAL

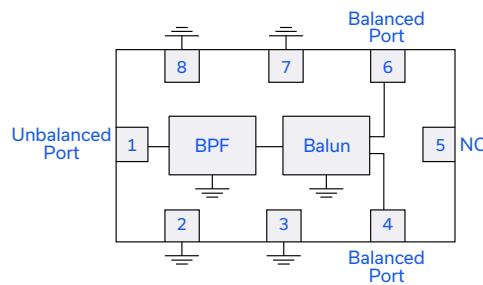
- Compact Design includes Balun and Filter in One Package
- Low Passband Insertion Loss, Typ. 2 dB avg.
- Excellent CMRR, Typ. 21.5 dB
- Small 0805 Surface Mount Footprint

*Generic photo used for illustration purposes only*

## APPLICATIONS

- 5G Sub-6 GHz, MIMO Wireless Infrastructure Systems
- Satellite Communications
- Telecommunications
- RF Front-End Modules

## FUNCTIONAL DIAGRAM



## PRODUCT OVERVIEW

Mini-Circuits' BBFCG2-472+ is a tiny ceramic RF balun filter with an impedance ratio of 1:2, covering a variety of wireless communications applications from 4200 to 5200 MHz. This model provides low insertion loss, low phase unbalance (relative to 180°), and low amplitude unbalance. Fabricated using LTCC technology, the unit comes housed in a tiny, rugged ceramic package (0.079" x 0.049" x 0.037") suitable for harsh operating environments.

## KEY FEATURES

Features	Advantages
Compact Design	Integrates filter and balun in one small package.
Tiny Size, 0805	Accommodates tight space requirements for dense PCB layouts.
LTCC Construction	LTCC process enables tiny size and low cost, suitable for high-volume production. Rugged ceramic package provides excellent reliability in harsh operating environments.

REV. OR  
ECO-027830  
BBFCG2-472+  
MCL NY  
251126





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ELECTRICAL SPECIFICATIONS<sup>1,2</sup> AT +25°C

Parameter	F#	Frequency (MHz)	Min.	Typ.	Max.	Units
Impedance Ratio	-	-	-	1:2	-	-
Passband	Center Frequency <sup>3</sup>	-	-	4700	-	MHz
	Average Insertion Loss <sup>4</sup>	F3-F4	4200 - 5200	2	2.5	dB
	Return Loss - Unbalanced Port	F3-F4	4200 - 5200	13.5	-	dB
	Return Loss - Balanced Port <sup>4</sup>	F3-F4	4200 - 5200	13.5	-	dB
Stopband, Lower Rejection <sup>4</sup>	DC-F1	10 - 3100	27	32	-	dB
	F1-F2	3100 - 3580	-	25	-	dB
Stopband, Upper Rejection <sup>4</sup>	F5-F6	6740 - 7650	28	32	-	dB
	F6-F7	7650 - 11000	30	39	-	dB
Amplitude Unbalance (±)	F3-F4	4200 - 5200	-1.4	±1.2	+1.4	dB
Phase Unbalance (Relative to 180°)	F3-F4	4200 - 5200	-11	-	+11	Deg.
CMRR	F3-F4	4200 - 5200	18	21.5	-	dB

1. Tested on Evaluation Board P/N TB-BBFCG2-472C+ with connectors and feedline loss compensated.

2. This component should not be used as a DC Block. In applications where DC voltage and/or current is present at either the input or output ports, external DC blocking capacitors are required.

3. Typical variation ±3%.

4. Measured in mixed mode.

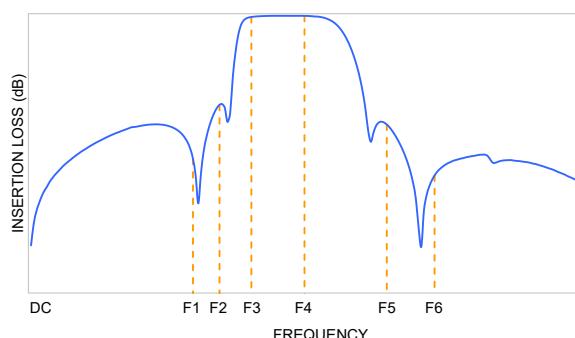
ABSOLUTE MAXIMUM RATINGS<sup>5</sup>

Parameter	Ratings
Operating Temperature	-55°C to +125°C
Storage Temperature	-55°C to +125°C
Input Power <sup>6</sup>	2 W

5. Permanent damage may occur if any of these limits are exceeded.

6. Power rating applies only to signals within the passband at +25°C. Power rating above +25°C operating temperature decreases linearly to 0.5 W at +125°C.

## TYPICAL FREQUENCY RESPONSE AT +25°C



## DC RESISTANCE PORT-PORT

Function	Pad Number
Unbalanced Port to Ground	DC Short
Unbalanced Port to Balanced Port	DC Open
Balanced Port to Ground	DC Open
Balanced Port to Balanced Port	DC Short



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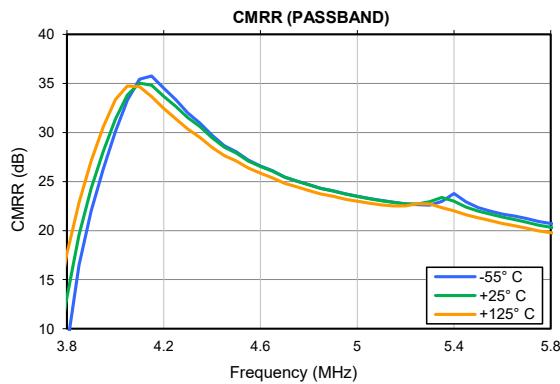
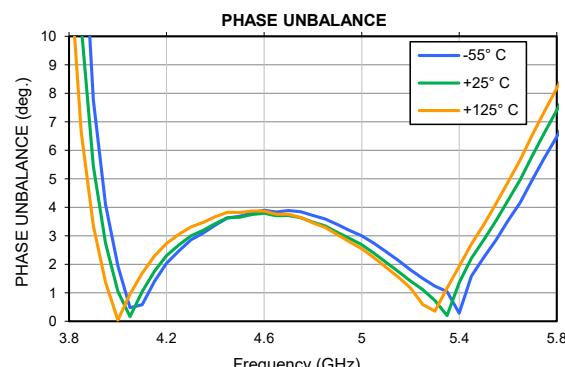
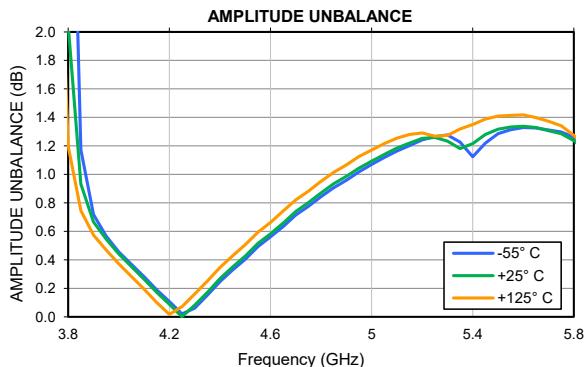
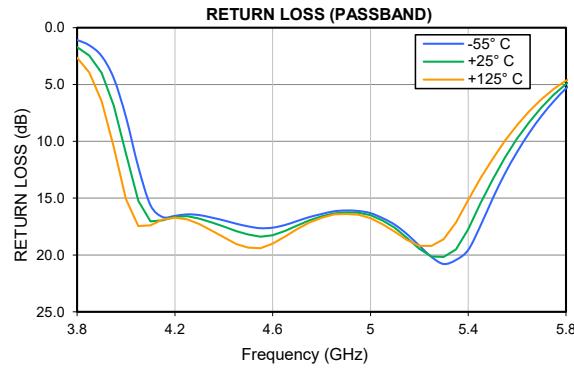
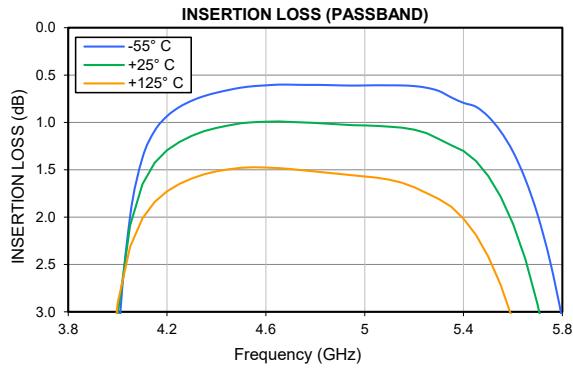
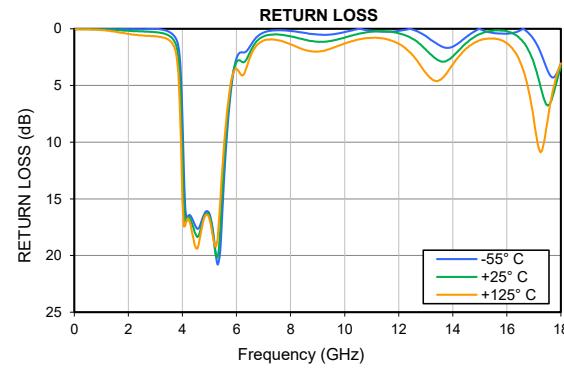
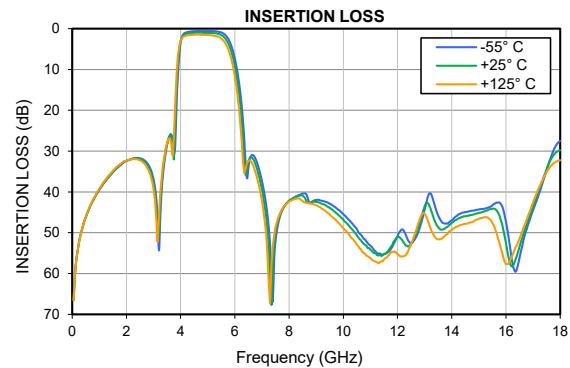
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Mini-Circuits

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## TYPICAL PERFORMANCE GRAPHS





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### FUNCTIONAL DIAGRAM

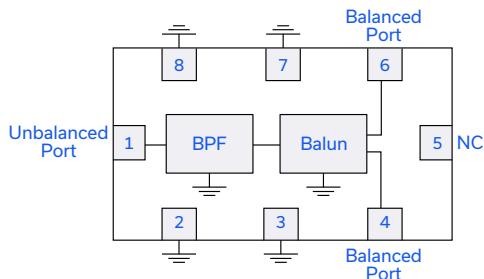
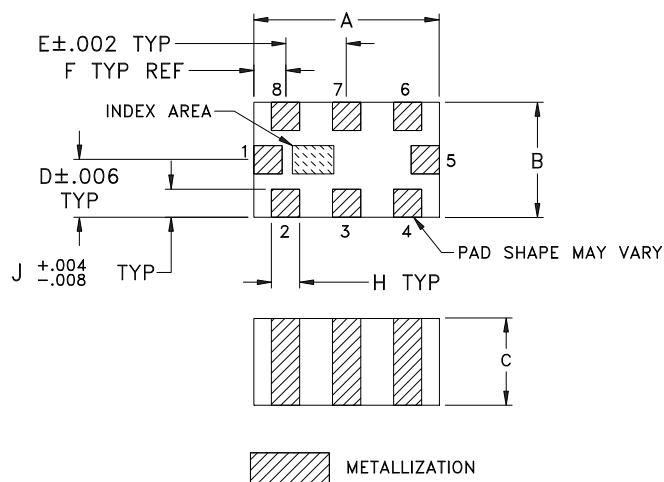


Figure 1. BBFCG2-472+ Functional Diagram

### PAD DESCRIPTION

Function	Pad Number	Description
Unbalanced Port	1	Unbalanced Input Port
Balanced Ports	4, 6	Balanced Output Ports
Ground	2, 3, 7, 8	Connects to Ground on PCB. (See drawing PL-724)
NC	5	No connection, not used internally. See drawing PL-724 for connection to PCB.

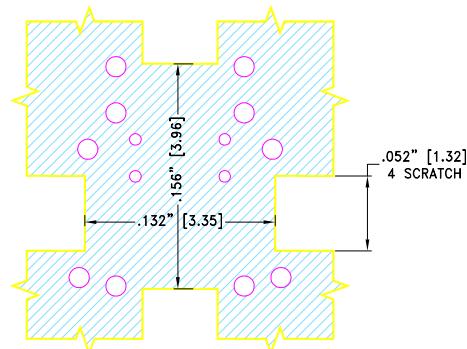
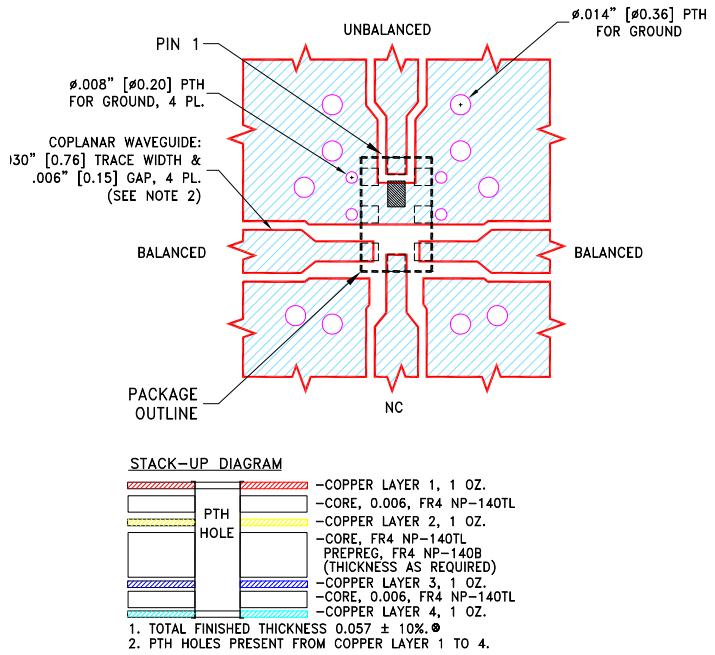
### CASE STYLE DRAWING



### OUTLINE DIMENSIONS (Inch mm)

A	B	C	D	E	F	G	H	J	wt
.079 2.00	.049 1.25	.037 0.95	.025 0.63	.026 0.65	.014 0.35	.110 2.80	.012 0.30	.010 0.25	.008

### SUGGESTED PCB LAYOUT (PL-724)



TOP VIEW TO LAYER 2

#### NOTES:

1. PCB IS MULTILAYER PCB, SEE STACK-UP DIAGRAM.
2. TRACE WIDTH & GAP PARAMETERS ARE SHOWN FOR FR4 NP-140TL WITH DIELECTRIC THICKNESS .006"±.0005"; COPPER: 1 OZ. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
3. COPPER LAYERS 3,4 OF THE PCB IS CONTINUOUS GROUND PLANES.

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

Figure 2. Suggested PCB Layout PL-724

### PRODUCT MARKING\*: NO MARKING

\*Marking may contain other features or characters for internal lot control.



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**ADDITIONAL DETAILED INFORMATION IS AVAILABLE ON OUR DASHBOARD.****CLICK HERE**

<b>Performance Data &amp; Graphs</b>	Data Graphs S-Parameter (S3P Files) Data Set (.zip file) with connectors and feedline loss compensated.
<b>Case Style</b>	GE0805C-15 Lead Finish: Tin over Nickel Plating
<b>RoHS Status</b>	Compliant
<b>Tape and Reel</b>	F114
<b>Suggested Layout for PCB Design</b>	PL-724
<b>Evaluation Board</b>	TB-BBFCG2-472C+ Gerber File
<b>Environmental Rating</b>	ENV06T10

**Notes**

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits' standard limited warranty and terms and conditions (collectively, "Standard Terms"; Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at <https://www.minicircuits.com/terms/viewterm.html>

