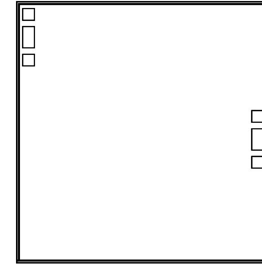
MMIC DIE

X3 Frequency Multiplier

CY3-64-D+

50Ω Output 30 to 60 GHz



+RoHS Compliant
The +Suffix identifies RoHS Compliance.
See our website for methodologies and qualifications

SEE ORDERING INFORMATION ON THE LAST PAGE

THE BIG DEAL

- Wideband Output from: 30 to 60 GHz
- Outstanding Fundamental and Close-in Harmonic Suppression:
  - F1: +30 dBc Typ.
  - F2: +33 dBc Typ.
  - F4: +42 dBc Typ.
- Input Drive Level: +12 to +19 dBm
- Conversion Loss: 21 dB Typ.

APPLICATIONS

- 5G MIMO and Back Haul Radio Systems
- Satellite Communications
- Test and Measurement Equipment
- Radar, EW, and ECM Defense Systems

PRODUCT OVERVIEW

Mini-Circuits' CY3-64-D+ is a wideband MMIC Frequency Tripler, converting input frequencies from 10 to 20 GHz into output frequencies from 30 to 60 GHz. Its wide output range makes this model suitable for broadband systems as well as a wide variety of narrow-band applications. The CY3-64-D+ die utilizes GaAs HBT technology and is suitable for chip and wire assemblies.

KEY FEATURES

Feature	Advantages
Broadband, 30 to 60 GHz output	With an output frequency range spanning 30 to 60 GHz, this multiplier supports broadband applications such as defense and instrumentation as well as a wide range of narrowband system requirements including 5G.
Excellent fundamental and harmonic suppression: • F1, +30 dBc Typ. • F2, +33 dBc Typ. • F4, +42 dBc Typ.	Harmonic and fundamental filtering requirements are dramatically simplified due to the high suppression resulting from internal cancellation within the diode configuration.
Wide input power range +12 to +19 dBm	Wide input power signal range accommodates different input signal levels while still maintain a low conversion loss
Unpackaged Die	Enable user to integrate it directly into hybrids chip and wire assemblies

REV. A
ECO-018288
CY3-64-D+
MCL NY
230711

ELECTRICAL SPECIFICATIONS[1] AT +25°C AND Zo = 50Ω, UNLESS NOTED OTHERWISE

Parameter		Condition (GHz)	Min.	Typ.	Max.	Unit
Multiplication Factor				3		
Frequency Range, Input (F1)			10		20	GHz
Frequency Range, Output (F3)			30	-	60	GHz
Input Power[4]			+12	+18	+19	dBm
Conversion Loss (F3)[3]		10-12		20.0		dB
		12-14		19.3		
		14-16		20.7		
		16-18		22.2		
		18-20		23.8		
Harmonic Output[2,3]	F1	10-12		37		dBc
		12-14		34		
		14-16		30		
		16-18		26		
		18-20		22		
	F2	10-12		49		dBc
		12-14		35		
		14-16		33		
		16-18		28		
		18-20		22		
	F4	10-12		44		dBc
		12-14		47		
		14-16		42		
		16-18		35		
		18-20		43		

1. Electrical specifications are measured by attaching the die on Mini-Circuits Die Characterization Test Board. Trace and connector losses are de-embedded. Specifications Include the effect of bond wires.
2. Harmonics of input frequency below the power of F3.
3. F1=Input Frequency, F2=Second Harmonic, F3=Fundamental Output, F4=Fourth Harmonic
4. All specifications are measured with RF input power = +18 dBm.

MAXIMUM RATINGS[5]

Parameter	Ratings
Operating Temperature	-40°C to +85°C
Storage Temperature	+20°C to +35°C
RF Input Power	+22 dBm

5. Permanent damage may occur if any of these limits are exceeded.

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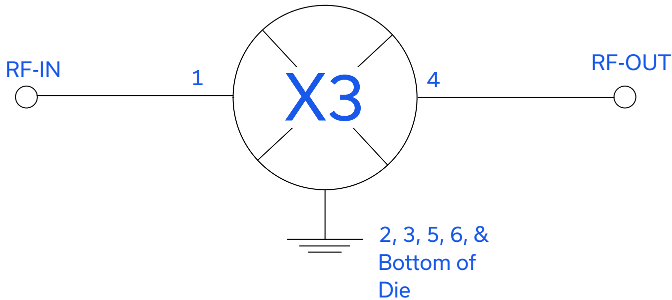
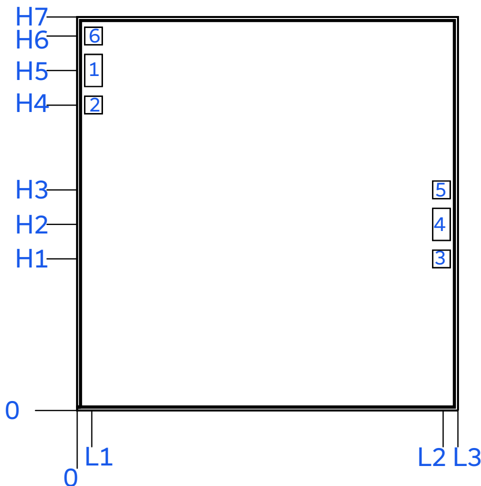
SIMPLIFIED SCHEMATIC AND PAD DESCRIPTION

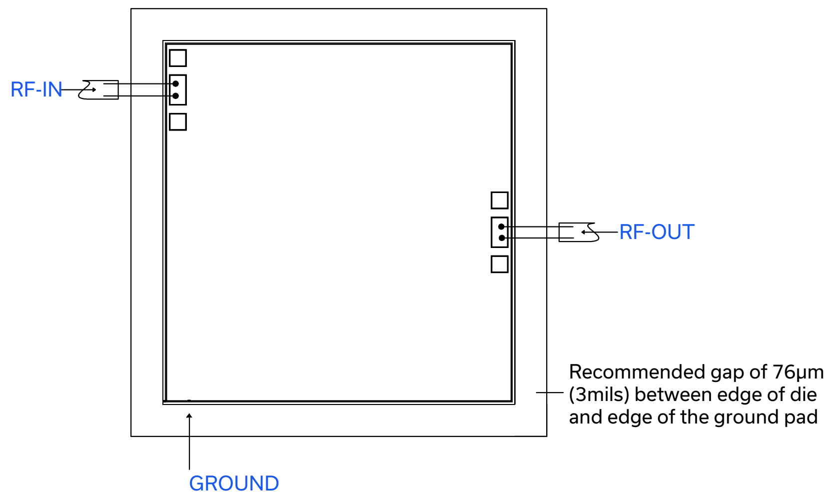


Function	Pad Number	Description
RF-IN	1	RF-Input pad
RF-OUT	4	RF-Output pad
Ground	2, 3, 5 & 6	The bond pads are connected to back-side through vias and do not require wire-bond connections to ground.

DIE DIMENSIONS



ASSEMBLY DIAGRAM



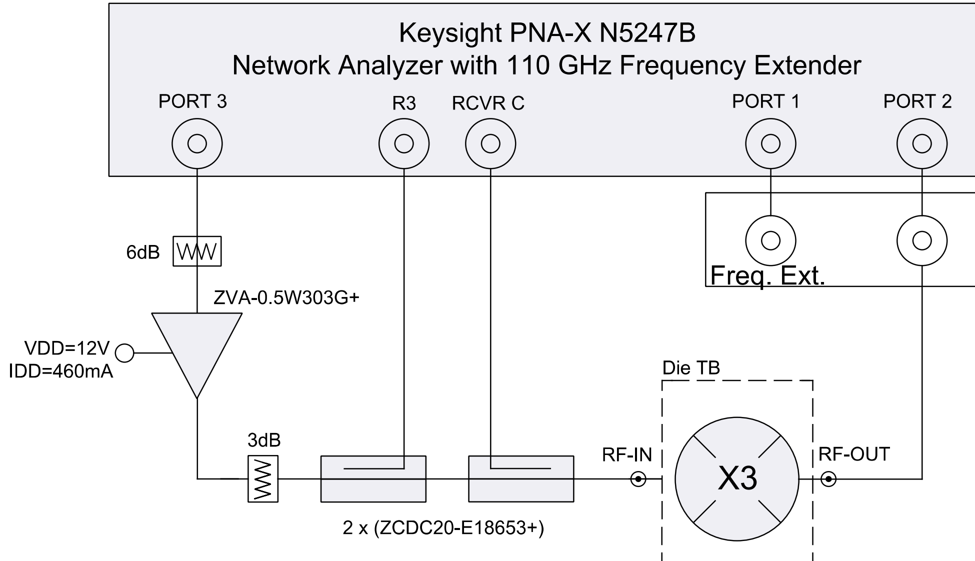
Note: bond wires should be as short as possible

DIMENSIONS μm, TYPICAL

L1	L2	L3	H1	H2	H3	H4	H5	H6	H7
81.0	2019.0	2100.0	836.0	1026.0	1217.0	1684.0	1875.0	2066.0	2170.0

Thickness	Die Size	Pad Size 1 & 4	Pad Size 2, 3, 5 & 6
100	2100 x 2170	92 x 172	92 x 92

CHARACTERIZATION CIRCUIT

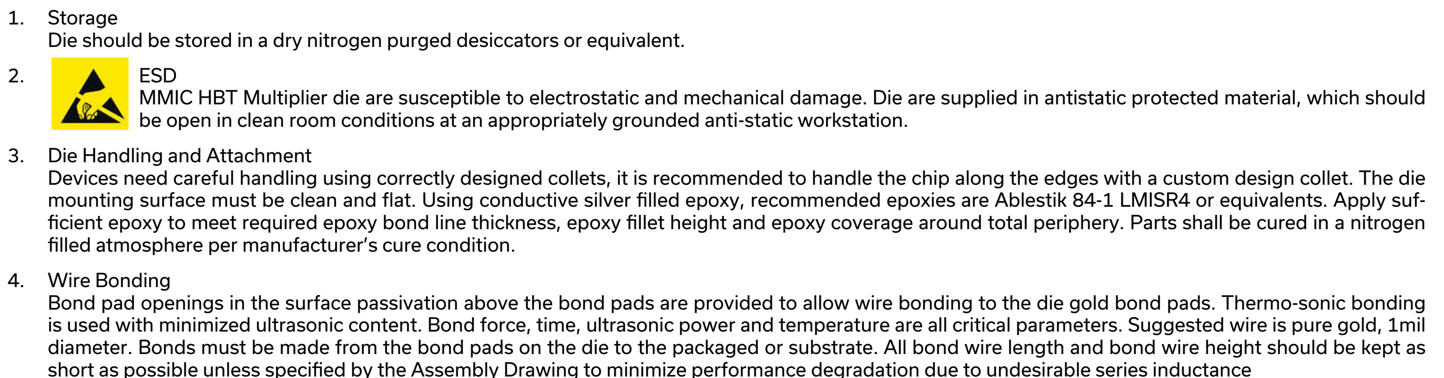


6 dB attenuator P/N BW-E6-1W653+
3 dB attenuator P/N BW-E3-1W653+

Note: DUT attached on a Mini-Circuits Die Characterization Test Board. Conversion Loss and Harmonic Output are measured using PNA-X Network Analyzer.

Test Condition: For CL and Harmonic Rejection: RF input power: +12 to +19dBm.

ASSEMBLY PROCEDURE



1. Storage
   Die should be stored in a dry nitrogen purged desiccators or equivalent.
2. ESD
   MMIC HBT Multiplier die are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be open in clean room conditions at an appropriately grounded anti-static workstation.
3. Die Handling and Attachment
   Devices need careful handling using correctly designed collets, it is recommended to handle the chip along the edges with a custom design collet. The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are Ablestik 84-1 LMISR4 or equivalents. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition.
4. Wire Bonding
   Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the die gold bond pads. Thermo-sonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1mil diameter. Bonds must be made from the bond pads on the die to the packaged or substrate. All bond wire length and bond wire height should be kept as short as possible unless specified by the Assembly Drawing to minimize performance degradation due to undesirable series inductance

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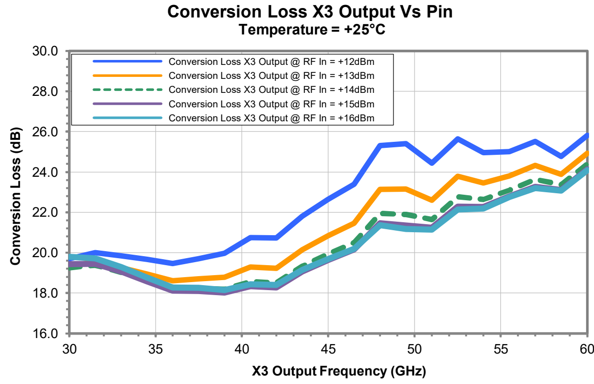
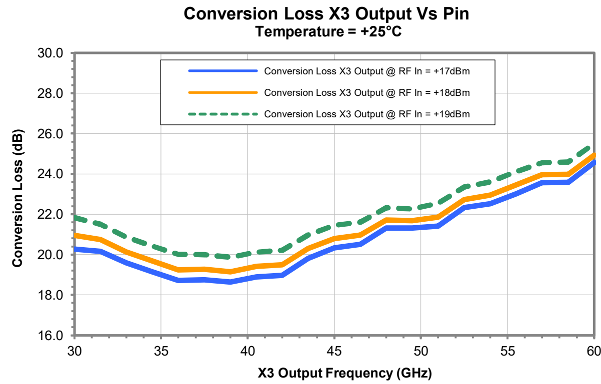
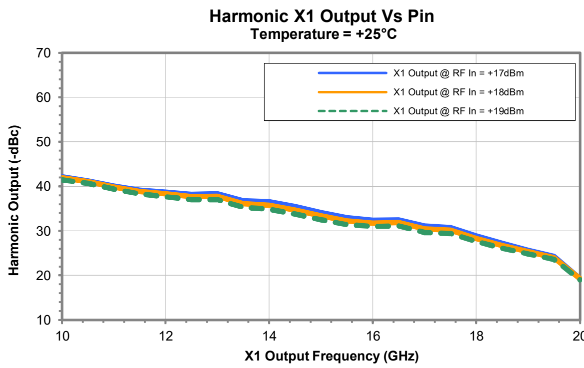
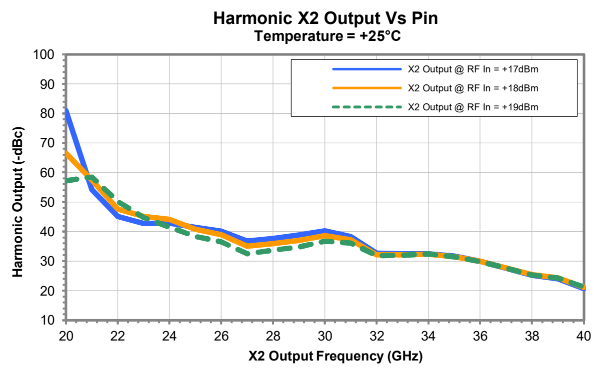
TYPICAL PERFORMANCE CURVES

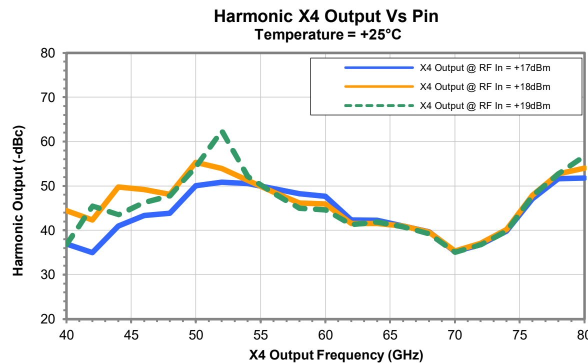
Note: Harmonics data is presented as the harmonic of input frequency below the power of F3











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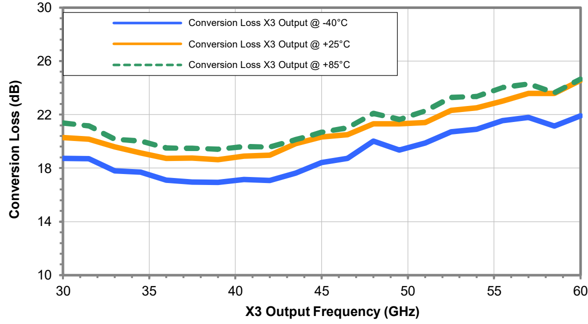

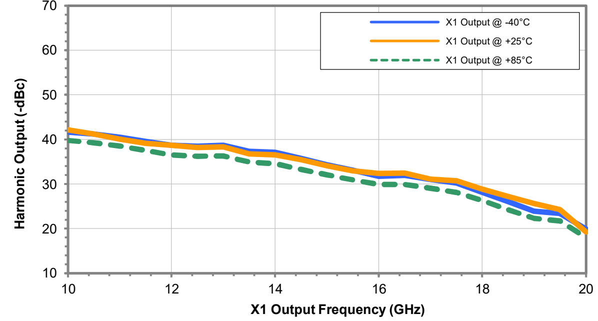

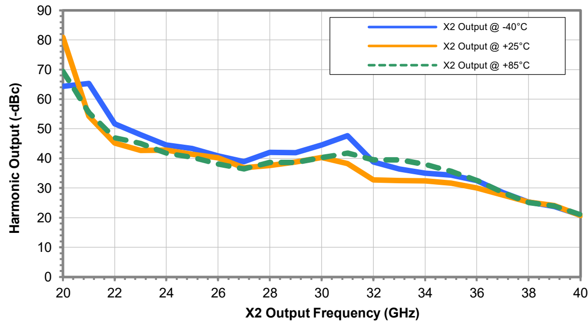

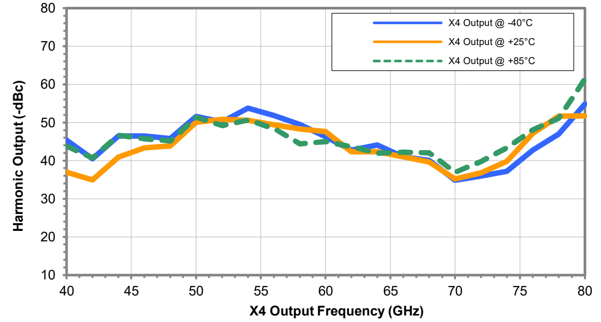

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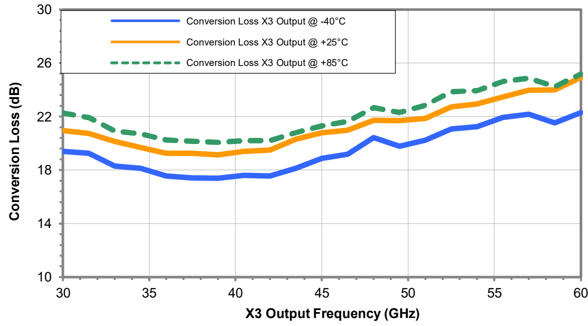

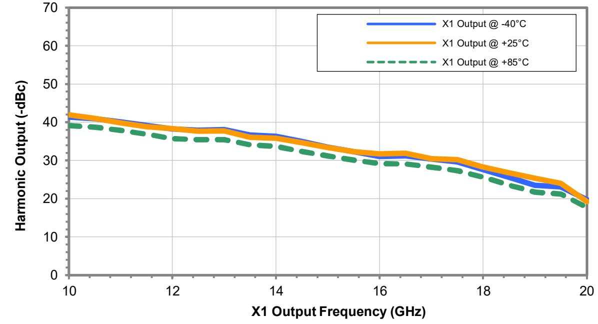

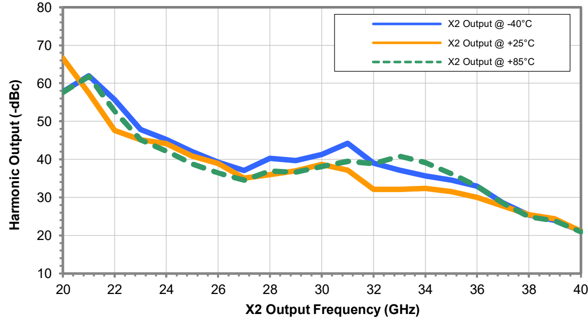

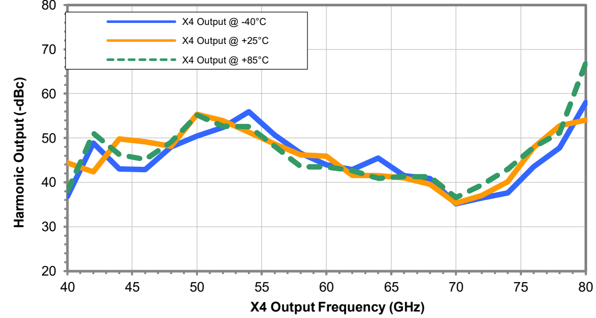

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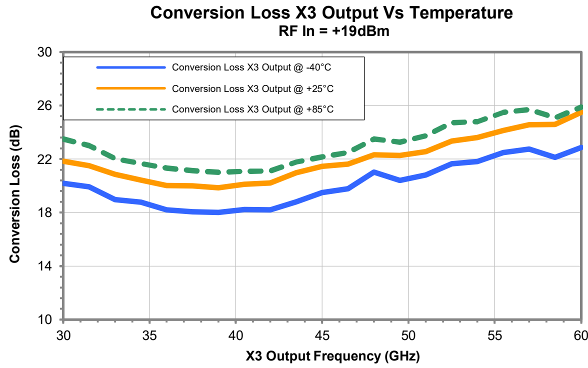

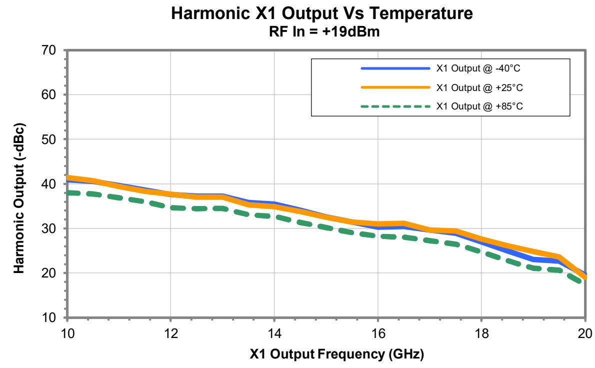

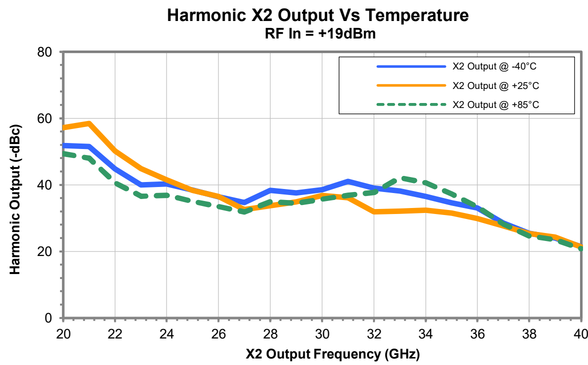

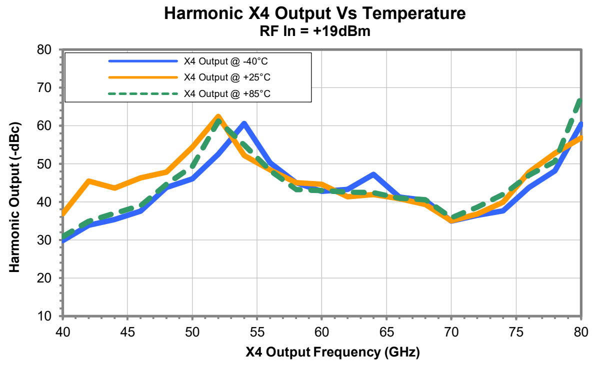

ADDITIONAL DETAILED TECHNICAL INFORMATION IS AVAILABLE ON OUR DASH BOARD.

Performance Data	Data Table Swept Graphs	
Case Style	Die	
Die Ordering and packaging information	Quantity, Package Gel – Pak: 5, 10, 50, 100 Medium†, Partial wafer: <506 Full Wafer† †Available upon request contact sales representative Refer to AN-60-067	Model No. CY3-64-DG+ CY3-64-DP+ CY3-64-DF+
Die Marking	EL-MUL-7	
Environmental Ratings	ENV80	

NOTES

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.

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