

## Engineering Development Model

### Dual Low Pass Filter

Surface Mount

**DLPF-EDU1159**

#### Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



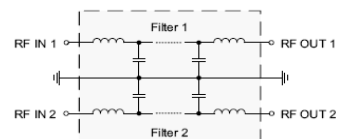
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**CASE STYLE: HU1186**

| Electrical Specifications 50Ω @ +25°C        |     |     |      |       |
|----------------------------------------------|-----|-----|------|-------|
| Parameter                                    | Min | Typ | Max  | Units |
| Passband (Loss < 1 dB)                       | DC  | -   | 12   | MHz   |
| Stopband (Loss > 20 dB)                      | 20  | -   | 50   | MHz   |
| Stopband (Loss > 40 dB)                      | 50  | -   | 1500 | MHz   |
| Passband VSWR                                |     | 1.1 | 1.4  | :1    |
| Group Delay variation b/w 2 filters @ 12 MHz |     | 1   |      | nS    |

#### Functional Schematic

| MAXIMUM RATINGS       |                |
|-----------------------|----------------|
| Operating Temperature | -40°C to +60°C |
| Storage Temperature   | -55°C to +85°C |
| RF Power Input        | 0.5 W          |



| PIN CONNECTIONS |                         |
|-----------------|-------------------------|
| RF IN 1         | 2 (filter1)             |
| RF OUT 1        | 13 (filter1)            |
| RF IN 2         | 6 (filter2)             |
| RF OUT 2        | 9 (filter2)             |
| GROUND          | 1,3,4,5,7,8,10,11,12,14 |