

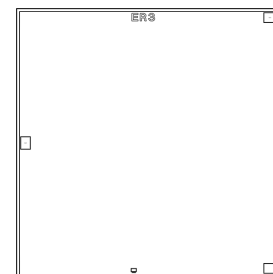
# MMIC Power Splitter/Combiner Die

## EP2C-D+

2 Way-0° 50Ω 1800 to 12500 MHz

### The Big Deal

- Ultra-Wide Bandwidth, 1800-12500 MHz
- High Power Handling, 1.85W as a Splitter



### Product Overview

Mini-Circuits EP2C-D+ is a MMIC splitter/combiner die designed for wideband operation from 1800 to 12500 MHz. This model provides excellent power ratings with up to 1.85W power handling (as a splitter) and up to 0.4A DC current handling. Manufactured using GaAs IPD technology, it provides a high level of ESD protection and excellent reliability.

### Key Features

Feature	Advantages
Wideband, 1800 to 12500 MHz	One power splitter can be used in many applications, saving component count. Also ideal for wideband applications such as military and instrumentation.
Excellent power handling: 1.85W as a splitter 0.85W internal dissipation as a combiner	In power combiner applications, half the power is dissipated internally. EP2C-D+ is designed to handle 0.85W internal dissipation as a combiner allowing reliable operation without excessive temperature rise. Similar splitters implemented as Wilkinson splitters on PCB require big resistors and additional heat sinking. As a splitter, EP2C-D+ can handle up to 1.85W in a very small package.
DC Passing up to 0.4A	DC current passing is helpful in applications where both RF & DC need to pass through the DUT, such as antenna mounted hardware.
Unpackaged die	Enables user to integrate it directly into hybrids.

# MMIC Power Splitter/Combiner Die

## EP2C-D+

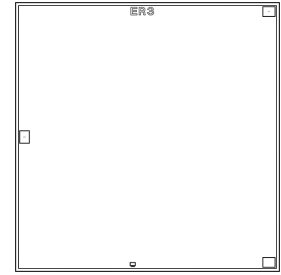
2 Way-0° 50Ω 1800 to 12500 MHz

### Features

- Wide bandwidth, 1800 to 12500 MHz
- Excellent amplitude unbalance, 0.2 dB typ.
- Good phase unbalance, 6 deg. typ.
- High ESD level\*
- DC passing

### Applications

- WiMAX
- ISM
- Instrumentation
- Radar
- WLAN
- Satellite communications
- LTE



**+RoHS Compliant**  
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Ordering Information: Refer to Last Page

### Electrical Specifications<sup>1</sup> at 25°C

Parameter	Frequency (MHz)	Min.	Typ.	Max.	Unit
Frequency Range		1800		12500	MHz
Insertion Loss, above 3.0 dB	1800-3800		0.8		dB
	3800-8500		0.9		
	8500-12500		1.4		
Isolation	1800-3800		12.0		dB
	3800-8500		19.7		
	8500-12500		17.9		
Phase Unbalance	1800-3800		1.5		Degree
	3800-8500		3.2		
	8500-12500		4.9		
Amplitude Unbalance	1800-3800		0.05		dB
	3800-8500		0.09		
	8500-12500		0.23		
VSWR (Port S)	1800-3800		1.4		:1
	3800-8500		1.3		
	8500-12500		1.3		
VSWR (Port 1-2)	1800-3800		1.2		:1
	3800-8500		1.2		
	8500-12500		1.3		

1. Measured on Mini-Circuits Die Characterization Test Board.

### Maximum Ratings<sup>2,3</sup>

Parameter	Ratings
Operating Temperature	-40°C to 85°C
Power Input (as a splitter)	1.85 W max.
Internal Dissipation	0.85 W max.
DC Current	0.4 A max

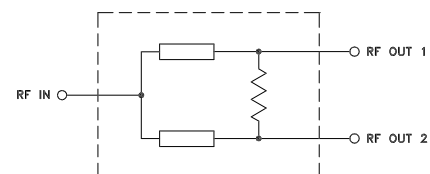
ESD rating<sup>3</sup>  
Human body model (HBM): Class 2(1800 to <4000 V) in accordance with ANSI/ESD 5.1-2007  
Machine model (MM): Class M3 (200 to <400 V) in accordance with ANSI/ESD 5.2-2009

2. Permanent damage may occur if any of these limits are exceeded.  
3. Tested in industry standard 4x4x1mm MCLP package 24-lead

### Pad Connections

Function	Pad Number
SUM PORT	RF IN
RF OUT1	RF OUT1
RF OUT2	RF OUT2
GROUND	Bottom of die

### Simplified Electrical Schematic



Die Layout

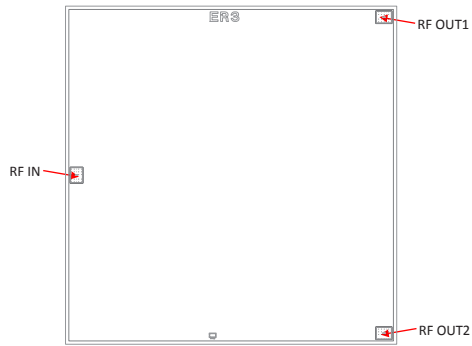


Fig 2. Die Layout

Bonding Pad Position

(Dimensions in  $\mu\text{m}$ , Typical)

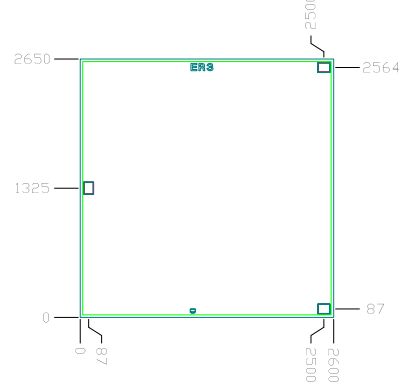


Fig 3. Bonding Pad Positions

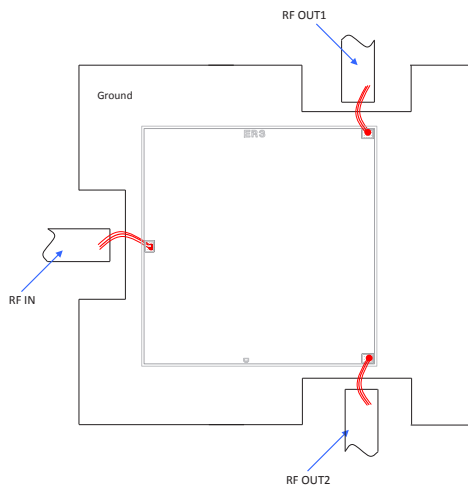
Critical Dimensions

Parameter	Values
Die Thickness, $\mu\text{m}$	200
Die Width, $\mu\text{m}$	2650
Die Length, $\mu\text{m}$	2600
Bond Pad Size, $\mu\text{m}$	100 X 125

Assembly and Handling Procedure

- Storage**  
Dice should be stored in a dry nitrogen purged desiccators or equivalent.
- ESD**  
MMIC amplifier dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.
- Die Attach**  
The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030HK-PT/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition. It is recommended to use antistatic die pick up tools only.
- Wire Bonding**  
Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.

Assembly Diagram



Three 1 mil bond wires should be used for RF input and RF output.

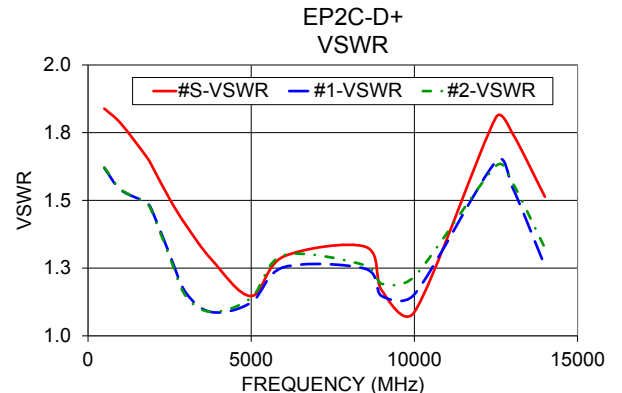
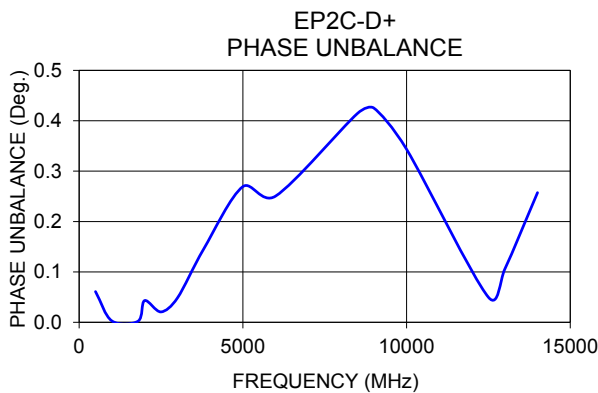
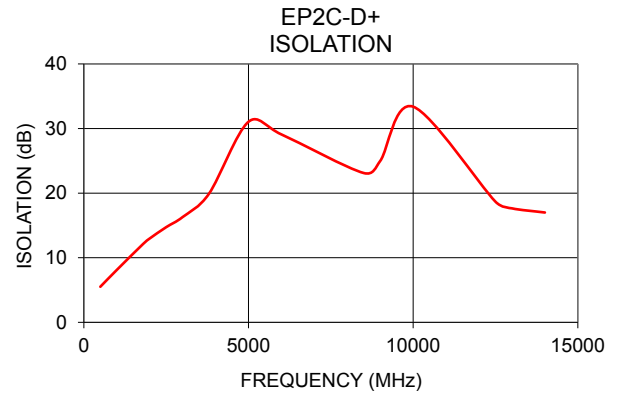
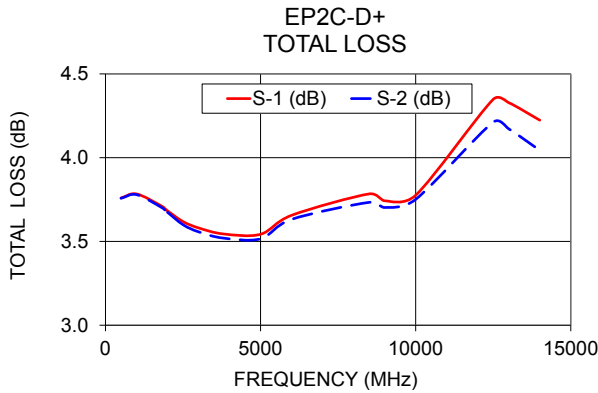
Recommended Wire Length, Typical

Wire	Wire Length (mm)	Wire Loop Height (mm)
RF-IN, RF-OUT1, RF OUT2	0.50	0.15

Typical Performance Data

Frequency (MHz)	Total Loss <sup>1</sup> (dB)		Amplitude Unbalance (dB)	Isolation (dB)	Phase Unbalance (deg.)	VSWR S	VSWR 1	VSWR 2
	S-1	S-2						
500	3.76	3.76	0.01	5.51	0.06	1.84	1.62	1.62
1000	3.78	3.78	0.00	8.11	0.00	1.78	1.54	1.54
1800	3.71	3.70	0.00	12.14	0.00	1.66	1.49	1.49
2000	3.68	3.67	0.01	12.98	0.04	1.62	1.45	1.45
2500	3.62	3.60	0.02	14.73	0.02	1.51	1.30	1.29
3000	3.58	3.56	0.02	16.30	0.05	1.41	1.16	1.15
3800	3.54	3.52	0.03	19.91	0.15	1.28	1.09	1.09
5000	3.54	3.52	0.03	31.05	0.27	1.15	1.12	1.14
6000	3.66	3.63	0.02	29.09	0.25	1.29	1.25	1.30
8500	3.78	3.73	0.04	23.11	0.41	1.33	1.25	1.26
9000	3.74	3.70	0.04	24.99	0.43	1.17	1.15	1.19
10000	3.78	3.75	0.03	33.38	0.34	1.09	1.15	1.22
12500	4.35	4.21	0.13	18.67	0.05	1.81	1.64	1.63
13000	4.33	4.17	0.16	17.64	0.11	1.75	1.55	1.57
14000	4.22	4.04	0.16	17.00	0.26	1.51	1.26	1.32

1. Total Loss = Insertion Loss + 3dB splitter loss.



<b>Additional Detailed Technical Information</b> <i>additional information is available on our dash board.</i>		
<b>Performance Data</b>	Data Table	
	Swept Graphs	
	S-Parameter (S2P Files) Data Set with and without port extension(.zip file)	
<b>Case Style</b>	Die	
<b>Die Ordering and packaging information (Note 3)</b>	Quantity, Package	Model No.
	Small, Gel - Pak: 5,10,50	EP2C-DG+
	Medium†, Partial wafer: 350	EP2C-DP+
	†Available upon request contact sales representative	
	Refer to <a href="#">AN-60-067</a>	
<b>Environmental Ratings</b>	ENV-80	

3. Dice taken from PCM good wafer. No RF or DC test performed.

### Additional Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at [www.minicircuits.com/MCLStore/terms.jsp](http://www.minicircuits.com/MCLStore/terms.jsp)
- D. Mini-Circuits does not warrant the accuracy or completeness of the information, text, graphics and other items contained within this document and same are provided as an accommodation and on an "As is" basis, with all faults.
- E. Purchasers of this part are solely responsible for proper storing, handling, assembly and processing of Known Good Dice (including, without limitation, proper ESD preventative measures, die preparation, die attach, wire bonding and related assembly and test activities), and Mini-Circuits assumes no responsibility therefor or for environmental effects on Known Good Dice.
- F. Mini-Circuits and the Mini-Circuits logo are registered trademarks of Scientific Components Corporation d/b/a Mini-Circuits. All other third-party trademarks are the property of their respective owners. A reference to any third-party trademark does not constitute or imply any endorsement, affiliation, sponsorship, or recommendation by any such third-party of Mini-Circuits or its products.