Engineering Development Model

Power Splitter/Combiner

JPS-ED9624/1

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



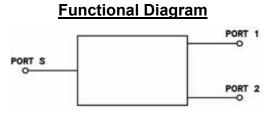
Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE: BH292

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		Min.	Тур.	Max.	Units
Frequency		1		1100	MHz
Isolation	1 - 10 MHz		25		dB
	10 - 550 MHz		24		dB
	550 - 1100 MHz		18		dB
Insertion Loss Above 3.0 dB	1 - 10 MHz		0.63		dB
	10 - 550 MHz		0.54		dB
	550 - 1100 MHz		1.21		dB
Phase Unbalance	1 - 10 MHz		0.041		deg.
	10 - 550 MHz		0.536		deg.
	550 - 1100 MHz		0.636		deg.
Amplitude Unbalance	1 - 10 MHz		0.009		dB
	10 - 550 MHz		0.035		dB
	550 - 1100 MHz		0.308		dB
VSWR	SUM Port		1.20		(:1)
	OUT Ports		1.27		(:1)

MAXIMUM RATINGS			
Operating Temperature	-40°C to 85°C		
Storage Temperature	-55°C to 100°C		

PIN CONNECTIONS			
SUM PORT	1		
PORT 1	3		
PORT 2	4		
GND EXT	6		
NOT USED	2, 5		



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