Engineering Development Model

Voltage Controlled Oscillator JTOS-EDR4083

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

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CASE STYLE : BK377

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	**Min.**	**Typ.**	**Max.**	**Units**
Frequency	95		195	MHz
Tuning Voltage	1		16	V
Power Output		+13		dBm
Phase Noise				
at 1 kHz offset		-86		dBc/Hz
at 10 KHz offset		-106		dBc/Hz
at 100 KHz offset		-126		dBc/Hz
at 1000 kHz offset		-143		dBc/Hz
Pulling at 12 dBr PK-PK all phases		0.2		MHz
Pushing at Vcc=5V±0.25V		0.8		MHz/V
Tuning Sensitivity		5.5 - 11.5		MHz/V
Harmonic Suppression		-9	-6	dBc
3 dB Modulation Bandwidth		100		kHz
Supply Voltage		12		V
Supply Current			16	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+14V
Absolute Tuning Voltage (Vtune)	+18V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	5
GROUND	1,3,4,6,7,8,9,10,11,12,14