

Engineering Development Model

Voltage Controlled Oscillator

JTOS-EDR4128/1

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.

At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : BK377

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	158		226	MHz
Tuning Voltage	0.5		7	V
Power Output		+7.5		dBm
Phase Noise				
at 1 kHz offset		-83		dBc/Hz
at 10 KHz offset		-103		dBc/Hz
at 100 KHz offset		-122		dBc/Hz
at 1000 kHz offset		-141		dBc/Hz
Pulling at 12 dB PK-PK all phases		0.5		MHz
Pushing at Vcc=5V±0.25V		1.3		MHz/V
Tuning Sensitivity		10.5 - 22		MHz/V
Harmonic Suppression		-33	-15	dBc
3 dB Modulation Bandwidth		154		kHz
Supply Voltage		5		V
Supply Current			20	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+7V
Absolute Tuning Voltage (Vtune)	+9V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	5
GROUND	1,3,4,6,7,8,9,10,11,12,14