

Engineering Development Model

Voltage Controlled Oscillator

JTOS-EDR4733

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.

At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE : BK377

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	1400		2690	MHz
Tuning Voltage	1		20	V
Power Output		+4.5		dBm
Phase Noise				
at 1 kHz offset		-66		dBc/Hz
at 10 KHz offset		-93		dBc/Hz
at 100 KHz offset		-114		dBc/Hz
at 1000 kHz offset		-138		dBc/Hz
Pulling at 12 dBm PK-PK all phases		11		MHz
Pushing at Vcc=5V±0.25V		1.6		MHz/V
Tuning Sensitivity		41 - 95		MHz/V
Harmonic Suppression		-28	-8	dBc
3 dB Modulation Bandwidth		15000		kHz
Supply Voltage		10		V
Supply Current			30	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+12V
Absolute Tuning Voltage (Vtune)	+22V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	5
GROUND	1,3,4,6,7,8,9,10,11,12,14