Frequency Synthesizer

KSN-EDR10373/1

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.

At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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ELECTRICAL SPECIFICATIONS 50Ω, over -35°C to +85°C							
Parameter		Min.	Тур.	Max.	Units		
Frequency		2060		2310	MHz		
Step size			50		kHz		
Settling Time Within ±1kHz			25		⊕msec		
Output Power		+2	+6	+10	dBm		
Phase Noise	at 100 Hz offset at 1 kHz offset at 10 KHz offset at 100 KHz offset at 1000 kHz offset	9	-60 -68 -97 -116 -137	-110 -131	dBc/Hz dBc/Hz dBc/Hz dBc/Hz dBc/Hz		
Reference Spurious Suppression		- A	-84	101	dBc		
Comparison Spurious Suppression			117		dBc		
Non-Harm. Spurious Suppression		40	-90		dBc		
Harmonic Suppression		407	-32	-23	dBc		
Supply voltage	VCO PLL		5		V V		
Supply current	VCO PLL		13 13	30 21	mA mA		
` *	Frequency Amplitude Impedance h. N @ 1kHz	7	14.4 1 100 -145		MHz Vp-p kΩ dBc/Hz		
Input Logic Levels	Logic high Logic Low	4		5 1	V		
Digital Lock Detect	Locked Unlocked	4.6		5 0.4	V		
Frequency Synthesizer PLL		ADF4113					

ABSOLUTE MAXIMUM RATINGS					
Operating Temperature	-45°C to 85°C				
Storage Temperature	-55°C to 100°C				
VCO Supply Voltage	6V				
PLL Supply Voltage	6V				
Reference Frequency voltage	5.8Vp-p				
Data, Clock & LE levels	5.3V				

Power On sequence: Vcc VCO followed by Vcc PLL Power Off sequence: Vcc PLL followed by Vcc VCO

PIN CONNECTIONS					
RF OUT	7	CLOCK	10		
VCC VCO	5	DATA	11		
VCC PLL	1	LATCH ENABLE	12		
REF IN	3	GROUND	2,4,6,8,13,14		
LOCK DETECT	9				