Engineering Development Model

Frequency Synthesizer

KSN-EDR10549

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE: DK1171

ELECTRICAL SPECIFICATIONS 50Ω, over -5°C to +75°C						
Parameter	Min.	Тур.	Max.	Units		
Frequency	2304		2304	MHz		
Step size		10240		kHz		
Settling Time Within ±1kHz		35		msec		
Output Power	-4	0	+3	d₿m		
Phase Noise at 100 Hz offs at 1 kHz offs at 10 KHz offs at 100 KHz offs at 1000 kHz offs A	eet eet	-88 -89 -113 -135 -156 -55 -100	-83 -107 -130 -150	dBc/Hz dBc/Hz dBc/Hz dBc/Hz dBc/Hz dBc dBc		
Harmonic Suppression		-33	-25	dBc		
Supply voltage PLL	08	5 3.3		V		
Supply current PLL VCO		43	52 19	V V		
Reference in Amplitude (External) Impedance Ph. N @ 1kHz	or John	10.24 1 100 -145		MHz Vp-p kΩ dBc/Hz		
Digital Lock Detect Locked Unlocked	2.9		3.3 0.4	V		
Frequency Synthesizer PLL Self-programmed (internal microcontroller)						

ABSOLUTE MAXIMUM RATINGS				
Operating Temperature	-45°C to 85°C			
Storage Temperature	-55°C to 100°C			
VCO Supply Voltage	6V			
PLL Supply Voltage	4.3V			
Reference Frequency voltage	3.6Vp-p			
Data, Clock & LE levels	3.6V			

Power On sequence: Vcc VCO followed by Vcc PLL Power Off sequence: Vcc PLL followed by Vcc VCO

PIN CONNECTIONS					
RF OUT	7	N.C	10,11,12		
VCC VCO	5	GROUND	2,4,6,8,13,14		
VCC PLL	1				
REF IN	3				
LOCK DETECT	9				

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