Engineering Development Model

Frequency Synthesizer

KSN-EDR10835S/

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE: DK1042

ELECTRICAL SPECIFICATIONS 50Ω, over -15°C to +75°C						
Parameter	Min.	Тур.	Max.	Units		
Frequency	870		910	MHz		
Step size		10000		kHz		
Settling Time Within ±1kHz		307		μsec		
Output Power	-1	+3	+7	dBm		
Phase Noise						
at 100 Hz	offset	-96		dBc/Hz		
at 1 kHz	offset	-108		dBc/Hz		
at 10 KHz	offset	-108		dBc/Hz		
at 100 KHz	offset	-126		dBc/Hz		
at 1000 kHz	offset	-154		dBc/Hz		
Integrated SSB Phase Noise	A C	-61		dBc		
Ref & Comp Spurious Suppression		-80		dBc		
Non-Harm. Spurious Suppression		-90		dBc		
Harmonic Suppression		-30	-22	dBc		
Supply voltage VCO	96,	5 3.3		V V		
Supply current VCO PLL	X X	42 10	51 18	V V		
Frequency		10		MHz		
Reference In Amplitude	40	1		Vp-p		
(External) Impedance		100		kΩ		
Ph. N @ 1kHz		-145		dBc/Hz		
Input Logic high	1.4		3.3	V		
Levels Logic Low			0.6	V		
Digital Lock Locked	2.9		3.3	V		
Detect Unlocked	N (9)		0.4	V		
Frequency Synthesizer PLL		ADF410)6			

ABSOLUTE MAXIMUM RATINGS					
Operating Temperature	-45°C to 85°C				
Storage Temperature	-55°C to 100°C				
VCO Supply Voltage	6V				
PLL Supply Voltage	4.3V				
Reference Frequency voltage	3.6Vp-p				
Data, Clock & LE levels	3.6V				

Power On sequence: Vcc VCO followed by Vcc PLL Power Off sequence: Vcc PLL followed by Vcc VCO

PIN CONNECTIONS					
RF OUT	3	CLOCK	10		
VCC VCO	5	DATA	11		
VCC PLL	1	LATCH ENABLE	12		
REF IN	7	GROUND	2,4,6,8,13,14		
LOCK DETECT	9				

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