

Engineering Development Model

Frequency Synthesizer

KSN-EDR10900F2

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE : DK1042

ELECTRICAL SPECIFICATIONS 50Ω, over -5°C to +75°C				
Parameter	Min.	Typ.	Max.	Units
Frequency	2085		4080	MHz
Step size		1000		kHz
Settling Time Within ±1kHz		400		μsec
Output Power	+1	+5	+10	dBm
Phase Noise				
at 100 Hz offset		-79		dBc/Hz
at 1 kHz offset		-88		dBc/Hz
at 10 KHz offset		-89		dBc/Hz
at 100 KHz offset		-108		dBc/Hz
at 1000 kHz offset		-134		dBc/Hz
Integrated SSB Phase Noise		-43		dBc
Reference Spurious Suppression		-85		dBc
Comparison Spurious Suppression		-90		dBc
0.5 Step size Spurious Suppression		-107		dBc
Non-Harm. Spurious Suppression		-90		dBc
Harmonic Suppression		-25		dBc
Supply voltage VCO & PLL		3.3		V
Supply current VCO & PLL		105	123	V
Reference In (External)				
Frequency		10		MHz
Amplitude		1		Vp-p
Impedance		100		kΩ
Ph. N @ 1kHz		-145		dBc/Hz
Input Logic Levels	Logic high			
Logic Low			0.6	V
Digital Lock	Locked	2.9		
Detect	Unlocked		3.3	V
0.4				
Frequency Synthesizer PLL	ADF4350			

ABSOLUTE MAXIMUM RATINGS	
Operating Temperature	-45°C to 85°C
Storage Temperature	-55°C to 100°C
Supply Voltage	4.3V
Reference Frequency voltage	3.9Vp-p
Data, Clock & LE levels	3.6V

PIN CONNECTIONS			
RF OUT	7	CLOCK	10
VCC	1,5	DATA	11
REF IN	3	LATCH ENABLE	12
LOCK DETECT	9	GROUND	2,4,6,8,13,14

Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
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