Engineering Development Model

Frequency Synthesizer

KSN-EDR11095

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE: DK1042

ELECTRICAL SPECIFICATIONS 50Ω, over -40°C to +85°C						
Parameter	Min.	Тур.	Max.	Units		
Frequency	600		600	MHz		
Settling Time Within ±1kHz		27		msec		
Output Power	+4	+8	+11	₄dBm		
Phase Noise				(7)		
at 100 Hz offs	et	-94		dBc/Hz		
at 1 kHz offs	et	-111	-105	dBc/Hz		
at 10 KHz offs	et	-114	-108	dBc/Hz		
at 100 KHz offs	et	-120	-115/	dBc/Hz		
at 1000 kHz offs	et	-153	-148	dBc/Hz		
Integrated SSB Phase Noise		-63	4	dBc		
Comparison Spurious Suppression		-84		dBc		
Non-Harm. Spurious Suppression	.4	-90		dBc		
Harmonic Suppression		-33	-28	dBc		
Supply voltage VCO	.0	5 3.3		V V		
Supply current VCO		43	52 18	V		
Frequency		20		MHz		
Reference In Amplitude		1		Vp-p		
(External) Impedance		100		kΩ		
Ph. N @ 1kHz		-145		dBc/Hz		
Digital Lock Detect Unlocked	2.9		3.3 0.4	V		
Frequency Synthesizer PLL Self-programmed (internal microcontroller)						

ABSOLUTE MAXIMUM RATINGS				
Operating Temperature	-45°C to 85°C			
Storage Temperature	-55°C to 100°C			
VCO Supply Voltage	6V			
PLL Supply Voltage	4.3V			
Reference Frequency voltage	3.6Vp-p			
Data, Clock & LE levels	3.6V			

PIN CONNECTIONS					
RF OUT	7	N.C	10,11,12		
ACC ACO	5	GROUND	2,4,6,8,13,14		
VCC PLL	1				
REF IN	3				
LOCK DETECT	9				

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B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.

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