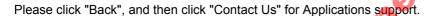
Engineering Development Model

Frequency Synthesizer

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



KSN-EDR11097



CASE STYLE: DK1171

ELECTRICAL SPECIFICATIONS 50Ω, over -40°C to +85°C						
Parameter	Min.	Тур.	Max.	Units		
Frequency	2457.6		2457.6	MHz		
Step size		10240		kHz		
Settling Time Within ±1kHz		40		msec		
Output Power	+1	+4	+8	dBm		
Phase Noise at 100 Hz offs at 1 kHz offs at 10 KHz offs at 100 KHz offs at 100 KHz offs at 1000 kHz offs at	eet eet	-89 -90 -112 -135 -154 -56 -103 -90 -40	-84 -106 -129 -148	dBc/Hz dBc/Hz dBc/Hz dBc/Hz dBc/Hz dBc dBc dBc		
Supply voltage VCO PLL VCO	08	5 3.3	55	V V		
Supply current	4	11	19	V		
Reference In (External) Frequency Amplitude Impedance Ph. N @ 1kHz	JOU	30.72 1 100 -145	10	MHz Vp-p kΩ dBc/Hz		
Digital Lock Locked Detect Unlocked	2.9		3.3	V		
Frequency Synthesizer PLE Self-programmed (internal microcontroller)						

ABSOLUTE MAXIMUM RATINGS				
Operating Temperature	-45°C to 85°C			
Storage Temperature	-55°C to 100°C			
VCO Supply Voltage	6V			
PLL Supply Voltage	4.3V			
Reference Frequency voltage	3.6Vp-p			
Data, Clock & LE levels	3.6V			

PIN CONNECTIONS					
RF OUT	7	CLOCK	10		
VCC VCO	5	DATA	11		
VCC PLL	1	LATCH ENABLE	12		
REF IN	3	GROUND	2,4,6,8,13,14		
LOCK DETECT	9				