Engineering Development Model

Frequency Synthesizer

KSN-EDR11124

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE: DK1042

ELECTRICAL	SPECIFICATIONS	5 50Ω, over -30°C	to +85°C	
Parameter	Min.	Тур.	Max.	Units
Frequency	1025	-	2095	MHz
Step size		100		kHz
Settling Time Within ±1kHz		0.19		msec
Output Power	-16	-13	-9	dBm
Phase Noise				
at 100 Hz offset		-85		dBc/Hz
at 1 kHz offset		-92		dBc/Hz
at 10 KHz offset		-95		dBc/Hz
at 100 KHz offset		-113		dBc/Hz
at 1000 kHz offset		-139		dBc/Hz
Integrated SSB Phase Noise		-48		dBc
Reference Spurious Suppression		-92		dBc
Comparison Spurious Suppression		-105		dBc
Non-Harm. Spurious Suppression		-90		dBc
Harmonic Suppression		-12		dBc
Supply voltage VCO & PLL		3.3		V
Supply current VCO & PLL		100	117	V
Frequency		10		MHz
Reference In Amplitude		1		Vp-p
(External) Impedance		100		kΩ
Ph. N @ 1kHz		-145		dBc/Hz
Input Logic Logic high	1.5			V
Levels Logic Low			0.6	V
Digital Lock Locked	2.9		3.3	V
Detect Unlocked			0.4	V
Frequency Synthesizer PLL		ADF43	50	

ABSOLUTE MAXIMUM RATINGS				
Operating Temperature	-40°C to 85°C			
Storage Temperature	-55°C to 100°C			
Supply Voltage	4.3V			
Reference Frequency voltage	3.9Vp-p			
Data, Clock & LE levels	3.3V			

PIN CONNECTIONS					
RF OUT	7	CLOCK	10		
VCC	1,5	DATA	11		
REF IN	3	LATCH ENABLE	12		
LOCK DETECT	9	GROUND	2,4,6,8,13,14		

A. Performance and quality attributes and conditions not express
B. Electrical specifications and performance data contained in thi
C. The parts covered by this specification document are subject to the rights and benefits contained therein. For a full statement of the