Engineering Development Model

Frequency Synthesizer

KSN-EDR7987

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE: DK801

ELECTRICAL SPECIFICATIONS 50Ω, over -45°C to +85°C						
Parameter	Min.	Тур.	Max.	Units		
Frequency	1681		1742	MHz		
Step size		50		kHz		
Settling Time Within ±1kHz	70	10		msec		
Output Power	+2	+6	+9	₫₿m		
Phase Noise at 1 kHz offse		-71	-65	dBc/Hz		
at 10 KHz offse at 100 KHz offse at 1000 kHz offse	t	-103 -128 -148	-97	dBc/Hz dBc/Hz dBc/Hz		
Integrated SSB Phase Noise		-39	4	dBc		
Reference Spurious Suppression	A C	-101		dBc		
Comparison Spurious Suppression		-122	W (2)	dBc		
Non-Harm. Spurious Suppression		-90		dBc		
Harmonic Suppression		-52		dBc		
Supply voltage VC6	28,	5 3.3		V V		
Supply current VCO PLL		33 10	41 18	mA mA		
Reference In (External) Frequency Amplitude Impedance Ph. N @ 1kHz	40%	10 1 100 -145		MHz Vp-p kΩ dBc/Hz		
Input Logic Logic high Levels Logic Low	2.64		3.3 0.66	V		
Digital Lock Locked Unlocked	2.9		3.3 0.4	V		
Frequency Synthesizer PLL ADF4113						

ABSOLUTE MAXIMUM RATINGS					
Operating Temperature	-45°C to 85°C				
Storage Temperature	-55°C to 100°C				
VCO Supply Voltage	6V				
PLL Supply Voltage	4.3V				
Reference Frequency voltage	5.8Vp-p				
Data, Clock & LE levels	3.6V				

Power On sequence: Vcc VCO followed by Vcc PLL Power Off sequence: Vcc PLL followed by Vcc VCO

PIN CONNECTIONS					
RF OUT	3	CLOCK	10		
VCC VCO	5	DATA	11		
VCC PLL	1	LATCH ENABLE	12		
REF IN	7	GROUND	2,4,6,8,13,14		
LOCK DETECT	9				