## Engineering Development Model

## **Frequency Synthesizer**

## KSN-EDR9370/1

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability.

At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



CASE STYLE: DK1042

Vp-p

kΩ

dBc/Hz

3.3 0.6

3.3

0.4

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Parameter	Min.	Тур.	Max.	Units
Frequency	258.048		258.048	MHz
Step size		6.144		kHz
Settling Time Within ±1kHz		2		msec
Output Power	+2	+5	+9	dBm
Phase Noise at 100 Hz offs at 1 kHz offs		-85 -100	-95	dBc/Hz dBc/Hz
at 10 KHz offs at 100 KHz offs	et	-107 -137	-132	dBc/Hz dBc/Hz
at 1000 kHz offs	et 💮 🥕	-159	-154	dBc/Hz
Integrated SSB Phase Noise		-60		dBc
Reference Spurious Suppression		-89	V	dBc
Comparison Spurious Suppression		-27		dBc
0.5 Step size Spurious Suppression	401	-116		dBc
Non-Harm. Spurious Suppression		-90		dBc
Harmonic Suppression	10,1	-33	-27	dBc
Supply voltage VCO PLL		5.5 3,3		V V
Supply current VCO PLL	10	44 14	52 22	mA mA
Frequency	3	61.44		MHz

3

100

-145

ADF4153

ELECTRICAL SPECIFICATIONS 50Ω, over -45°C to +85°C

ABSOLUTE MAXIMUM RATINGS				
Operating Temperature	-45°C to 85°C			
Storage Temperature	-55°C to 100°C			
VCO Supply Voltage	6.5V			
PLL Supply Voltage	4.3V			
Reference Frequency voltage	3.6Vp-p			
Data, Clock & LE levels	3.6V			

Amplitude

Impedance

Ph. N @ 1kHz

Logic high

Logic Low Locked

Unlocked

Power On sequence: Vcc VCO followed by Vcc PLL Power Off sequence: Vcc PLL followed by Vcc VCO

PIN CONNECTIONS					
RF OUT	7	CLOCK	10		
VCC VCO	5	DATA	11		
VCC PLL	1	LATCH ENABLE	12		
REF IN	3	GROUND	2,4,6,8,13,14		
LOCK DETECT	9				

Reference In

(External)

Input Logic

Levels

**Digital Lock** 

Detect

Frequency Synthesizer PLL

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification assument instructions.

B. Electrical specifications and performance data contained in this specification document are based on Mirrie established test performance printing and assument instructions.

C. The parts covered by this specification document are subject to Mirri-Circuits standard limited warrantly and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled.