MMIC SURFACE MOUNT

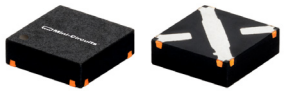
Monolithic Amplifier

LEE-49+

50Ω DC to 5 GHz

FEATURES

- Frequency Range, DC to 5 GHz
- Internally Matched to 50Ω
- Output Power, +15.8 dBm Typ.
- Excellent Package for Heat Dissipation, Exposed Metal Bottom
- Aqueous Washable
- Protected By US Patent 6,943,629



Generic photo used for illustration purposes only

CASE STYLE: FG873

+RoHS Compliant
The +Suffix identifies RoHS Compliance.
See our website for methodologies and qualifications

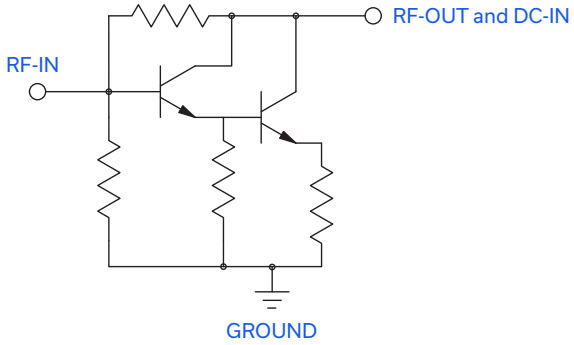
APPLICATIONS

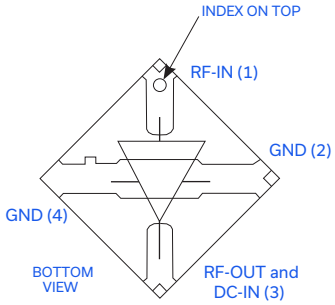
- Cellular
- PCS
- Communication Receivers & Transmitters
- Satellite Communication, Military

PRODUCT OVERVIEW

LEE-49+ (RoHS compliant) is a wideband amplifier offering high dynamic range. It has repeatable performance from lot to lot. It is enclosed in a 3 x 3 mm MCLP molded plastic package.

SIMPLIFIED SCHEMATIC AND PIN DESCRIPTION





Function	Pin Number	Description
RF-IN	1	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
RF-OUT and DC-IN	3	RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit".
GND	2,4	Connections to ground. Use via holes as shown in "Suggested Layout for PCB Design" to reduce ground path inductance for best performance.

REV. N
ECO-027975
LEE-49+
MCL NY
251219

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ELECTRICAL SPECIFICATIONS AT +25°C AND 65 mA UNLESS NOTED OTHERWISE

Parameter	Conditions (GHz)	Min.	Typ.	Max.	Units
Frequency Range[1]		DC		5	GHz
Gain	0.1	12.0	14.0		dB
	1		13.9		
	2		14.3		
	4		14.0		
	5		13.1		
	8		7.8		
Input Return Loss	DC - 3		12.5		dB
	3 - 5		21		
Output Return Loss	DC - 3		15.5		dB
	3 - 5		15.5		
Output Power @ 1 dB Compression	2	+15.8	+16.7		dBm
	5	+9.7	+10.7		
Output IP3			+33		dBm
Noise Figure			5.5		dB
Recommended Device Operating Current			65		mA
Device Operating Voltage		+4.2	+4.9	+5.3	V
Device Voltage Variation vs. Temperature at 65 mA			-2.9		mV/°C
Device Voltage Variation vs. Current at +25°C			10.4		mV/mA
Thermal Resistance, Junction-to-Case[2]			151		°C/W

1. Guaranteed specification DC-5 GHz. Low frequency cut off determined by external coupling capacitors.
2. Case is defined as ground leads.

ABSOLUTE MAXIMUM RATINGS

Parameter	Ratings
Operating Temperature[3]	-45°C to +85°C
Storage Temperature	-65°C to +150°C
Operating Current	85 mA
Input Power	+15 dBm

3. Based on typical case temperature rise +5°C above ambient.

Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation.

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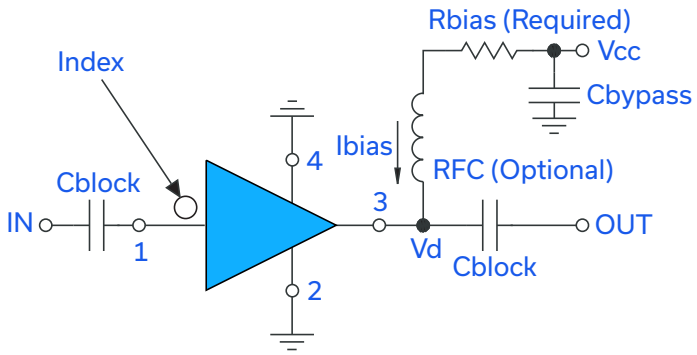
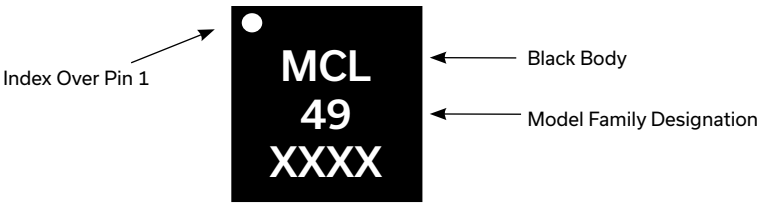
RECOMMENDED APPLICATION CIRCUIT



Test Board includes case, connectors, and components (in bold) soldered to PCB

R BIAS	
Vcc	"1%" Res. Values (Ohms) for Optimum Biasing
7	34.8
8	48.7
9	63.4
10	78.7
11	95.3
12	110
13	127
14	140
15	158
16	174
17	191
18	205
19	221
20	232

PRODUCT MARKING



Markings in addition to model number designation may appear for internal quality control purposes.

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ADDITIONAL DETAILED TECHNICAL INFORMATION IS AVAILABLE ON OUR DASHBOARD. CLICK HERE

Performance Data & Graphs	Data Table Swept Graphs S-Parameter Data Set (.zip file)
Case Style	FG873 Plastic package, exposed paddle, Lead finish: Tin-Silver over Nickel
Tape & Reel **Standard Quantities Available on Reel**	F68 7" Reels with 20, 50, 100, 200, 500 or 1K devices 13" Reels with 2K, 3K, 4K devices
Suggested Layout for PCB Design	PL-252
Evaluation Board	TB-413-49+
Environmental Ratings	ENV08T2

ESD RATING

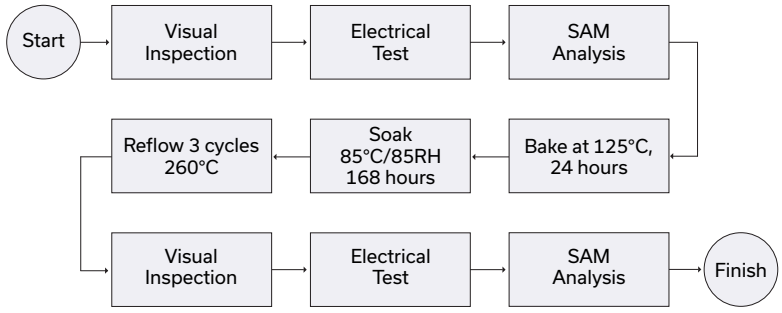
Human Body Model (HBM): Class 1A (250 V to < 500 V) in accordance with ANSI/ESD STM 5.1 - 2001
Machine Model (MM): Class M1 (< 100 V) in accordance with ESD STM 5.2 - 1999

MSL RATING

Moisture Sensitivity: MSL1 in accordance with IPC/JEDECJ-STD-020C

No.	Test Required	Condition	Standard	Quantity
1	Visual Inspection	Low Power Microscope Magnification 40x	MIP-IN-0003 (MCT spec)	45 units
2	Electrical Test	Room Temperature	SCD (MCL spec)	45 units
3	SAM Analysis	Less than 10% growth in term of delamination	J-Std-020C (Jedec Standard)	45 units
4	Moisture Sensitivity Level 1	Bake at 125°C for 24 hours Soak at 85°C/85%RH for 168 hours Reflow 3 cycles at 260°C peak	J-Std-020C (Jedec Standard)	45 units

MSL TEST FLOW CHART



NOTES

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits' standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/terms/viewterm.html