

Engineering Development Model

Voltage Controlled Oscillator

MOS-EDR5665/1

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

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CASE STYLE : CZ682

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	78		82	MHz
Tuning Voltage	0.5		4.5	V
Power Output		0		dBm
Phase Noise				
at 1 kHz offset		-100		dBc/Hz
at 10 KHz offset		-120		dBc/Hz
at 100 KHz offset		-140		dBc/Hz
at 1000 kHz offset		-154		dBc/Hz
Pulling at 12 dBm PK-PK all phases		0.05		MHz
Pushing at Vcc=5V±0.25V		0.2		MHz/V
Tuning Sensitivity		2 - 3		MHz/V
Harmonic Suppression		-21	-15	dBc
3 dB Modulation Bandwidth		2000		kHz
Supply Voltage		5		V
Supply Current			25	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+7V
Absolute Tuning Voltage (Vtune)	+6.5V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	1
GROUND	2,4,6,7,8