

Engineering Development Model

Voltage Controlled Oscillator

MOS-EDR6202

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



CASE STYLE : CZ682

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ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	1725		1840	MHz
Tuning Voltage	0.5		6	V
Power Output		+3		dBm
Phase Noise				
at 1 kHz offset		-74		dBc/Hz
at 10 KHz offset		-101		dBc/Hz
at 100 KHz offset		-123		dBc/Hz
at 1000 kHz offset		-143		dBc/Hz
Pulling at 12 dBr PK-PK all phases		1		MHz
Pushing at Vcc=5V±0.25V		1		MHz/V
Tuning Sensitivity		27 - 40		MHz/V
Harmonic Suppression		-23	-15	dBc
3 dB Modulation Bandwidth		13000		kHz
Supply Voltage		5		V
Supply Current			35	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+7V
Absolute Tuning Voltage (Vtune)	+8V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	1
GROUND	2,4,6,7,8