

Engineering Development Model

Voltage Controlled Oscillator

MOS-EDR6310

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



CASE STYLE : CZ682

Please click "Back", and then click "Contact Us" for Applications support.

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	115		155	MHz
Tuning Voltage	0.5		5	V
Power Output		+2.5		dBm
Phase Noise				
at 1 kHz offset		-82		dBc/Hz
at 10 KHz offset		-104		dBc/Hz
at 100 KHz offset		-124		dBc/Hz
at 1000 kHz offset		-144		dBc/Hz
Pulling at 12 dBr PK-PK all phases		0.5		MHz
Pushing at Vcc=5V±0.25V		0.5		MHz/V
Tuning Sensitivity		9 - 18		MHz/V
Harmonic Suppression		-22.5	-10	dBc
3 dB Modulation Bandwidth		240		kHz
Supply Voltage		5		V
Supply Current			20	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+7V
Absolute Tuning Voltage (Vtune)	+7V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	1
GROUND	2,4,6,7,8