Engineering Development Model

Voltage Controlled Oscillator

MOS-EDR8620/2A

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE: CZ682

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Тур.	Max.	Units
Frequency	585		700	MHz
Tuning Voltage	2		20	V
Power Output		+3		dBm
Phase Noise at 1 kHz offset at 10 KHz offset at 100 KHz offset at 1000 kHz offset		-87 -114 -136 -157	That!	dBc/Hz dBc/Hz dBc/Hz dBc/Hz
Pulling at 12 dBr PK-PK all phases		0.6	.0	MHz
Pushing at Vcc=5V±0.25V	0	0.4		MHz/V
Tuning Sensitivity	0	7 - 10		MHz/V
Harmonic Suppression	2	-15		dBc
Supply Voltage		5		V
Supply Current		33	41	mA

MAXIMUM RATINGS			
Operating Temperature	-55°C to 85°C		
Storage Temperature	-55°C to 100°C		
Absolute Supply Voltage (Vcc)	+7V		
Absolute Tuning Voltage (Vtune)	+22V		
	W W.		

PIN CONNECTIONS		
RF OUT	5	
VCC	3	
V-TUNE	1	
GROUND	2,4,6,7,8	

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