# **Monolithic Amplifier**

PMA-545+

0.05 to 6 GHz  $50\Omega$ 

# **The Big Deal**

- Ultra Low Noise Figure, 0.8 dB
- Ultra High IP3
- Up to 6 GHz



3mm x 3mm MCLP [ EIA: QFN) Pkg

LTE Performance

## **Product Overview**

Mini-Circuits PMA-545+ is a E-PHEMT based Ultra-Low Noise MMIC Amplifier operating from 50 MHz to 6 GHz with a unique combination of low noise and high IP3 making this amplifier ideal for sensitive receiver applications. This design operates on a single 3V supply and is internally matched to 50 Ohms.

Summary Performance at 1 GHz			
Operating Frequency:	0.05 to 6.0	GHz	
Noise Figure	0.8	dB, typ.	
Gain	20	dB, typ.	
IP3	+36	dBm, typ.	
P <sub>out</sub> (at 1dB)	+20	dBm, typ.	
DC Current (at 3V)	80	mA, typ.	

## **Key Features**

Feature		Advantages		
Ultra Low Noise:	0.8 dB NF at 1GHz	Industry Leading Noise Figure, measured in a 50 Ohm environment – without any external matching		
High IP3:	+36 dBm IP3 at 1GHz	Combining Low Noise and High IP3 makes this MMIC amplifier ideal for Low Noise Receiver Front End (RFE) because it gives the user advantages at both ends of the dynamic range: sensitivity & two-tone IM dynamic range		
Output Power:	+20 dBm at 1GHz	The PMA-545+ maintains consistent output power capability over the full operating temperature range making it ideal to be used in remote applications such as LNB's as the L Band driver stage		
Broad Band:	0.05 to 6.0GHz	Broadband covering primary wireless communications bands: Cellular, PCS, LTE, WiMAX		
Internally Matched		No external matching elements required to achieve the advertized noise and output power over the full band		
MCLP Package		Low Inductance, repeatable transitions, excellent thermal pad		
Max Input Power	+20 dBm	Ruggedized design operates up to input powers often seen at Receiver inputs. Can operate up to + 20dBm without the need of an external limiter		
High Reliability		Low, small signal operating current of 80 mA nominal maintains junction temperatures typically below 130°C at 85°C ground lead temperature		

Notes
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# **Monolithic Amplifier**

0.05-6 GHz

#### **Product Features**

- Single Positive Supply Voltage, 3V
- Ultra Low Noise Figure, 0.8 dB typ. at 1GHz
- High IP3, 36 dBm typ. 1GHz
- Gain, 20dB typ. at 1 GHz
- Output Power, up to +20dBm typ.
- Micro-miniature size 3mm x 3mm
- Aqueous washable

## Typical Applications

- Cellular
- ISM
- GSM
- WCDMA
- LTE
- WiMAX
- WLAN
- UNII and HIPERLAN



CASE STYLE: DQ849

#### +RoHS Compliant

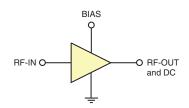
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

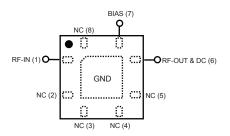
LTE Performance

## **General Description**

PMA-545+ is a high dynamic range, low noise, high IP3, high output power, monolithic amplifier. Manufactured using E-PHEMT\* technology enables it to work with a single positive supply voltage. Unconditionally stable over the operating frequency.

#### simplified schematic and pad description





Function	Pad Number	Description (See Application Circuit, Fig. 2)
RF-IN	1	RF input pad
RF-OUT & DC	6	RF output pad (connected to RF-OUT via blocking external cap C2, and Supply voltage Vs via RF Choke L1)
BIAS	7	Bias pad (connected to Vs via Rbias)
GND	paddle in center of bottom	Connected to ground
NOT USED	2,3,4,5,8	No internal connection; recommended use: per PCB Layout PL-299

<sup>\*</sup>Enhancement mode Pseudomorphic High Electron Mobility Transistor.

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## Electrical Specifications<sup>(1)</sup> at 25°C, Zo=50 $\Omega$ , (refer to characterization circuit)

Parameter	Condition (GHz)	Min.	Тур.	Max.	Units	
Frequency Range		0.05		6.0	GHz	
DC Voltage (V <sub>d</sub> )			3.0		V	
DC Current (I <sub>d</sub> ) <sup>(6)</sup>		65	80	98	mA	
DC Current (I <sub>Rbias</sub> )			5.6		mA	
( Tiblias)	0.05		1.3	_		
	0.5		0.8	_	4D	
	1.0		0.8	_		
Noise Figure	2.0		1.0	1.3		
Noise Figure	3.0		1.2	_	dB	
	4.0		1.5	_		
	5.0		2.0	_		
	6.0		2.4	_		
	0.05	_	26.1	_		
	0.5	_	23.3	-		
	1.0	_	19.4	_		
Gain	2.0	12.7	14.2	15.6	dB	
Gaiii	3.0	_	11.1	-	ub ub	
	4.0	_	8.9	_		
	5.0	_	7.0	-		
	6.0	_	5.5			
Input Return Loss	0.05-0.5		11.0		dB	
	0.5-6		7.0		uБ	
Output Return Loss	0.05		13.3			
	0.1-4		20.0		dB	
	4-6		16.0		ub ub	
	0.05		32.8			
	0.5		35.1			
	1.0		36.3			
Output IP3	2.0		36.4		dBm	
	3.0		38.1		42	
	4.0		40.0			
	5.0		36.0			
	6.0		37.6			
Output Power @ 1 dB compression (2)	0.05	_	19.6			
	0.5	_	19.9		dBm	
	1.0	_	19.3			
	2.0	18.3	20.3			
	3.0	_	20.1			
	4.0	_	20.7			
	5.0	_	20.0			
DC Current Variation va Tamparatura (3)	6.0	_	21.2		m 4 /0C	
DC Current Variation vs. Temperature (3)			-0.121		mA/°C	
Thermal Resistance			128		°C/W	

### **Absolute Maximum Ratings**(4)

Parameter         Ratings           Operating Temperature (5)         -40°C to 85°C           Storage Temperature         -55°C to 100°C           Channel Temperature         150°C           DC Voltage (Pad 6)         5V           Power Dissipation         500mW			
Storage Temperature         -55°C to 100°C           Channel Temperature         150°C           DC Voltage (Pad 6)         5V	Parameter	Ratings	
Channel Temperature         150°C           DC Voltage (Pad 6)         5V	Operating Temperature (5)	-40°C to 85°C	
DC Voltage (Pad 6) 5V	Storage Temperature	-55°C to 100°C	
	Channel Temperature	150°C	
Power Dissipation 500mW	OC Voltage (Pad 6)	5V	
	Power Dissipation	500mW	
DC Current (Pad 6) 160mA	OC Current (Pad 6)	160mA	
Bias Current (Pad 7) 10mA	3ias Current (Pad 7)	10mA	
Input Power <sup>(7)</sup> 20dBm	nput Power <sup>(7)</sup>	20dBm	

- (1) Measured on Mini-Circuits Characterization test board TB-502+.
- See Characterization Test Circuit (Fig. 1) (2) Current increases at P1dB
- (3) (Current at 85°C Current at -45°C)/130
- (4) Permanent damage may occur if any of these limits are exceeded.
- These maximum ratings are not intended for continuous normal operation.
- Defined with reference to ground pad temperature.
   Specified DC current consumption is under small signal conditions. Current will increase with input RF Power. To maintain maximum current
- consumption, external DC current limiting circuits are required on Vd line.

  Maximum input power is specified based external Vd current limiting of 100 mA. Maximum input power will degrade without external current limiting.

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#### **Characterization Test Circuit**

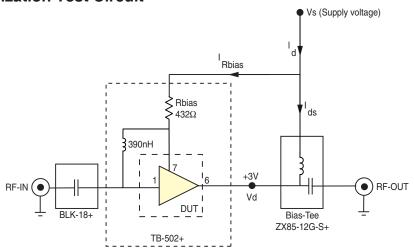


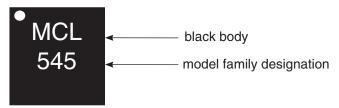
Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization Test Board TB-502+) Gain, Output power at 1dB compression (P1dB), Output IP3 (OIP3) are measured using R&S Network Analyzer ZVA-24. Noise Figure measured using Agilent's N5242A PNA-X microwave network analyzer.

#### Conditions:

- 1. Gain: Pin=-25 dBm
- 2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, 0 dBm/tone at output.
- 3. Vs adjusted for 3V at device (Vd), compensating loss of bias tee.

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#### **Product Marking**



Marking may contain other features or characters for internal lot control

#### Additional Detailed Technical Information

Additional information is available on our web site www.minicircuits.com. To access this information enter the model number on our web site home page.

Performance data, graphs, s-parameter data set (.zip file)

Case Style: DQ849

Plastic package, exposed paddle, lead finish: tin-silver over nickel

Tape & Reel: F104

Standard quantities availabe on reel: 7" reels with 20, 50, 100, 200, 500, 1K, or 2K devices.

Suggested Layout for PCB Design: PL-299

Evaluation Board: TB-501+

**Environmental Ratings: ENV08T1** 

## **Recommended Application Circuit**

(refer to evaluation board for PCB Layout and component values)

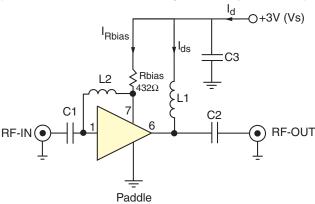


Fig 2. Recommended Application Circuit

Note: Resistance of L1,  $0.1-0.2\Omega$  typically

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### **ESD Rating**

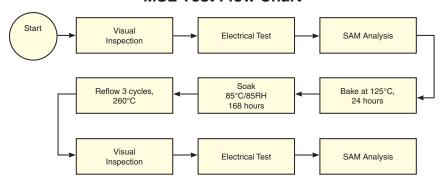
Human Body Model (HBM): Class 1A (250V to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class M1 (<100V) in accordance with ANSI/ESD STM5.2-1999; passes 40V

### **MSL Rating**

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

#### **MSL Test Flow Chart**



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