

Engineering Development Model

Voltage Controlled Oscillator

POS-EDR6570

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.



CASE STYLE : A06

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	470		1020	MHz
Tuning Voltage	1		16	V
Power Output		+8		dBm
Phase Noise				
at 1 kHz offset		-71		dBc/Hz
at 10 KHz offset		-93		dBc/Hz
at 100 KHz offset		-113		dBc/Hz
at 1000 kHz offset		-133		dBc/Hz
Pulling at 12 dBm PK-PK all phases		3.5		MHz
Pushing at Vcc=5V±0.25V		1		MHz/V
Tuning Sensitivity		30 - 51		MHz/V
Harmonic Suppression		-26.5	-15	dBc
3 dB Modulation Bandwidth		115		kHz
Supply Voltage		12		V
Supply Current			20	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+14V
Absolute Tuning Voltage (Vtune)	+18V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	8
GROUND	3,4,5,6,7