



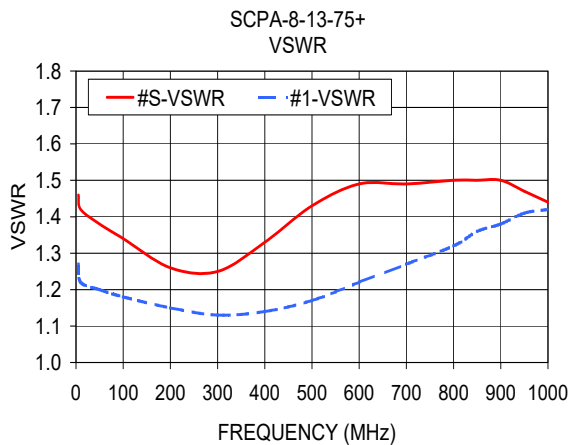
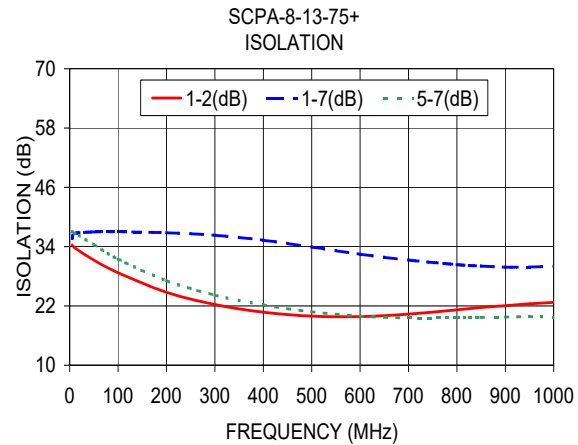
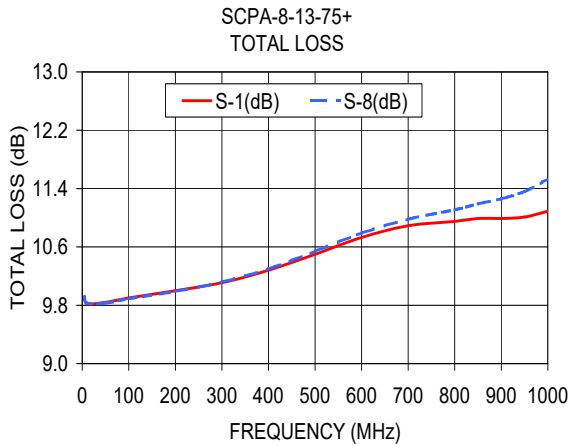
A diagram showing a central rectangular block representing a system. On the left, a single input line labeled "PORT S" enters the block. On the right, multiple output lines exit the block, labeled "PORT 1", "PORT 2", a vertical ellipsis, and "PORT N".

REV. B
ECO-001560
ED-13425/1
SCPA-8-13-75+
WZ/CP/AM
200127

Typical Performance Data

Freq. (MHz)	Total Loss ¹ (dB)						Ampl. Unbl. (dB)	Isolation (dB)				Phase Unbl. (deg.)	VSWR S	VSWR 1	VSWR 8
	S-1	S-2	S-3	S-4	S-6	S-8		1-2	1-7	3-4	5-7				
5.00	9.90	9.90	9.90	9.91	9.92	9.92	0.02	34.37	35.64	34.45	37.06	0.14	1.46	1.27	1.27
10.00	9.82	9.81	9.82	9.82	9.83	9.83	0.02	33.79	36.66	33.56	36.77	0.12	1.42	1.22	1.22
50.00	9.84	9.84	9.84	9.84	9.84	9.83	0.01	31.27	36.97	31.29	34.40	0.35	1.38	1.20	1.20
100.00	9.90	9.89	9.90	9.89	9.89	9.89	0.01	28.71	37.01	28.71	31.47	0.79	1.34	1.18	1.18
200.00	10.00	9.98	9.98	9.95	9.99	9.99	0.04	24.77	36.81	24.71	27.11	1.62	1.26	1.15	1.14
300.00	10.11	10.08	10.08	10.03	10.11	10.12	0.09	22.28	36.30	22.15	24.19	2.32	1.25	1.13	1.11
400.00	10.28	10.24	10.23	10.14	10.28	10.30	0.17	20.74	35.30	20.52	22.21	2.94	1.33	1.14	1.11
500.00	10.50	10.44	10.42	10.31	10.48	10.54	0.23	19.94	33.93	19.72	20.82	3.45	1.43	1.17	1.15
600.00	10.73	10.64	10.58	10.49	10.68	10.79	0.30	19.83	32.47	19.67	19.94	3.92	1.49	1.22	1.21
700.00	10.89	10.77	10.72	10.65	10.88	10.98	0.32	20.34	31.29	20.52	19.57	4.40	1.49	1.27	1.28
800.00	10.95	10.83	10.77	10.77	11.04	11.11	0.34	21.22	30.36	22.23	19.60	4.86	1.50	1.32	1.35
850.00	10.99	10.87	10.76	10.83	11.08	11.19	0.43	21.69	30.07	23.37	19.68	5.23	1.50	1.36	1.38
900.00	10.99	10.89	10.77	10.87	11.15	11.26	0.50	22.06	29.84	24.47	19.75	5.61	1.50	1.38	1.41
950.00	11.01	10.92	10.79	10.92	11.25	11.36	0.56	22.44	29.87	25.39	19.85	6.16	1.47	1.41	1.44
1000.00	11.09	11.01	10.81	11.01	11.36	11.53	0.72	22.72	30.03	26.02	19.73	6.85	1.44	1.42	1.47

1. Total Loss = Insertion Loss + 9dB splitter theoretical loss.



Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
 B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
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