## Engineering Development Model

## Power Splitter/Combiner SYPS-ED12877/2

2 Way-0°

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

**CASE STYLE: AH202-2** 

ELECTRICAL SPECIFICATIONS 75Ω @ +25°C						
Parameter		Min.	Тур.	Max.	Units	
Frequency		1		3000	MHz	
Isolation	1 - 10 MHz		21		dB	
	10 - 1500 MHz		23		dB	
	1500 - 3000 MHz		19		dB	
Insertion Loss Above 3.0 dB	1 - 10 MHz		0.90		dB	
	10 - 1500 MHz		1.20		dB	
	1500 - 3000 MHz		1.80		dB	
Phase Unbalance	1 - 10 MHz		0.02		deg.	
	10 - 1500 MHz		0.17		deg.	
	1500 - 3000 MHz		1.04		deg.	
Amplitude Unbalance	1 - 10 MHz		0.01		dB	
	10 - 1500 MHz		0.04		dB	
	1500 - 3000 MHz		0.09		dB	
VSWR	SUM Port		1.60		(:1)	
	<b>OUT Ports</b>		1.65		(:1)	

MAXIMU	<u>Functiona</u>	<u>Functional Diagram</u>		
Operating Temperature	-40°C to 85°C		PORT	
Storage Temperature	-55°C to 100°C	PORT S	-	
		0	PORT 2	