Engineering Development Model

Frequency Synthesizer

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

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TSN-EDR11165



CASE STYLE: 99-01-1895

ELECTRICAL SPECIFICATIONS 50Ω, over -45°C to +85°C						
Parameter	Min.	Тур.	Max.	Units		
Frequency	374		374	MHz		
Step size		2000		kHz		
Settling Time Within ±1kHz		1.5		msec		
Output Power	+4	+7	+11	d Bm		
Phase Noise		- 60		40		
at 100 Hz offse		-101		dBc/Hz		
at 1 kHz offse		-106	-100	dBc/Hz		
at 10 KHz offse		117	-111	dBc/Hz		
at 100 KHz offse	t	-142	-136	dBc/Hz		
at 1000 kHz offse	t	-161	-156	dBc/Hz		
Integrated SSB Phase Noise		-67		dBc		
Reference Spurious Suppression		-98		dBc		
Comparison Spurious Suppression		-36		dBc		
Non-Harm. Spurious Suppression		-90		dBc		
Harmonic Suppression	A 5	-28	-21	dBc		
Supply voltage VCO	2	5 5		V V		
Supply current VCO		43	51	mA		
PLL PLL		1 0	18	mA		
Frequency	40	10		MHz		
Reference In Amplitude		1		Vp-p		
(External) Impedance		100		kΩ		
Ph. N @ 1kHz	4	-145		dBc/Hz		
Digital Lock Locked	4.6		5	V		
Detect Unlocked		0-16	0.4			
Frequency Synthesizer PLL Self-programmed (internal microcontroller)						

ABSOLUTE MAXIMUM RATINGS				
Operating Temperature	-45°C to 85°C			
Storage Temperature	-55°C to 100°C			
VCO Supply Voltage	6V			
PLL Supply Voltage	6V			
Reference Frequency voltage	5.8Vp-p			

Power On sequence: Vcc VCO followed by Vcc PLL Power Off sequence: Vcc PLL followed by Vcc VCO

PIN CONNECTIONS					
RF OUT	11	REF IN	1		
VCC VCO	9	LOCK DETECT	16		
VCC PLL	15	GROUND	2-8,10,12-14		