# Solid state USB / I<sup>2</sup>C RF SP4T Switch

# U2C-1SP4T-63H

50Ω 2 to 6000 MHz

### The Big Deal

- USB and I<sup>2</sup>C power & control
- High speed switch transition, 200 ns
- High power handling (+30 dBm)
- Very High Isolation (80 dB)
- Small case (3.75" x 2.50" x 0.6")

### **Typical Applications**

- High volume production testing / ATE
- Design verification testing
- RF signal routing / switch matrices



methodologies and qualifications

### **Product Overview**

Mini-Circuits' U2C-1SP4T-63H is a low cost, absorptive SP4T switch with USB and I<sup>2</sup>C control. The fast switching, solid state switch operates from 2 MHz to 6000 MHz with 200 ns typical switch transition speed. High linearity (+55 dBm typ IP3), and high isolation (80 dB typical) allow the model to be used for a wide variety of RF applications.

Full software support is provided for USB control, including our user-friendly GUI application for Windows and a full API with programming instructions for USB and I<sup>2</sup>C control in both Windows and Linux environments (32-bit and 64-bit systems). The latest version of the full software package can be downloaded from <u>https://www.minicircuits.com/softwaredownload/</u>solidstate.html at any time.

The U2C-1SP4T-63H is housed in a compact, rugged metal case (3.75" x 2.50" x 0.6") with 5 SMA (F) connectors (COM, and J1 to J4), a USB Mini-B and a 9-pin D-sub connector for USB and  $I^2C$  control and power.

### **Key Features**

Feature	Advantages
Dual control interface (I <sup>2</sup> C & USB)	USB provides a quick and easy PC control method with full software support, while I2C allows multiple switches to be controlled in parallel from a microcontroller / embedded system using minimal hardware (2 wire control)
RF SP4T absorptive switch	Wideband (2 to 6000 MHz) with low insertion loss (3.7 dB typ.), high isolation (80 dB typ.), and high power rating (+30 dBm through path).
High Linearity (IP3 +55 dBm typ.)	Results in little or negligible inter-modulation generation, meeting requirements for digital communications signals
DC Blocking at RF ports	Built in blocking capacitors eliminate the need for external DC blocking circuitry at RF ports.

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#### Electrical Specifications @ 0 to 50°C

Parameter	Port	Conditions	Min.	Тур.	Max.	Units
Operating Frequency			2		6000	MHz
		2 to 700 MHz	_	2.3	3.5	
Incontion Lago		700 to 2500 MHz	_	3.0	4.5	dB
nsertion Loss	COM to any active port	2500 to 5000 MHz	_	3.8	5.5	aв
		5000 to 6000 MHz	_	4.2	6.0	
		2 to 700 MHz	82	105	-	
	Between any of ports J1 to J4	700 to 2500 MHz	72	90	-	
	Between any of ports 51 to 54	2500 to 5000 MHz	53	73	-	
		5000 to 6000 MHz	49	62	-	
		2 to 700 MHz	82	105	-	
	COM to any terminated part	700 to 2500 MHz	77	95	-	
	COM to any terminated port	2500 to 5000 MHz	58	76	-	
adation		5000 to 6000 MHz	54	64	-	dP
solation		2 to 700 MHz	82	105	-	dB
	Disconnected state	700 to 2500 MHz	77	95	-	
	(COM port open) <sup>1</sup> : COM to J2, J3, or J4	2500 to 5000 MHz	58	76	-	
	,,	5000 to 6000 MHz	54	64	-	
		2 to 700 MHz	42	60	-	
	Disconnected state (COM port open) <sup>1</sup> : COM to J1	700 to 2500 MHz	30	44	-	
		2500 to 5000 MHz	25	34	-	
		5000 to 6000 MHz	25	32	-	
	COM port in all active states 1	2 to 700 MHz	-	1.40	-	
		700 to 2500 MHz	_	1.25	-	
		2500 to 5000 MHz	-	1.25	-	
		5000 to 6000 MHz	-	1.30	-	
	Any port connected to COM	2 to 700 MHz	-	1.40	-	
/SWR		700 to 2500 MHz	-	1.25	-	
12MR		2500 to 5000 MHz	_	1.25	-	:1
		5000 to 6000 MHz	_	1.30	-	
		2 to 700 MHz	-	1.15	-	
		700 to 2500 MHz	_	1.15	-	
	Any terminated port	2500 to 5000 MHz	-	1.20	-	
		5000 to 6000 MHz	-	1.35	-	
Power Input @1 dB Compression <sup>2,3,4</sup>	COM to any active port	100 to 6000 MHz	-	+35	-	dBm
P3 <sup>2,4,5</sup>	COM to any active port	100 to 6000 MHz	-	+55	-	dBm
Fransition Time <sup>6</sup>	-	-	-	200	300	ns
Vinimum dwell time <sup>7</sup>	High Speed Mode	-	-	5	-	μs
Switching Time (USB) <sup>8</sup>	-	-	-	2	-	ms
Supply voltage (Vcc)	USB or D-Sub port	-	4.75	5	5.25	V <sub>DC</sub>
Supply Current (Icc)		-	-	30	50	mA
	COM to any active port	Hot & Cold Switching	-	-	+30	
	Any active port to COM	Hot & Cold Switching	-	-	+23	
Operating RF Input	Any terminated port	-	-	-	+23	dBm
Power <sup>3</sup>	Through path	2 to 50 MHz		Max power at through path derates linearly from +30 dBm @ 50 MHz to +18 dBm @2 MHz		авт
		50 to 6000 MHz	_		+30	

<sup>1</sup> When switch is at DISCONNECT state (see page 9) COM port will be in OPEN (reflective) state and all output ports (J1-J4) will be absorptive, however J1 port isolation is affected under this configuration, while J2, J3, J4 still maintain the same isolation as in the normal operating conditions.

<sup>2</sup> Do not exceed absolute maximum ratings in table on page 3

<sup>3</sup> Max power at through path derates linearly from +30 dBm @ 50 MHz to +18 dBm @2 MHz <sup>4</sup> Compression and IP3 may degrade below 100 MHz

<sup>5</sup> IP3 tested with 1 MHz span between signals, +5 dBm per tone.

<sup>6</sup> Transition time spec represents the time that the RF signal paths are interrupted during switching and thus is specified without communication delays.

<sup>7</sup> Minimum dwell time is the shortest time that can be achieved between 2 switch transitions when programming an automated switch sequence.
<sup>8</sup> Switching time(USB) is the time from issuing a single software command via USB to the switch state changing. The most significant factor is the host PC, influenced by CPU load and USB protocol. The time shown is an estimate for a medium CPU load and USB 2.0 connection.



## U2C-1SP4T-63H

#### Control voltages for I<sup>2</sup>C Control

Parameter		Min.	Тур.	Max.	Units
Control	Low	0	—	0.8	V
Input	High	2.0	—	3.3	v

### Connections

RF Switch (J1 to J4, COM)	(SMA female)
USB	(USB type Mini-B receptacle)
l <sup>2</sup> C*	(9 pin D-sub female)

#### \*9 Pin D-Sub Pin Connections

PIN Number	Function
1	SDA
2	SCL
3	GND
4	Vcc
5	GND
6	A0
7	A1
8	A2
9	GND

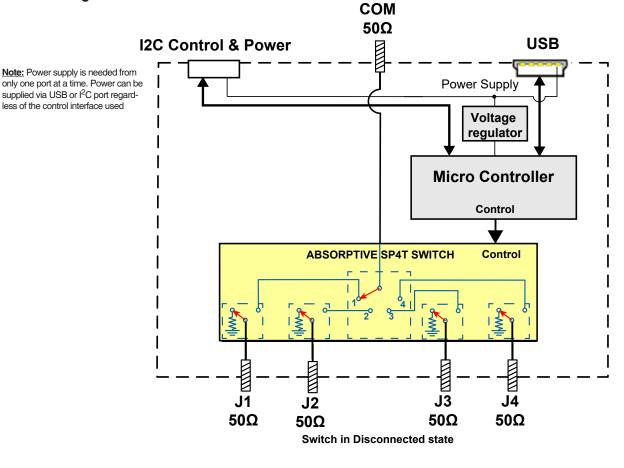
### **Absolute Maximum Ratings**

Operating Temperature	0°C to 50°C
Storage Temperature	-20°C to 60°C
DC supply voltage max. @ USB and pin 4 of D-sub	6V
Voltage on I <sup>2</sup> C control pins (1 & 2 in D-sub)	3.6V
Voltage on address pins (6 - 8 in D-sub)	3.6V
RF power @ 2 - 6000 MHz into termination	+24 dBm
RF power @ 2 - 50 MHz into COM or active port	Derate linearly from +35 dBm @ 50 MHz to +20 dBm @2 MHz
RF power @ 50 - 6000 MHz into COM or active port	+35 dBm
DC voltage @ RF Ports	16V

Permanent damage may occur if any of these limits are exceeded. Operating in the range

between operating power limits and absolute maximum ratings for extended periods of time may result in reduced life and reliability.

#### **Block Diagram**



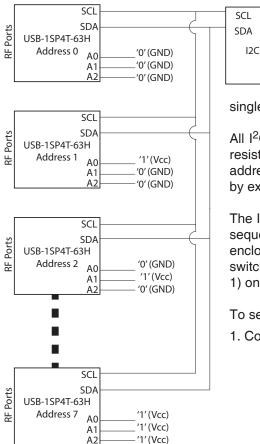
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# U2C-1SP4T-63H

#### I<sup>2</sup>C communication parameters

Parameter	Conditions		Min.	Тур.	Max.	Units
Voltage levels	Logic Low Voltage	Input	0	-	0.8	V
	Logic High Voltage	Input	2.0	-	3.3	
Clock Frequency	_		-	_	400	kHz



GCL DA I2C Master The I<sup>2</sup>C is a short-range synchronous communication protocol for simple 2-wire communication with slave devices using clock (SCL) and data (SDA) connections. The U2C-1SP4T-63VH interface also includes 3 address pins (A0, A1 and A2), allowing up to 8 switches to be controlled independently from a

single master with shared SDA and SCL connections.

All I<sup>2</sup>C pins in the U2C-1SP4T-63VH are connected to an internal pullup resistor so will float to logic 1 when disconnected. This sets a default address of 111 for all units (decimal 7). Addresses from 0 to 7 can be set by externally grounding the relevant address pins (A0, A1 and A2).

The I<sup>2</sup>C functionality is limited to setting or reading switch states. Control sequences are sent to the switch in several bytes on the data connection, enclosed by a start and stop signal, and clocked at up to 400 kHz. The switch will acknowledge each byte received with a single "ACK" bit (logic 1) on the same data connection.

To send a command to the switch 3 bytes will be sent:

1. Control byte (1010A<sub>2</sub>A<sub>1</sub>A<sub>0</sub>R/W)

Where: 1010 = Control code for U2C-1SP4T-63VH  $A_2A_1A_0 = 3\text{-bit address for the U2C-1SP4T-63VH switch module}$  R/W = Read / write select bit ('0' to write or '1' to read)Example: Control byte = 10101000 Address = 100 (binary) = 4 (decimal) R/W = 0 (write to switch)

2. Switch selector byte (00000001) - U2C-1SP4T-63VH contains only 1 switch so this byte is always 00000001.

3. Switch state byte (00000XYZ) - The switch state, represented by a binary string according to the truth table below.

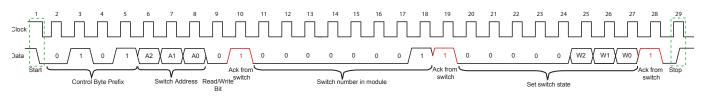
Switch	Switch		Port behavior				
state	state byte	СОМ	J1	J2	J3	J4	
Disconnected	0000 0000	Reflective	Terminated Internally (50 $\Omega$ )				
COM -> 1	0000 0001	Connected to J1	Connected to COM	Terminated Internally (50 $\Omega$ )	Terminated Internally (50 $\Omega$ )	Terminated Internally (50 $\Omega$ )	
COM -> 2	0000 0010	Connected to J2	Terminated Internally (50 $\Omega$ )	Connected to COM	Terminated Internally (50 $\Omega$ )	Terminated Internally (50 $\Omega$ )	
COM -> 3	0000 0011	Connected to J3	Terminated Internally (50 $\Omega$ )	Terminated Internally (50 $\Omega$ )	Connected to COM	Terminated Internally (50 $\Omega$ )	
COM -> 4	0000 0100	Connected to J4	Terminated Internally (50 $\Omega$ )	Terminated Internally (50 $\Omega$ )	Terminated Internally (50 $\Omega$ )	Connected to COM	

In I<sup>2</sup>C protocol the Data line may not change states while the Clock is high, except for the start and stop signals which enclose each sequence of bytes. While the Clock is high a falling edge (transition from logic 1 to logic 0) signifies the start of a sequence, while a rising edge (transition from logic 0 to logic 1) signifies a stop signal. All other transitions must occur while the clock is low.

#### Setting switch state via I<sup>2</sup>C

The I2C communication sequence to set the switch state is:

- 1. Start signal
- 2. Send control byte (write mode)
- 3. Receive ACK response from switch
- 4. Send switch selector byte
- 5. Receive ACK response from switch
- 6. Send switch state byte
- 7. Receive ACK response from switch
- 8. Stop signal



#### Legend:

2.

Signal from master to switch \_\_\_\_\_\_ Signal from switch to master \_\_\_\_\_\_

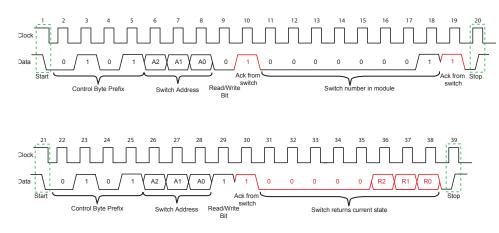
#### Reading switch state via I<sup>2</sup>C

The I2C communication sequence to set the switch state is:

1. Start signal

- 7. Start signal
- Send control byte (write mode)
- 3. Receive ACK response from switch
- 4. Send switch selector byte
- 5. Receive ACK response from switch
- 6. Stop signal

- 7. Start signal
- 8. Send control byte (read mode)
- Receive ACK response from switch
   Receive current switch state
- 11. Stop signal

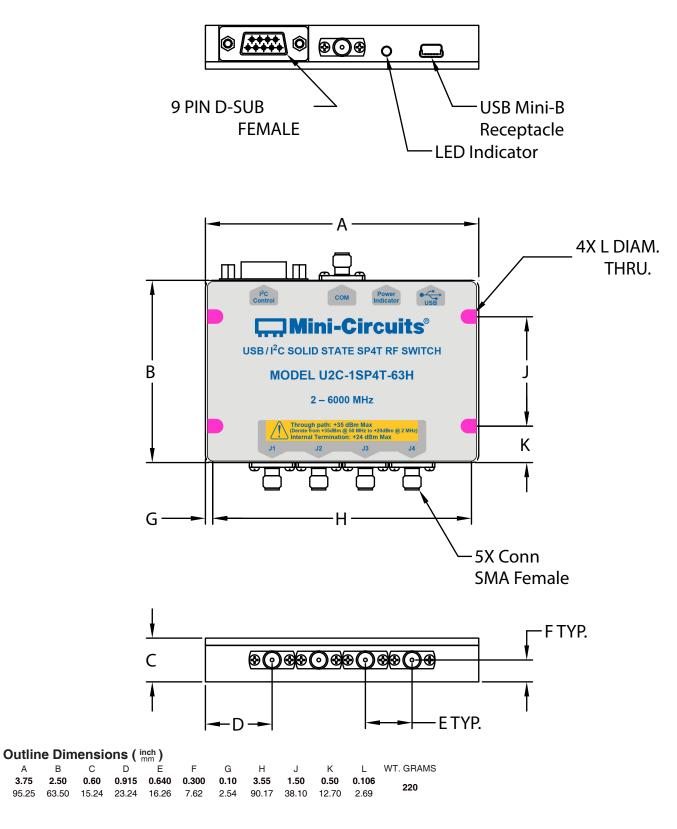


#### Legend:

### **Outline Drawing (RB2502)**

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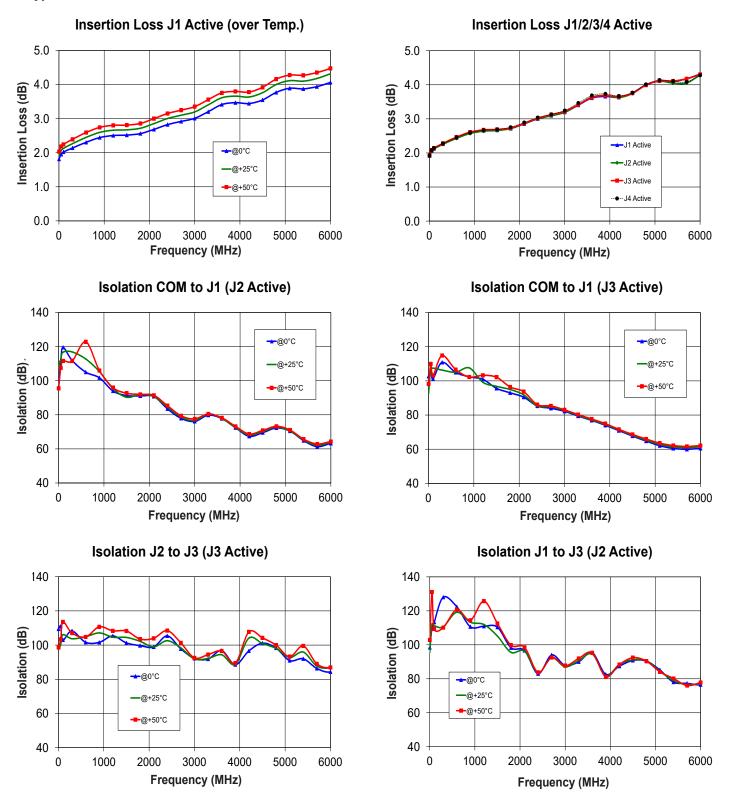
3.75



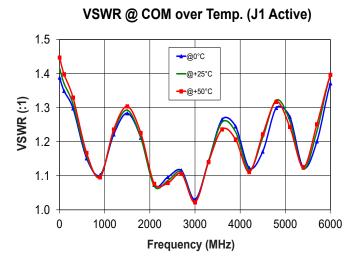
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## U2C-1SP4T-63H

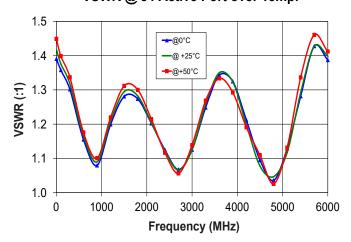
#### **Typical Performance Curves**



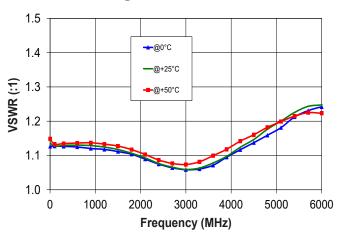
### **Typical Performance Curves (Continued)**

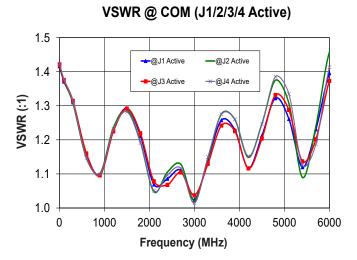


### VSWR @ J1 Active Port over Temp.

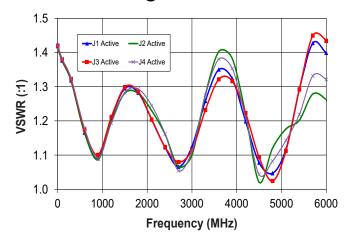




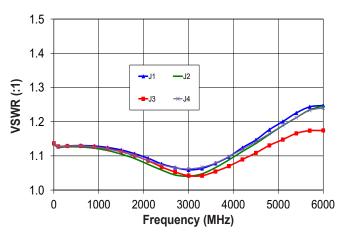




VSWR @ Active Ports J1/2/3/4



VSWR J1/2/3/4 Terminated Ports





#### Software & Documentation Download:

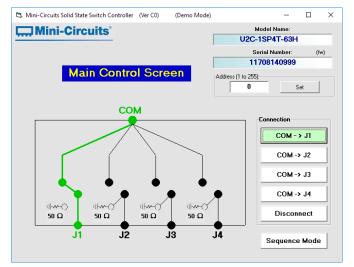
- Mini-Circuits' full software and support package including user guide, Windows GUI, DLL files, programming manual and examples can be downloaded free of charge from
- https://www.minicircuits.com/softwaredownload/solidstate.html
- Please contact testsolutions@minicircuits.com for support

#### **Minimum System Requirements**

Parameter	Requirements		
Interface	USB HID or I <sup>2</sup> C		
	GUI: Windows 32 & 64 bit systems from Windows 98 up to Windows 10		
System requirements	USB API (ActiveX & .Net) Windows 32 & 64 bit systems with ActiveX or .Net support from Windows 98 up to Window		
	l <sup>2</sup> C	Any computer with a suitable I/O port	
	USB direct programming support	Linux, Windows systems from Windows 98 up to Windows 10	
Hardware	Pentium <sup>®</sup> II or higher, RAM 256 MB		

# Graphical User Interface (GUI) for Windows Key Features:

- Set switch manually
- · Set timed sequence of switching states
- Configure switch address and upgrade Firmware



#### Application Programming Interface (API) Windows Support:

- · API DLL files exposing the full switch functionality
  - · ActiveX COM DLL file for creation of 32-bit programs
  - .Net library DLL file for creation of 32 / 64-bit programs
- Supported by most common programming environments (refer to application note <u>AN-49-001</u> for summary of tested environments)

#### Linux Support:

• Full switch control in a Linux environment is achieved by way of USB interrupt commands.

#### **Recommended Accessories**

U2C-1SP4T-63H includes a standard 9 pin D-Sub connector for I2C control applications. An optional shielded control cable is available for implementing custom cable harnesses. D-SUB9-MPT-3+ has a D-Sub connector for mating to U2C-1SP4T-63H and bare wires (pig tail) on the other end. The cable is 36 inches (0.9 meter) long using 28 AWG wires

### Control Cable D-SUB9-MPT-3+



Pin Number	Function	Description	Pigtail Wire Color
1	SDA	I <sup>2</sup> C Data	BLACK
2	SCL	I <sup>2</sup> C Clock	BROWN
3	GND	Ground connection	RED
4	VCC	Supply Voltage	ORANGE
5	GND	Ground connection	YELLOW
6	A0	Address bit 0 (LSB)	GREEN
7	A1	Address bit 1	BLUE
8	A2	Address bit 2 (MSB)	PURPLE
9	GND	Ground connection	WHITE

### U2C-1SP4T-63H

#### Ordering, Pricing & Availability Information see our web site

Model	Description
U2C-1SP4T-63H	USB & I <sup>2</sup> C Single Pole Four Throw Switch

Included Accessories	Part No.	Description
	MUSB-CBL-3+	2.6 ft (0.8 m) USB Cable: USB type A(Male) to USB type Mini-B(Male)

<b>Optional Accessories</b>	Description
MUSB-CBL-3+ (Spare)	2.6 ft (0.8 m) USB Cable: USB type A(Male) to USB type Mini-B(Male)
MUSB-CBL-7+	6.6 ft (2.0 m) USB Cable: USB type A(Male) to USB type Mini-B(Male)
D-SUB9-MPT-3+	3 ft I2C Cable: 9 pin D-sub(Male) to Pig-Tail (Bare wires)
USB-AC/DC-5+	AC/DC +5V power adaptor with USB connector 9,10

<sup>9</sup> The USB-AC/DC-5 may be used to provide additional power if needing to connect a number of switches in series exceeding 500mA total current draw.
<sup>10</sup> Includes power plugs for US, UK, EU, IL, AU & China. Plugs for other countries are also available, if you need a power plug for a country not listed please contact testsolutions@minicircuits.com

#### **Additional Notes**

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
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