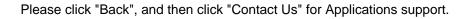
## Engineering Development Model

## **Voltage Controlled Oscillator**

## **UOS-EDR5265/2**

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.





**CASE STYLE: CZ682** 

ELECTRICAL SPECIFICATIONS 50Ω					
Parameter	Min.	Тур.	Max.	Units	
Frequency	1768		1834	MHz	
Tuning Voltage	0.5		5	V	
Power Output		+2		dBm	
Phase Noise					
at 1 kHz offset		-75		dBc/Hz	
at 10 KHz offset		-101		dBc/Hz	
at 100 KHz offset		-122		dBc/Hz	
at 1000 kHz offset		-142		dBc/Hz	
Pulling at 12 dBr PK-PK all phases		6.5		MHz	
Pushing at Vcc=5V±0.25V		0.85		MHz/V	
Tuning Sensitivity		21 - 38.5		MHz/V	
Harmonic Suppression		-24	-14	dBc	
3 dB Modulation Bandwidth		10000		kHz	
Supply Voltage		5		V	
Supply Current			25	mA	

MAXIMUM RATINGS			
Operating Temperature	-55°C to 85°C		
Storage Temperature	-55°C to 100°C		
Absolute Supply Voltage (Vcc)	+7V		
Absolute Tuning Voltage (Vtune)	+7V		

PIN CONNECTIONS		
RF OUT	10	
VCC	14	
V-TUNE	1	
GROUND	2,4,6,7,8	