

Engineering Development Model

Voltage Controlled Oscillator

UOS-EDR5674

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : CZ682

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	732		794	MHz
Tuning Voltage	0.5		6.5	V
Power Output		+1		dBm
Phase Noise				
at 1 kHz offset		-88		dBc/Hz
at 10 KHz offset		-114		dBc/Hz
at 100 KHz offset		-133		dBc/Hz
at 1000 kHz offset		-147		dBc/Hz
Pulling at 12 dBm PK-PK all phases		0.5		MHz
Pushing at Vcc=5V±0.25V		1.5		MHz/V
Tuning Sensitivity		11 - 20		MHz/V
Harmonic Suppression		-22.5	-13	dBc
3 dB Modulation Bandwidth		11500		kHz
Supply Voltage		5.7		V
Supply Current			25	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+7.7V
Absolute Tuning Voltage (Vtune)	+8.5V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	1
GROUND	2,4,6,7,8